

Electron emission properties of relaxation-induced traps in InAs/GaAs quantum dots and the effect of electronic band structure

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[Electron emission properties of relaxation-induced traps in InAs/GaAs](http://dx.doi.org/10.1063/1.2770817) [quantum dots and the effect of electronic band structure](http://dx.doi.org/10.1063/1.2770817)

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The electron-emission properties of relaxation-induced traps in InAs/GaAs quantum dots (QDs) are studied in detail using capacitance-voltage (C-V) profiling and bias-dependent deep-level transient spectroscopy. Strain relaxation is shown to induce a threading-dislocation-related trap in the top GaAs layer and a misfit-dislocation-related trap near the QD. The threading trap decreases its electron-emission energy from 0.63 to 0.36 eV from sample surface toward the QD, whereas the misfit trap gradually increases its electron-emission energy from 0.28 to 0.42 eV from near the QD toward the GaAs bottom layer, indicating that both traps near the QD have lower electron-emission energies. Hence, the emission-energy change is attributed to the related traps across the QD interface where a band offset exists. The *C*-*V* profiling at 300 K shows extended carrier depletion near the QD. As temperature is increased, an electron-emission peak emerges at the QD followed by a prominent peak, suggesting that the trap responsible for the prominent peak lies in energy below the QD electron ground state. From a simulation, this trap is identified to be the misfit trap located at the QD and at the observed emission energy below the GaAs conduction band. Based on the energy location of this trap, we deduce a possible mode of strain relaxation. © *2007 American Institute of Physics.* [DOI: [10.1063/1.2770817](http://dx.doi.org/10.1063/1.2770817)]

I. INTRODUCTION

InAs/GaAs self-assembled quantum dots $1-9$ (QDs) have recently attracted considerable attention for both theoretical and experimental studies due to promising technological applications.^{10[–14](#page-5-3)} Many works have focused on experimentally determining the electronic band structure of the QD (Refs. $15-20$ $15-20$) by analyzing electron emission from the QD. However, this emission time is very short and difficult to resolve probably due to the presence of significant tunneling.^{15[,19,](#page-5-6)[20](#page-5-5)} On the other hand, deep traps with their strongly localized wave functions have been proposed as lo-cal probes for characterizing electronic band structure.^{22–[24](#page-6-1)} When InAs thickness is increased beyond the critical thickness (\sim 3 ML),^{[21](#page-6-2)} strain is relaxed by the formation of misfit dislocations confined near relaxation interface. This trap was previously shown to be point-defect-like²⁵ because its capacitance-time transience exhibits an exponential function. Hence, it would be worthwhile to explore the possibility of using this trap as a probe. Furthermore, understanding the electron-emission properties of this trap may also provide valuable information on strain relaxation. Therefore, in this work, the electron-emission properties of relaxation-induced traps in InAs QDs are investigated in detail by using capacitance-voltage (C-V) profiling and deep-level transient spectroscopy (DLTS) with small step voltages.

Among many electrical characterizations, DLTS is very convenient for evaluating the carrier-emission properties of defect traps. However, when the probed sample is not of a simple bulk structure, analyzing DLTS spectra is difficult because the emission parameters are affected by band structure, and thus bias dependent. This task is even more difficult when multiple traps are present. This work has demonstrated an important effect of electronic band structure. Selecting a small step voltage was necessary to resolve the DLTS spectra of relaxation-induced traps. Aided by *C*-*V* profiling at elevated temperatures and a band structure simulation, we were able to obtain important information on the electronemission parameters of two traps related to threading and misfit dislocations, respectively.

II. EXPERIMENTS

The InAs/GaAs QD samples studied here were grown on n^+ -GaAs (100) substrates by solid source molecular beam epitaxy in a Riber Epineat machine. The QDs were formed in Stranski–Krastanow growth mode by depositing the InAs layer at 490 °C. The QDs structure is sandwiched between two 0.3 μ m-thick Si-doped GaAs (~6×10¹⁶ cm⁻³) layers for electrical characterizations. Detailed growth of the QD samples can be found elsewhere. 26 A typical QD sheet density about 3×10^{10} cm⁻² was observed by atomic force microscopy images. Schottky diodes were realized by evaporating Al on sample surface with a dot diameter of $1500 \mu m$. Photoluminescence (PL) measurements were carried out using a double frequency yttrium-aluminum-garnet:Nd laser at 532 nm. A HP 4194A gain phase analyzer was used for *C*-*V* profiling.

III. MEASUREMENT AND RESULTS

A. DLTS spectra of relaxed QDs

In the previous transmission electron microscopy studies, 21 strain in InAs ODs was shown to relax by the induction of threading dislocations in the top GaAs layer and

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FIG. 1. (a) 300-K C -*V* and (b) concentration profiles measured at 10^5 Hz for the nonrelaxed 2.3 ML and relaxed 3.4 ML samples. Strain relaxation causes carrier depletion near the dot $(0.3 \mu m)$ and neighboring GaAs layers by the generation of two traps, E1 and E2, related to the threading and misfit dislocations, respectively. Their concentrations, determined from the DLTS spectra in Fig. [3,](#page-2-2) show that the E1 trap is located in the top GaAs layer and the E2 trap is located near the QD.

misfit dislocations near the QDs. The threading dislocations which propagate from the QDs to the sample surface are likely generated by the gliding process of interfacial dislocations due to elastic strain acting as a shear stress. $27,28$ $27,28$ In contrast, the misfit dislocations do not propagate into the GaAs layers but are confined near the QD, suggesting a bending of the misfit dislocations toward the QD interface by strain. Similar confinement of misfit dislocations has been observed in relaxed InGaAs/GaAs heterostructures.²⁸

Figures $1(a)$ $1(a)$ and $1(b)$ show the 300-K $C-V$ and the converted concentration profiles of InAs QD samples with InAs thicknesses of 2.3 and 3.4 ML, respectively, measured at $10⁵$ Hz. The latter sample is relaxed because the InAs thickness exceeds the critical thickness of \sim 3 ML.²¹ While the 2.3 ML sample shows strong electron accumulation, the relaxed sample exhibits carrier depletion at the dots $(0.3 \mu m)$ and neighboring GaAs layers. Hence, strain relaxation must induce electron traps to deplete the QDs. DLTS measurements were used to reveal the traps. Figure [2](#page-2-1) shows the obtained spectra of the 2.3 and 3.4 ML samples, along with a 1.1 ML InAs sample which is of a quantum-well structure because QD has not formed. The sweeping voltages are as indicated. While the 1.1 and 2.3 ML samples display no traps, the 3.4 ML sample exhibits a broad signal at low temperature superimposed upon a background signal, suggesting the presence of significant relaxation-induced traps. Figure [3](#page-2-2) shows that when step voltage is reduced to -0.5 V (superimposed upon a quiescent bias from 0 to -4 V), the spec-

FIG. 2. DLTS spectra of the 1.1 ML InAs quantum-well sample, nonrelaxed 2.3 ML, and relaxed 3.4 ML InAs QDs samples under various sweeping voltages as indicated. Strain relaxation causes a broad signal at low temperatures superimposed on a high-temperature background signal, which can be resolved into two major traps in Fig. [3](#page-2-2) when a small step voltage is selected.

trum can be well resolved into two prominent traps, E1 from 0/−0.5 to −3.5/−4 V and E2 from −1/−1.5 to −4/−4.5 V. The rate window is 8.6 ms and filling pulse time is set at 30 ms to ensure the peak-height saturation of the E2 trap. As guided by the dash lines, the peak temperature of $E1$ $(E2)$ shifts toward a lower (higher) temperature as reverse bias is increased. This strong bias dependence can explain the broad spectrum in Fig. [2.](#page-2-1) As shown in Fig. [3,](#page-2-2) the E2 traps for −1/−1.5 and −1.5/−2 V are rather asymmetric with a broad low-temperature tail, and thus their emission energies cannot be determined accurately. Figure [4](#page-3-0) shows the corresponding Arrhenius plots of the traps, from which the activation energies and capture cross sections are obtained.

Given the DLTS spectra in Fig. [3](#page-2-2) and the *C*-*V* curve in Fig. $1(a)$ $1(a)$, the concentrations and spatial locations of the traps

FIG. 3. DLTS spectra of the relaxed 3.4 ML sample, showing two traps; the E1 trap is associated with the threading dislocations in the top GaAs layer and the E2 trap is associated with the misfit dislocations near the QD. With increasing reverse quiescent bias, the peak temperature of the E1 (E2) trap shifts toward a lower (higher) temperature, reflecting smaller electronemission energies for both the traps near the QD. This effect is attributed to the related traps across the QD interface where a band offset locates. The E2 spectra from $-1/-1.5$ to $-2.5/-3$ V is asymmetric with a very broad lowtemperature tail, suggesting the presence of continuous energy states below the GaAs CB edge, to which electrons are emitted from the E2 trap. The continuous states are attributed to the QD electron states.

FIG. 4. Arrhenius plots of the E1 and E2 traps at different sweeping voltages. The electron-emission energies are also indicated in the DLTS spectra in Fig. [3.](#page-2-2)

are evaluated, as shown in Fig. $1(b)$ $1(b)$. The concentration is determined from the expression $(2\Delta C/C)N$, where ΔC is the peak intensity of the related trap and *N* is the background concentration 6×10^{16} cm⁻³. The spatial location is simply obtained from the edge of depletion region (EOD) from the *C*-*V* curve. For example, for the E1 trap observed in 0/ −0.5 V, a middle bias −0.25 V is used to find the EOD. A trap will donate carriers only when Fermi level crosses it. However, at this point, due to the energetic depth of the trap, the EOD is deeper than the crossing point, and thus the obtained location is deeper than the actual location of the trap. From a simple simulation (to be shown), a trap that is located at the QDs $(0.3 \mu m)$ and at 0.35 eV below the GaAs conduction-band (CB) edge will show its emission signal at 0.39 μ m. Thus, the depth 0.39 μ m in Fig. [1](#page-2-0)(b) can be roughly treated as the location of the QDs. Hence, the E1 trap is located from the sample surface to a depth near the QDs. Its concentration gradually decreases from the surface toward the QD. This spatial distribution correlates with that of the threading dislocations, and thus the E1 trap is attributed to the threading dislocations. The E2 trap is located in the vicinity of the QDs; its concentration gradually increases from the top GaAs layer near the QDs, reaches a maximum at the QD, and then rapidly decreases toward the lower GaAs layer. This spatial distribution leads us to attribute the E2 trap to the misfit dislocations.

B. Properties of the threading trap

As shown in Figs. [3](#page-2-2) and [4,](#page-3-0) the electron-emission energy of the E1 trap decreases from 0.63 eV $(\alpha = 3.9)$ $\times 10^{-15}$ cm⁻²) for 0/-0.5 V to 0.36 eV (σ =1.9 $\times 10^{-16}$ cm⁻²) for -3.5/-4 V, suggesting a smaller electron-emission energy near the QDs. The DLTS peak height of this trap shows no saturation even when filling pulse duration time is increased to 100 ms. This feature is consistent with the capacitance-time transience of the traps associated with threading dislocations, which exhibits a logarithmic function, 27.28 27.28 characteristic of Coulombic repulsion of the carriers captured at the traps along with the linearly arrayed dislocation lines. 27 This trap is believed to be the ED1 (0.68 eV), reported by Wosinski²⁷ from studying plas-

FIG. 5. *C-V* spectra and the corresponding concentration profiles (in the inset) of the relaxed 3.4 ML sample, measured at 370 K. When frequency is reduced to 200 Hz, the *C*-*V* spectra show a *C* plateau, corresponding to an electron-emission peak at the QD, and a *C* protrusion, corresponding to electron emission from the E2 trap.

tically deformed GaAs, and the trap at 0.58 eV observed in InGaAs/GaAs by Watson *et al.*[29](#page-6-7) who assigned it to the cores of α -type dislocations. The electron-emission energy obtained by Watson *et al.*^{[29](#page-6-7)} is smaller than that by the Wosinski²⁷. Watson *et al.*^{[29](#page-6-7)} explained this by the threading dislocations in the InGaAs side which has a smaller band gap, as relative to GaAs. He suggested that the trap was probably tied to the valence-band edge and thus the emission-energy difference might be the band gap difference between GaAs and InGaAs. This band structure effect can explain the decrease of the electron-emission energy of the E1 trap. A trap generally decreases its emission energy as the band gap of the host material is reduced. For example, Irvine and Palmer 30 have shown an electron-emission-energy decrease in EL2 from 0.84 to 0.62 as *x* increases from 0 to 0.18 for an In_xGa_{1−*x*}As layer grown on GaAs. Hence, the electronemission-energy change may reflect the energy band structure of the host material. Further investigation is needed to make a quantitative argument.

C. *C***-***V* **profiling at elevated temperatures**

As Fig. $1(b)$ $1(b)$ is shown, the QD in the relaxed 3.4 ML sample is depleted of electrons. This depletion is presumably caused by the E2 trap which must lie in energy below the QD electron ground state. Due to the long emission time, electrons trapped on the E2 trap cannot follow the alternating current (ac) signal to be modulated. This depletion remains as frequency is lowered to 200 Hz. However, one may directly activate the electron emission from the trap by increasing temperature to reduce the emission time. Figure [5](#page-3-1) shows the $C-V$ and the corresponding depth profiles (in the inset) at 370 K. The profile shows similar carrier depletion at 10^5 Hz; however, at 200 Hz, a *C* plateau at around −3 V and a *C* protrusion at around −4.5 V emerge. The *C* plateau is converted to a carrier-accumulation peak centered at 0.33μ m. This depth is close to the location $(0.34 \mu m)$ of the carrieraccumulation peak in the nonrelaxed 2.3 ML sample \lceil in Fig. $1(b)$ $1(b)$, and thus the peak is considered as electron emission from the QD electron states and is designated as quantum emission (QE). The presence of the QD electron states can

FIG. 6. 10-K PL spectra of the nonrelaxed 2.3 and relaxed 3.4 ML QD samples, showing a redshift of the QD ground emission. Strain relaxation does not severely degrade the QD quality. Also included is a schematic CB diagram showing the QD electron ground state (at 0.19 eV below the GaAs CB edge) and energetic and spatial locations of the threading trap E1 and misfit trap E2. For simplicity, the effect of electronic band structure on the electron-emission energies of both traps is not shown.

be justified from the 10-K PL spectra in Fig. [6](#page-4-0) of the nonrelaxed 2.3 ML and the relaxed 3.4 ML samples. The 2.3 ML sample shows a QD ground emission at 1.13 eV which is redshifted to 1.1 eV in the 3.4 ML sample presumably due to an increase in dot size. The peak of the 3.4 ML sample is only slightly broader than that of the 2.3 ML sample, suggesting that strain relaxation does not destroy the QD electron ground state. Kapteyn *et al.*[15](#page-5-4) reported a value of 0.19 eV for the confinement energy of InAs QD electron ground state for an QD ground emission at 1.12 eV, a value close to the emission energy of the 3.4 ML sample. Thus, the QD electron ground state is placed at 0.19 eV below the GaAs CB edge, as shown in the simplified schematic CB diagram in Fig. [6.](#page-4-0) Neglecting the effect of band structure, the energetic locations of the E1 and E2 traps are also illustrated.

D. Depth-profile simulation of the misfit trap

As to the *C* protrusion at -4.5 -4.5 V (in Fig. 5), it may be due to the electron emission from the E2 trap. From the DLTS data, the emission rate of the E2 trap (at 0.37 eV for $-3/-3.5$ V) can be extrapolated to 1.7×10^3 Hz at 370 K which is larger than the ac modulating frequency of 200 Hz, allowing for the E2 trap to be modulated. This *C* protrusion is seen after the QE peak, suggesting that the E2 trap lies below the QD electron ground state, consistent with the carrier-depletion effect. To see what energy level the electron-emission energy is relative to, we simulate *C*-*V* profile by solving the Poisson equation for a *n*-type GaAs Schottky diode (a barrier height of 0.84 eV) with a background doping of 6×10^{16} cm⁻³ and an acceptor trap of 3 \times 10¹⁷ cm⁻³ at the QD (0.3 μ m) with a thickness of 10 nm (equivalent to a sheet concentration of 3×10^{11} cm⁻²) and at 0.35 eV below the GaAs CB edge, as shown in Fig. $7(a)$ $7(a)$. Figure $7(b)$ $7(b)$ shows the simulated $C-V$ profiles under three conditions: the *L* curve represents a low ac frequency so that the trapped electron can be modulated, the *M* curve represents a medium frequency so that the trapped electron cannot

FIG. 7. (a) Energy band diagram used for simulation, assuming the presence of a trap with a concentration of 3×10^{17} cm⁻³, located near the QD with a thickness of 10 nm and at 0.35 eV below the GaAs CB edge. (b) Simulated *C*-*V* spectra under three conditions depending upon ac frequency and dc sweeping rate: the *L* curve represents that the trapped electron can follow ac signal, the *M* curve represents that the trapped electron cannot follow ac signal but can follow dc sweeping rate, and the *H* curve represents that the trapped electron cannot even follow dc sweeping rate. (c) Simulated concentration profile without any trap emission (in the M condition) and concentration profile of electron emission from the E2 trap.

follow the ac signal but can follow the direct current (dc) sweeping rate, and the *H* curve represents that the trapped electron cannot even follow the dc sweeping rate (this condition is not satisfied during the experiment). As shown, the *L* and *M* curves are in general agreement with the experi-mental curves in Fig. [5](#page-3-1) under 200 (neglecting QE plateau) and 10^5 Hz, respectively. Figure $7(c)$ $7(c)$ shows the simulated depth profile in the *M* condition, illustrating carrier depletion at around 0.3 μ m which is in fair agreement with the experimental carrier depletion in Fig. $1(b)$ $1(b)$. Figure [7](#page-4-1)(c) also shows the simulated concentration profile of the E2 trap using the same equation, $(2\Delta C/C)N$ as used in Fig. [1](#page-2-0)(b), except ΔC is taken from the difference between the *L* and *M* curves. The spatial location of the E2 trap is obtained from the *M* curve, which shows a nearly constant C (0.55 nF) for the observation of the *C* protrusion in the *L* curve. This constant *C* yields an EOD depth of 0.38 μ m which is in fair agreement with the maximum concentration (0.39 μ m) of the E2 trap in Fig. $1(b)$ $1(b)$. Therefore, the carrier depletion is mainly due to the E2 trap that is located at the QDs and at 0.37 eV below the GaAs CB edge. Note that the simulated *C* protrusion starts at −3 V; however, the DLTS spectra show the E2 trap starting from −1/−1.5 V. Thus, E2 trap probably spread into the top GaAs layer near the QDs.

E. Mode of strain relaxation

The DLTS peak height of the E2 trap (at 0.37 eV) is found to increase and finally saturate with increasing filling pulse time. This is consistent with its capacitance-time transience which displays an exponential function, 25 suggesting an isolated point defect. In terms of its activation energy, it is believed the one at 0.395 eV $(\sigma = 1 \times 10^{-16} \text{ cm}^{-2})$ observed by Uchida *et al.*[27](#page-6-5) who related it to the misfit dislocations near the relaxed InGaAs/GaAs heterojunction. Uchida *et* al^{27} al^{27} al^{27} claimed that this trap was a monolevel trap because its capture cross section was temperature independent. Recently, Yastrubchak *et al.*^{[31](#page-6-9)} reported an exponential function for a misfit-related hole trap in InGaAs/GaAs heterostructures. This trap was claimed to have a higher regularity as compared with threading dislocations. Since the E2 trap is likely a point defect and is at 0.37 eV below the GaAs CB edge, we compare it with point defects in GaAs. In terms of its energy location, it is likely EL6 $(E_c - 0.35 \text{ eV})$ (Ref. [32](#page-6-10)) observed in low-temperature grown GaAs, commonly considered as V_{Ga} -As_{*i*} complex. If it is indeed V_{Ga} -As_{*i*}, we can deduce a likely mode of strain relaxation. Due to underlying GaAs layer, the deposited InAs QDs are compressed laterally. At the onset of relaxation, the compressive stress in the QDs is relieved probably by the displacement of As atoms from their lattice sites, leading to excess As in the form of As*ⁱ* near the QD. These As*ⁱ* may react with the misfit dislocations to produce Ga vacancies (V_{Ga}) by the interaction³⁰: As_i + dislocation \rightarrow dislocation climb + V_{Ga} . After that, the induced V_{Ga} may interact with residual As_i and form As_i - V_{Ga} complex, leading to the spatial correlation between the E2 trap and misfits.

As Fig. [3](#page-2-2) shown, the electron-emission energy of the E2 trap increases from 0.28 to 0.42 eV as sweeping voltage increases from $-2/-2.5$ to $-3.5/-4$ V. Additionally, the E2 spectra from $-1/-1.5$ to $-2.5/-3$ V (corresponding to a region near the QDs) contain a very broad low-temperature tail. The bias dependence of the electron-emission energy cannot be explained by a field-enhanced tunneling, since increasing the amplitude of bias would enhance emission rate and reduce electron-emission energy. This bias dependence seems to suggest a rather broad energy spectrum of the E2 trap. However, the E2 trap at 0.37 eV for $-3/-3.5$ V, as well as for −3.5/−4 V, is quite symmetric and narrow in linewidth. Also, from the simulation, electrons are emitted to the GaAs CB for these sweeping voltages. Thus, we explained the lower electron-emission energy and the low-temperature tail (from $-1/-1.5$ to $-2.5/-3$ V) as an effect of band structure. Continuous energy states may exist below the GaAs CB edge to which electrons trapped on the E2 traps are emitted. Since they are observed in a region near the QD, the continuous states are considered as the QD electron states. Note that this trend is similar as the E1 trap; both traps show smaller electron-emission energies for a region near the QDs. Strain relaxation is expected to enhance fluctuation of the QD states, leading to a smearing of emission energies and formation of tails. The results of the present studies illustrate a significant effect of electronic band structure on the

electron-emission properties of the traps. Hence, it is possible that they can be used as probes for energy band structure of the host material.

IV. CONCLUSIONS

We show that strain relaxation in InAs QDs can produce two electron traps: one is associated with threading dislocations in the top GaAs layer and the other is associated with misfit dislocations near the QD. The threading trap decreases its electron-emission energy from 0.63 to 0.36 eV from the sample surface toward the QD. This is explained by the trap across the QD interface where a band offset exists. By performing *C*-*V* profiling at elevated temperatures, we observe a *C* plateau followed by a *C* protrusion; the plateau is identified as the electron emission from the QD and the protrusion as the electron emission from a defect trap. A simple simulation establishes that the defect trap is the misfit trap located at 0.37 eV below the GaAs conduction-band edge. In terms of its emission energy, this misfit trap is ascribed to EL6, commonly considered as V_{Ga} -As_{*i*} complex. As the E1 trap, the emission energy of the E2 trap shows a smaller electronemission energy and a broad low-temperature tail for a region near the QD.

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