

Bistable Resistive Switching in Al₂O₃ Memory Thin Films

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In this study, we investigate the resistive switching behavior of radio frequency (rf)-sputtered Al_2O_3 thin films. It is observed that both high-conducting state (ON state) and low-conducting state (OFF state) are stable and reproducible during successive resistive switchings by dc voltage sweeping. The ratio of resistance of the ON and OFF state is over 10^3 . Such a reproducible resistive switching can be performed at 150° C, and the resistance of the ON state can be altered by various current compliances. The conduction mechanisms of the ON and OFF states are demonstrated as ohmic conduction and Frenkel-Poole emission, respectively. Both states, performed by dc voltage sweeping and applying short pulse, are stable over 10^4 s at a read voltage of 0.3 V and the electrical-pulse-induced resistance change (EPIR) phenomenon is demonstrated, which are all important properties for further resistive random access memory application.

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The next-generation nonvolatile memory (NVM) has attracted extensive attention due to the conventional flash memory approaching its scaling limits. When the tunneling oxide thickness is below 10 nm, the charge stored in the floating gate is lost due to direct tunneling current or defects formed in the tunneling oxide during long-term operation. Therefore, several types of NVMs such as ferroelectric random access memory, magnetic random access memory, and resistive random access memory (RRAM) are being investigated. Many different material systems such as perovskite-type oxides, $^{1\text{-}3}$ organic materials, 4 and binary metal oxides exhibit bistable switching of resistance by applying voltage bias, and both states can maintain their own resistance values for a long time. Such a memory application should have the merits of low power consumption, compatibility of the current complementary metal oxide semiconductor (CMOS) process, high-speed operation, high scalability, and simple metal-insulator-metal (MIM) trilayer structure. Among the materials used for RRAM, simple binary metal oxides such as NiO,⁵⁻⁷ TiO₂,⁸ HfO₂,⁹ ZrO₂,¹⁰ Nb₂O₅,¹¹ and Cu_xO¹² have also been extensively investigated due to their simple compositions which are easily controlled during device fabrications. As for Al₂O₃, the negative resistance phenomenon was observed in anodic Al_2O_3 under vacuum condition by Hickmott several decades ago.¹³⁻¹⁵ Recently, it was demonstrated that the reversible switching of local conductivity in thin Al₂O₃ film, used for magnetic tunnel junctions, was studied in situ by scanning tunneling microscopy (STM) under vacuum condition.¹⁶ The resistive switching phenomenon in the Al₂O₃ thin film seems to be easily observed under vacuum condition, indicating that the phenomenon under vacuum might be more stable in comparison with that under the atmosphere condition. Kim et al. deposited Al₂O₃ resistive switching films by atomic layer deposition (ALD) between a Pt top electrode and a Ru bottom electrode to complete a MIM structure and performed the resistive switching behavior in a Pt/Al₂O₃/Ru device under atmosphere.¹⁷ However, the resistive switching behavior in the Pt/Al₂O₃/Ru device showed large variations in both memory states due to more random formation and rupture of conducting filaments. Moreover, the conducting filaments inside the Pt/Al₂O₃/Ru device are more difficult to rupture, leading to a lower resistance ratio between high and low resistance states. In this study, we report the reproducible and stable resistive switching phenomenon in a Ti/Al2O3/Pt device under atmosphere conditions with a large resistance ratio above 10^3 , demonstrating that Al₂O₃ thin films have promising potential for NVM applications.

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Experimental

The boron-doped p-type silicon (100) wafer was adopted as substrate for device fabrication. After the standard RCA cleaning, the 200 nm thick SiO₂ film was thermally grown by a furnace to insulate the leakage current from the Si substrate. A 20 nm thick Ti layer and then an 80 nm thick Pt layer were deposited on SiO₂ by electron-beam evaporation at room temperature. Subsequently, a 40 nm thick Al₂O₃ film was deposited on Pt/Ti/SiO₂/Si substrates by rf magnetron sputtering. During the deposition process, the substrate temperature was kept at 250°C and the working pressure was maintained at 10 mTorr by a mixture of Ar and O₂ at a mixing ratio of 1:1 with a total flow rate of 20 sccm. According to X-ray diffraction analysis, the low-temperature (250°C) deposited Al₂O₃ thin film on the Pt bottom electrode was amorphous. Finally, different top electrodes, including the 300 nm thick Ti and Al, and 80 nm thick Pt with 250 µm diameter were deposited by electron-beam evaporation at room temperature to perform electrical measurement with Agilent 4155C and 81110A. All the current-voltage (I-V) characteristics were measured by the Agilent 4155C. While performing the electrical-pulse-induced resistance change (EPIR) phenomenon, the Agilent 81110A was employed to generate voltage pulses to switching the resistance of the device and then the resistance states were verified by the Agilent 4155C. The measurement system is depicted schematically in Fig. 1. The bias voltage is applied on the Ti top electrode with Pt bottom electrode common. All the measurements were performed under atmospheric conditions.

Results and Discussion

Before any resistive switching is performed, a forming process, achieved by applying +11 V dc voltage bias on Ti top electrode with a current compliance of 5 mA, is necessary. After the forming process, the Ti/Al₂O₃/Pt device reaches its high conductive state (ON



Figure 1. Schematic view of electrical measurement system.



Figure 2. (a) Typical I-V curve of a $Ti/Al_2O_3/Pt$ structure. Arrows indicate the sweep direction of the applied dc voltage. The current compliance is set to 5 mA. The inset shows the variation of the resistance ratio with the bias voltage. (b) Evolution of ON state and OFF state during sequent voltage sweeping.

state). By sweeping voltage bias to negative over a certain voltage of about -1.7 V, the device is switched from high conductive state to low conductive state (OFF state). An abrupt increase in current is present and the device is switched back to ON state again by sweeping toward positive voltage bias above a value of about 1.4 V. The typical I-V curve of Ti/Al₂O₃/Pt device under atmosphere at room temperature is shown in Fig. 2a, and the forming curve is not shown here. Both ON and OFF states are nonvolatile and reversible, quite stable, and the ratio of resistance in both polarities is above three orders of magnitude at low voltage, shown in the inset of Fig. 2a. The bistable memory switching is entirely reversible and reproducible during successive voltage sweeping as shown in Fig. 2b.

The Ti/Al₂O₃/Pt device exhibits good resistive switching behavior and has low forming voltage of about 11 V. It might be attributed to the Ti top electrode extracting a large amount of oxygen ions from the Al₂O₃ film (Al₂O₃ + Ti \rightarrow TiO_x + Al₂O_{3-x}) and then inducing a large amount of oxygen vacancies at the metal-insulator interface ($XO_0 \rightarrow XV''_0 + XO^{2-}$), or modifying the oxygen distribution within Al₂O₃ memory film. As a result, the oxygen content and oxygen-related defects presented in Al₂O₃ films have a great influence on their forming voltage and resistive switching properties.^{1,3,5} When using Al and Pt as top electrodes in our devices, their forming voltages are so large (over 25 V) that they cause permanent damage on the devices, resulting in complete dielectric breakdown. Although



Figure 3. Resistive switching phenomenon also can be performed at 150°C with a resistance ratio of two orders of magnitude.

the Pt electrode (work function ~5.8 eV) shows a catalytic adsorption and desorption effect for oxygen ions, or Al electrode with a similar work function to Ti, their ability of extracting oxygen ions from Al_2O_3 films is worse than that of Ti, leading to higher forming voltages. In Fig. 3 the resistive switching in the Ti/Al₂O₃/Pt device is also performed at 150°C. However, the resistance ratio, shown in inset of Fig. 3, is obviously lower than that at room temperature, and the ratio of resistance remains 2 orders of magnitude, indicating that the device still can be operated under high temperature. The characteristics of ON state under various magnitudes and current compliances from 1 to 20 mA were measured and are shown in Fig. 4. The current level of ON state has a linear relation to the compliance current as shown in the inset of Fig. 4. The current values of ON state increase as the current compliances increase, implying the possibility for multibit memory storage.

The I-V characteristics indicated in Fig. 2a were obtained from a typical I-V curve within ± 1 V in a double logarithmic plot. Because the slope in the low-field region is very close to 1, the conduction mechanisms of OFF state under low electric field is believed to be satisfied with ohmic conduction. At higher voltage bias, the conduction mechanism of ON state still obeys ohmic conduction, implying that ohmic conduction dominates the carrier transport in the ON



Figure 4. I-V curves obtained by different current compliances. The inset shows the ON state current is in linear relation to value of current compliance.



Figure 5. (a) I-V characteristics, obtained from a typical I-V curve within ± 1 V, in a double logarithmic plot. (b) The conduction mechanisms of OFF state in both positive and negative regions under high electric field are Frenkel-Poole emission by curve fitting. (c) The I-V curves of the OFF state measured and collected at various temperatures from 30 to 150°C and the inset shows the Arrhenius plot.

state as shown in Fig. 5a, but it is not suitable for the OFF state. Figure 5b depicts the conduction mechanisms of the OFF state in both positive and negative regions under high electric field as Frenkel-Poole emission by curve fitting. Therefore, the conduction mechanisms dominating in the ON state and OFF state are ohmic



Figure 6. Both ON and OFF state, obtained by dc voltage sweeping and by applying short voltage pulses, stable over 10^4 s at 0.3 V stress.

conduction and Frenkel-Poole emission, respectively. The I-V curves of the OFF state are also measured and collected at various temperatures from 30 to 150°C as shown in Fig. 5c. The increasing OFF-state current is observed as the temperature increases. The activation energy obtained from the slope of the Arrhenius plot as shown in the inset of Fig. 5c is 0.11 eV. Therefore, we believe that the conduction mechanism in the OFF state is Poole-Frenkel emission.

By sweeping dc voltage bias and applying short voltage pulses, both ON state and OFF state can be achieved and are stable over 10^4 s at a read voltage of 0.3 V, as shown in Fig. 6. The EPIR property shown in Fig. 7 is investigated as well, which is important for future nonvolatile memory applications. The asymmetric bipolar switchings are shown in Fig. 6 and 7 in which short pulses applied are 10 ns, 6 V, and 10 ns, and -3 V to switch the device to ON state and OFF state, respectively. In the previous reports, the bipolar resistive switching suggested in Pt/(Ba,Sr)(Zr,Ti)O₃/Pt might be due to charging and discharging of traps.¹⁸ Besides, they proposed a model in which the trap can be illustrated as a potential well with different sidewall heights, which might originate from the lattice distortion, tilting, and even the nanopolar regions in paraelectric state. The asymmetrical bipolar resistive switching was also reported



Figure 7. Write-read-erase-read cycles demonstrated over 300 times by successively applying a 10 ns, 6 V pulse to switch into ON state and back to OFF state by 10 ns, -3 V pulse.

in Ti/Cu₂O/Cu due to the interface reaction that caused asymmetric trap levels to both electrodes. $^{\rm 12}$ Based on the above previous works, the asymmetrical bipolar resistive switching in our Ti/Al₂O₃/Pt device is more likely due to the interface reaction between Ti/Al₂O₃. Moreover, such a high-speed resistive switching behavior was also demonstrated in the top-electrode/TiON/TiN device,¹⁹ the $Pt/TiO_2/TiN/Pt$ device,⁸ and a device composed of a Ti layer confined by an insulating self-assembly nanogap oxide.²⁰ The nanoscle conducting paths are supposed to be formed⁷ in our Ti/Al₂O₃/Pt device after the forming process. The resistive switching phenomenon is believed to occur in certain tiny segments in narrow conducting paths while the local Joule heating and electric field are expected to be reinforced in those tiny regions, leading to the high-speed resistive switching.²⁰ However, Fujimoto et al. suggested that a nanosecond pulse was quite enough for the sudden increase or decrease of donor concentration by O^{2-} migration in the TiO₂ layer of 2.5 nm thickness, which triggered the Mott transition.⁸ The Mott transition cannot be ruled out for the resistive switching phenomenon. Hence, both the local Joule heating and Mott transition mechanisms might be the reasons for the high-speed resistive switching in our Ti/Al2O3/Pt device. It is going to make the Ti/Al₂O₃/Pt devices more suitable for NVM applications by modifying the device fabrication parameters to improve the ratio of ON/ OFF state resistance and write-read-erase-read cycle number in future works.

Conclusions

We have demonstrated reproducible and stable resistive switching behavior in a Ti/Al₂O₃/Pt device. The ratio of resistance of ON and OFF states in both polarities was above 3 orders of magnitude at low voltage. The device also can be operated at 150°C and the resistance value of the ON state can be altered by current compliance for possible multibit applications. The dominated conduction mechanisms of both ON state and OFF state are demonstrated as ohmic conduction and Frenkel-Poole emission, respectively. On the basis of our experimental results, which are obtained by dc voltage sweeping and applying short pulse (10 ns + 6 V pulse to switch the device to ON state and 10 ns - 3 V pulse to OFF state), both states were stable over 10⁴ s and write-read-erase-read cycles were over 100 times. The Ti/Al₂O₃/Pt devices may be a candidate for further NVM applications.

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