

Design and Implementation of a Voice-Coil Motor Servo Control IC for Auto-Focus Mobile Camera Applications

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Abstract—This paper presents the design methodology to implement a fully digital control IC for the position control of the auto-focus (AF) lens module in applications to mobile phone camera using the field programmable gate array (FPGA). As compared with the conventional stepping motor control with a spring return fixture, the AF lens module is driven by a voice-coil motor (VCM) with a digital servo drive IC. The proposed digital servo drive IC includes a digital servo controller, a digital current controller, and a full-bridge dc-dc converter. The designed digital servo drive IC provides a total digital solution to the position control of the AF lens module in applications to high-performance light-weight slim-type mobile phone camera and video. The designed AF lens module can reach a control range of 0.6 mm within 30 ms with a control resolution less than 5 μm, a peak driving force of 30 mN, and a peak output current of 120 mA.

I. INTRODUCTION

In today's marketing world of mobile camera, there are mainly three types of the actuator: stepper motors, piezoelectric materials and VCM with spring return. The stepper motors are easy to control and need no holding power after lens movement is complete, but the mechanical architecture is complex and thus their volume are relatively large. Using the piezoelectric materials as actuators needs no gears and the power consumption is low, but they are sensitive to temperature and the control laws are complex. The VCM has features of small volume, low cost, easily to be implemented and shock-resistant [1]. However, a spring is needed to implement the open loop position control and thus holding power is inevitable. The current that sinks is positive to the displacement, and can be up to 0 ~100 mA.

The design of VCM in this paper is composed of a set of permanent-magnetic circuit and coil. Its operation principle follows Lorentz force law. Instead of using spring-return method, the lens' motion is under closed loop control. Thus the holding power is lowered and the ringing phenomenon does not exist. The volume of the lens module is 10mm × 14mm × 13mm and the mass of the mover is 1 g.

The proposed VCM servo control IC provides totally digital solution for fast auto focus mobile phone camera applications.

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The driver stage uses a full-bridge converter as the power amplifier, and the switching frequency is 100 kHz. The DC link voltage is 3.3V and the maximum output current is 120 mA. The settling time is less than 30ms, thus it is capable of dealing with a shooting speed of 30 frames per second.

II. DYNAMIC MODEL OF THE LENS MODULE

The lens module is composed of three parts: a camera lens, a VCM and a pair of guide pin. The mathematical model of the module can be represented as follows:

$$i \cdot K_f = M \frac{dv}{dt} + Mg + F_{pin}(p_o) + f(v, p_o) \quad (1)$$

$$v_o - v \cdot K_e = i \cdot R + L \frac{di}{dt} \quad (2)$$

where K_f is the force constant in N/A and equal to the back EMF constant in V-sec./m. Theoretically K_f is related to the number of windings and the flux density in the air gap and is various with position. $F_{pin}(p_o)$ represents the magnetic force between the guide pins and the mover along the moving direction. The guide pin force $F_{pin}(p_o)$ can be extracted from electromagnetic simulation software. According to Ansoft's Maxwell simulation, $|F_{pin}(p_o)|_{max}$ is about 15 mN at the ends of the guide pin and zero at the center of the full stroke. Mg is the weight of the camera lens, which should be taken into consideration while moving up and down. $f(v, p_o)$ represents the standard Coulomb plus static plus viscous friction model [2]-[3]. Let $iK_f - Mg - F_{pin}(p_o)$ be $F_u(p_o)$ and the stick region width be $\pm\Delta v$, during stick, $|v| < \Delta v$:

$$f(v, p_o) = \begin{cases} F_u(p_o) & \text{if } |F_u(p_o)| \leq |f_s| \\ f_s \cdot \text{sgn}(F_u(p_o)) & \text{otherwise} \end{cases} \quad (3a)$$

and during slip, $|v| > \Delta v$:

$$f(v, p_o) = f_c \text{sgn}(v) + K_B v \quad (3b)$$

where $f_c > 0$ is Coulomb friction level, f_s is the static friction force, and $K_B > 0$ is the viscous friction coefficient. Extra current is needed when the lens module is beginning to move, and the Stribeck effect [4] causes low frequency stick-slip

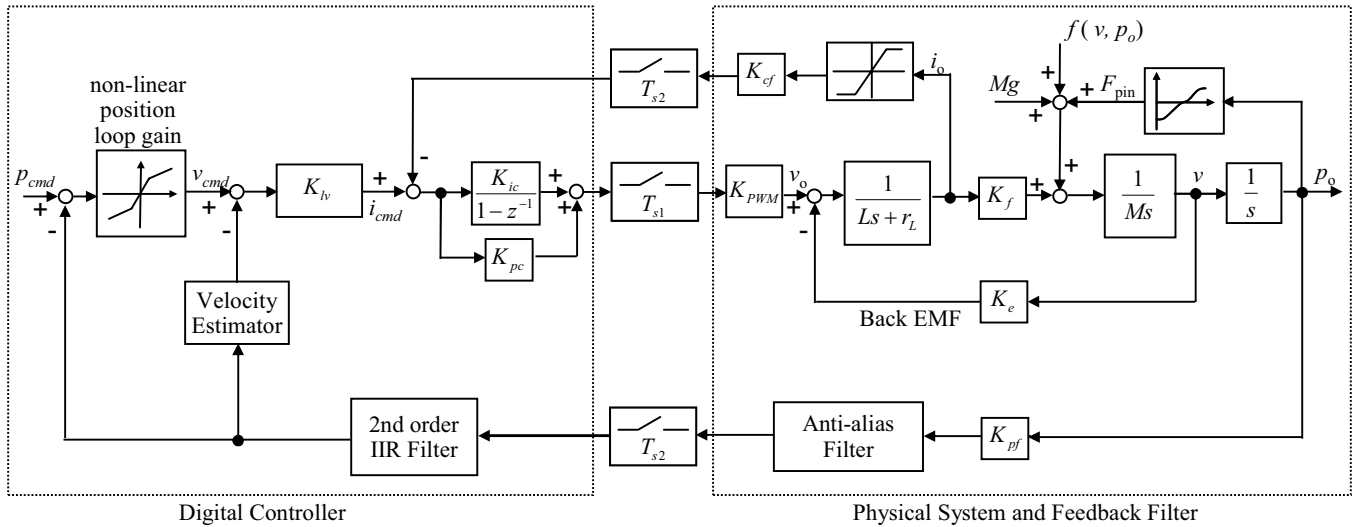


Fig. 1. Block diagram of the proposed digital control scheme.

limit cycle oscillation, thus the nonlinear effects arose from friction must be taken into consideration when designing the controllers.

As shown in Fig. 1, the physical system includes the lens module, a full bridge converter with switching frequency 100 kHz, a current sensing circuit, a position sensor and an anti-alias filter. A 200 kHz A/D converter is shared between the current feedback and position feedback. The digital controller is mainly composed of the three loops controllers: current-loop, velocity-loop, and position-loop controller. The second-order IIR filter is used to filter out the A/D conversion noise. The velocity estimator uses the second-order Taylor series expansion, which will be discussed later.

III. CURRENT LOOP DESIGN

Fig. 2 shows the current driver is realized by a digital control loop, in which the driver stage is a full-bridge converter. The n -bit digital pulse width modulator generates switch control signals by means of unipolar voltage switching.

Compared with bipolar voltage switching, under the same switching frequency, unipolar has less output current ripple:

$$\Delta i_L = V_d(1-D)DT_s / 2L \quad (4)$$

The main advantage in using a current driver is that the current through the VCM is insensitive of variations in winding impedance and back EMF. The back EMF can be viewed as a disturbance in the current loop. The transfer function $G_c(s)$ from v_o to i_o can be represented as:

$$G_c(s) = \frac{Ms}{MLs^2 + r_L Ms + K_e^2} \quad (5)$$

In this application, the VCM has small inductance and relatively large equivalent series resistance (ESR), thus

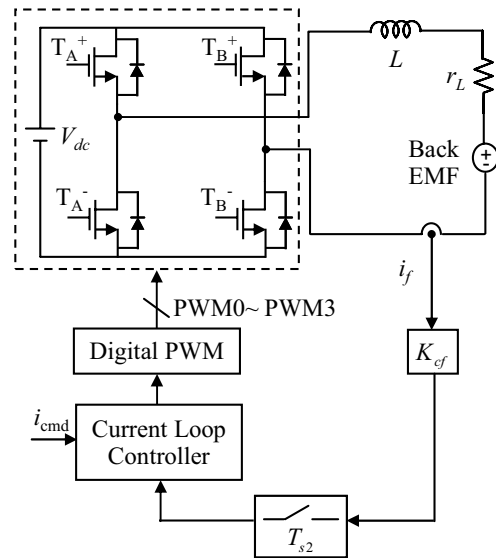


Fig. 2. Full-bridge converter used in the current loop.

$$\frac{r_L^2}{L} \gg \frac{4K_e^2}{M} \quad (6)$$

The transfer function $G_c(s)$ can be reduced to

$$G_c(s) \approx \frac{1}{Ls + r_L} \quad (7)$$

In order to improve dynamic response, the bandwidth of current loop can be extended with pole-zero cancellation to speed up the response time. Thus a PI controller is applied and the current-loop bandwidth is decided by the controller gain. Let $i_e[k] = i_c[k] - K_{cf} \cdot i_l[k]$ the current-loop control law is designed as

$$v_c(k) = (K_{ic} + K_{pc})i_e[k] + K_{pc}i_e[k-1] + v_c[k-1] \quad (8)$$

$$K_{pc} = \frac{2\pi K_c L}{K_{PWM} K_{cf}} \quad (9)$$

$$K_{ic} = \frac{2\pi K_c r_L}{f_{sc} K_{PWM} K_{cf}} \quad (10)$$

where f_{sc} is the sampling frequency of the current-loop controller and K_c is the controller gain that decides current-loop bandwidth. However, the switching frequency of the full-bridge converter is 100 kHz, according to sampling theorem, the current-loop bandwidth can not larger than 50 kHz. Fig. 3 shows that with 200 kHz A/D converter sampling rate and 100 kHz switching frequency is considered, rising K_c from 30 k to 50 k has less effect on increasing current-loop bandwidth. Instead, too large the current loop gain K_c introduces high frequency oscillation. On the other hand, with unipolar switching, the dead time between the upper and bottom transistor has large effect on the performance of current loop. The effect is evaluated by total harmonic

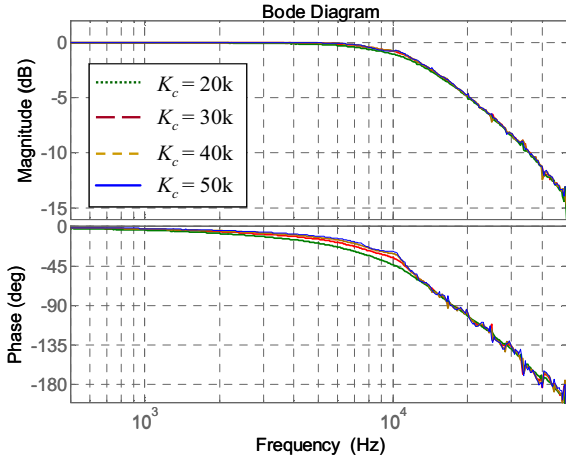


Fig. 3. Frequency response of the current loop with different controller gain K_c .

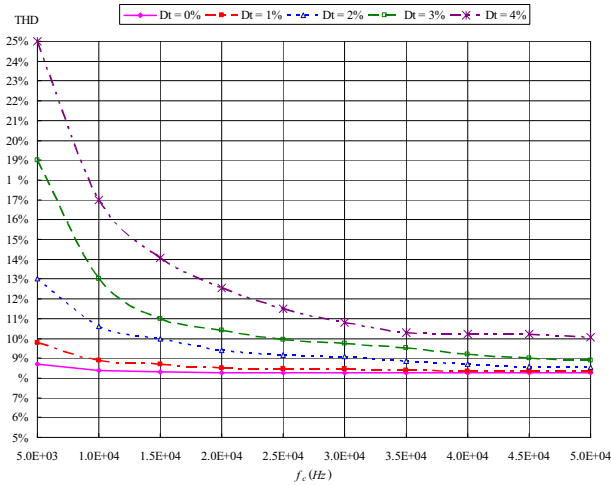


Fig. 4. Simulation results of f_c versus THD with dead time.

distortion (THD), which is defined as

$$\%THD = 100 \times \sqrt{\sum_{n=1}^{\infty} \left(\frac{I_n}{I_1}\right)^2} \quad (11)$$

where I_j is the RMS value of the fundamental current and I_n is the n -th harmonic. The effect of dead time on the THD is shown in Fig. 4 where D_t represents its percentage ratio.

The input current command is 20% of the maximum current and the fundamental frequency is 5 kHz. For $D_t = 2\%$ is concerned, increasing the current-loop gain can reduce the dead time effect, that is, lower the THD. However, when f_c is larger than 25 kHz, increasing f_c has little influence on reducing the THD, thus for THD improvement and stability consideration, the current-loop bandwidth is designed between 20 kHz to 25 kHz that provides good tracking ability for 5 kHz current command.

IV. SERVO LOOP DESIGN

A. Velocity Loop

Since the position signal is present in the digital controller, a numerical method is used to estimate the motor speed. The velocity estimator is implemented using the second-order Taylor series expansion [5]:

$$\hat{v}_k = K_D[\Delta x_k + 1/2(\Delta x_k - \Delta x_{k-1})] \quad (12)$$

where \hat{v}_k is the estimated velocity during k -th period, Δx_k and Δx_{k-1} represent measured position change during k -th and $(k-1)$ -th sampling interval. As Fig. 5 is shown, K_{pf} is the position sensor gain. The velocity feedback acts at the transient of the position response and rests when the lens module is in position. The velocity controller gain K_{iv} is decided by

$$K_{iv} = \frac{\omega_{cv} K_f K_{pf} K_D}{K_{cf} M} \quad (13)$$

where ω_{cv} is the velocity-loop bandwidth. However, increase of K_{iv} may introduce audible noise at the transient of position response. Based on the experiments, ω_{cv} is designed at 1 kHz. The sampling rate of the velocity loop is selected at 20 times of the velocity loop bandwidth, thus the phase lag contributes by sampling delay can be ignored. Since the current-loop bandwidth is designed at 10 kHz, the current loop can be viewed as a DC gain $1/K_{cf}$ while designing the velocity loop, where K_{cf} is the feedback gain of the current loop.

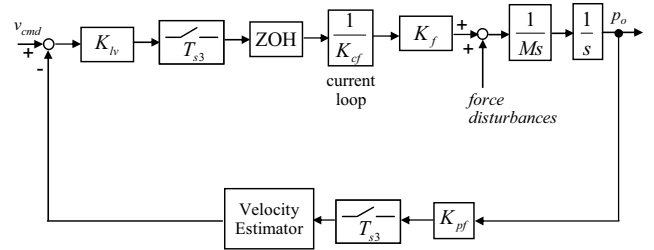


Fig. 5. Block diagram of the velocity loop.

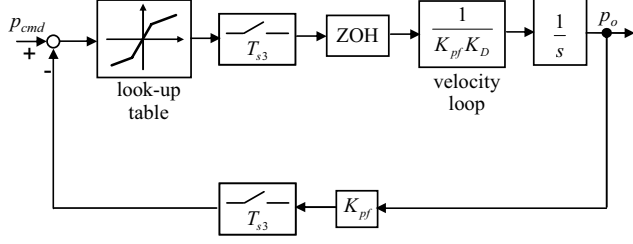


Fig. 6. Block diagram of the position loop.

B. Position Loop

Fig. 6 shows that the position controller is a look-up table that has higher gain when the position error is small and lower gain when the position error is large. The output of the controller is velocity command which is defined as

$$v_{cmd} = \begin{cases} K_{p1} \cdot p_{err} & \text{if } |p_{err}| \leq \varepsilon \\ K_{p2} \cdot p_{err} + \varepsilon \cdot \Delta K_p & \text{if } \varepsilon < p_{err} \\ K_{p2} \cdot p_{err} - \varepsilon \cdot \Delta K_p & \text{if } p_{err} < -\varepsilon \end{cases} \quad (14)$$

where $\Delta K_p = K_{p1} - K_{p2}$, K_{p1} and K_{p2} are the controller gain of different region of the position error defined by ε . Applying higher gain at low position error makes the system generate larger current command to overcome the force disturbances such as the weight of the lens Mg , guide pin magnetic force $F_{pin}(p_o)$ and static friction f_s when the VCM is beginning to move. The higher gain K_{p1} also reduces position steady state error since no integrator is present in the servo loop controller. The reason that not using an integrator is to prevent the system suffers from stick-slip oscillation. The steady state position error produced from Mg and $F_{pin}(p_o)$ is

$$P_{ess} = P_{max} \frac{Mg + F_{pin}(p_o)}{2I_{max} K_{p1} K_{lv} K_f} \quad (15)$$

where I_{max} is the maximum motor current and P_{max} is the full stroke of the lens module. It can be seen that the parameters P_{max} , I_{max} and K_f are fixed by motor specification, and K_{lv} is restricted by audible noise consideration, thus K_{p1} is the major parameter designed to reduce steady state position error. However, rising K_{p1} may introduce system oscillation, thus K_{p2} is designed to make the position response have more following error to prevent system from oscillation. The following error e_{fv} under this variable position loop gain is

$$e_{fv} = e_{f2} - \frac{\varepsilon}{e_{f1}} (e_{f2} - e_{f1}) \quad (16)$$

where e_{f1} and e_{f2} are the following error under constant position loop gain K_{p1} and K_{p2} , respectively. Thus ε can be decided by the specified e_{fv} .

V. HARDWARE REALIZATION AND EXPERIMENTAL RESULTS

Fig. 7 shows the interface and functional block diagram of the constructed servo control IC for the VCM lens module. This IC allows user to set the controller parameter according to the load (camera lens) and response time. All modules including current controller, position filter, position controller, velocity estimator and velocity controller share the same ALU unit by time-sharing scheme for arithmetic operation. The time-sharing scheme is mixed with 40 kHz and 200 kHz sampling frequency as shown in Fig. .

Fig. 9 shows the detail block diagram of the ALU unit, which supports both of add, minus and single cycle operation for arithmetic operation $a \times b + c$.

The validation of the ASIC implementation is simulated by NCVerilog. Fig. 10(a) shows the simulation result of position and current response. The VCM is in position in 15 ms and no stick-slip limit cycle oscillation occurs. Unipolar voltage switching scheme is applied to the full-bridge converter. The last two rows of wave form in Fig. 10(b) are the PWM signal of the two upper transistors respectively. It shows that the switches work as desired.

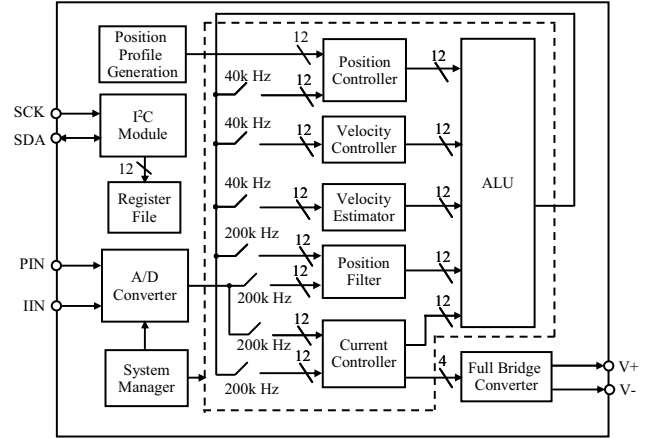


Fig. 7. Pin assignment of the constructed servo control IC.

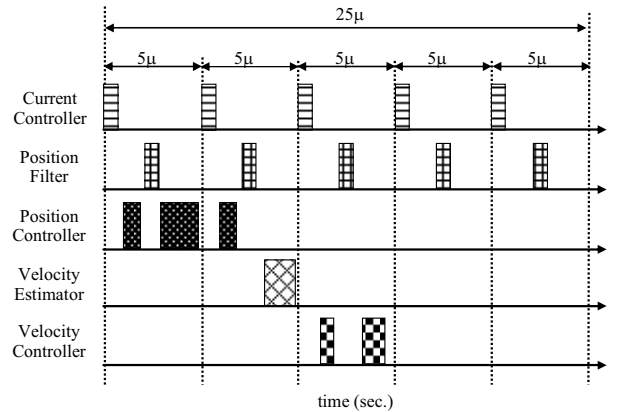


Fig. . Timing analysis of the ALU sharing effort.

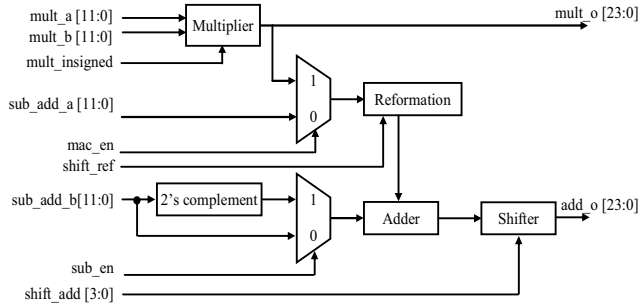


Fig. 9. Block diagram of the ALU.

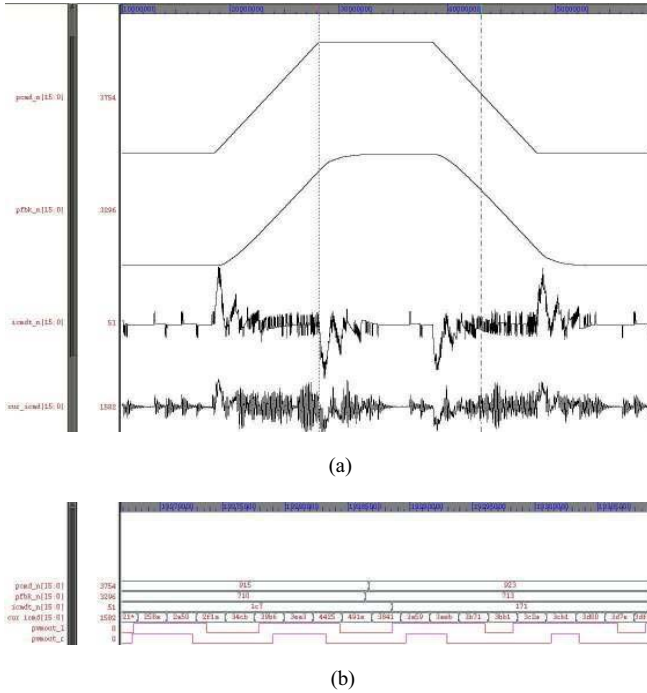


Fig. 10. Simulation results of the developed servo control IC. (a) Position and current response. (b) PWM signal of the two upper transistors.

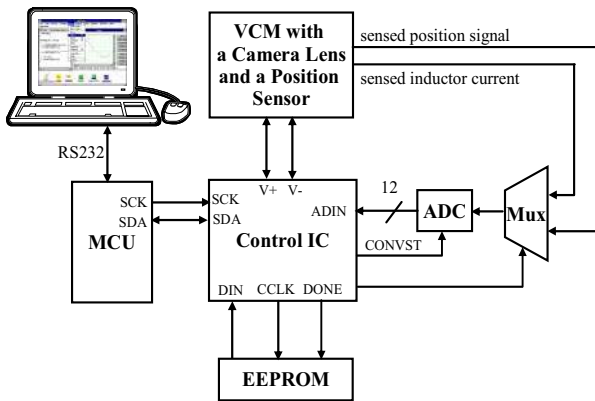
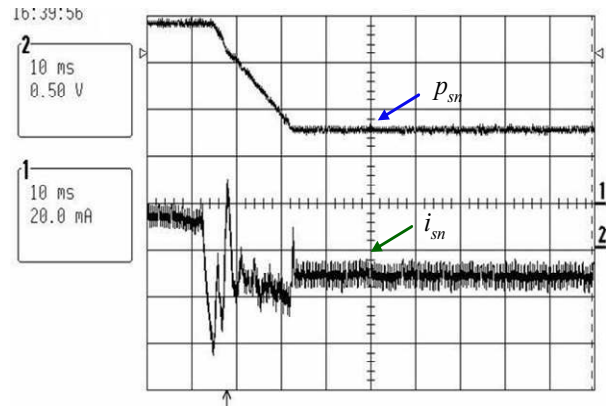
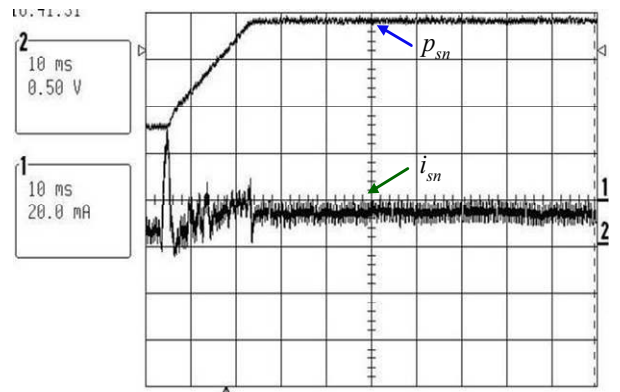


Fig. 11. Hardware architecture of the experimental setting.



(a)



(b)

Fig. 12. Experimental results of the position and current response. (a) The lens module moves from top to bottom; (b) The lens module moves from bottom to top.

Fig. 11 illustrates the hardware architecture of the experimental system constructed in this paper. The developed servo control IC works as a coprocessor with a single-chip MCU (AT 9C51RD2) from ATMEL. The MCU serves as a host processor and governs the work of tuning control parameters. The developed servo control IC receives sensed position signal and inductor current from the lens module. The feedback signals are multiplexed before feeding into the 12-bit AD converter. The position sensor maps the physical position 0-0.6 mm to 1.2-2.5 V, and the motor current is between ± 120 mA. The converted digital data is then sent to FPGA for further processing.

Fig. 12 illustrates the position responses to ramping commands of back-and-forth of the designed digital servo drive IC with the VCM for auto-focusing module. The traverse distance is 0.5 mm with a feed rate of $16.7 \mu\text{m}/\text{ms}$ and can achieve a position control resolution of $5 \mu\text{m}$. Experimental results verify the designed digital servo drive IC can meet the design specifications of an auto-focus module used for high-performance slim-type digital camera applications.

VI. CONCLUSION

In this paper, an FPGA-based servo control IC for the closed-loop control of VCM used in an auto-focus module of a mobile phone is developed. This paper also proposed a fully digital cascaded loops control scheme. The inner current loop reduces disturbances such as change of electrical impedance and back EMF. Also, the dead time effect is reduced and the current waveform distortion can be lowered by increasing the current loop gain. The velocity loop compensates the mechanical pole and fastens position response. The outer position loop is designed to meet the specifications such as response time and steady-state error. In the hardware realization, by using time-sharing scheme, the ALU is shared to each component of the controller to save resources. The simulation and experimental results show that the position response time meets the specification and no stick-slip limit cycle oscillation occur, thus the solution is realizable.

REFERENCES

- [1] Sung-Min Sohn, Sung-Hyun Yang, Sang-Wook Kim, Kug-Hyun Baek, and Woo-Hyun Paik, "Soc Design of An Auto-Focus Driving Image Signal Processor for Mobile Camera Applications," *IEEE Trans. Consumer Electronics*, vol. 52, Issue 1, pp. 10-16, Feb., 2006.
- [2] Craig T. Johnson and Robert D. Lorenz, "Experimental identification of friction and its compensation in precise, position controlled mechanisms," *IEEE Trans. Industry Applications*, vol. 28, Issue 6, pp. 1392-1399, Nov./Dec. 1992.
- [3] Liyu Cao and H. M. Schwartz, "Stick-slip friction compensation for PID position control," *American Control Conference*, vol. 2, pp. 107 - 110, June 2000.
- [4] George Ellis, *Control System Design Guide*, 2nd ed., Academic Press, pp. 234-237, 2000.
- [5] Xiaoyin Shao and Dong Sun, "Development of an FPGA-Based Motion Control ASIC for Robotic Manipulators," *Intelligent Control and Automation*, vol. 2, pp. 221- 225, June 2006.