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Chao Sung Lai, Woei Cheng Wu, Tien Sheng Chao, Jian Hao Chen, Jer Chyi Wang, Li-Lin Tay, and Nelson Rowell

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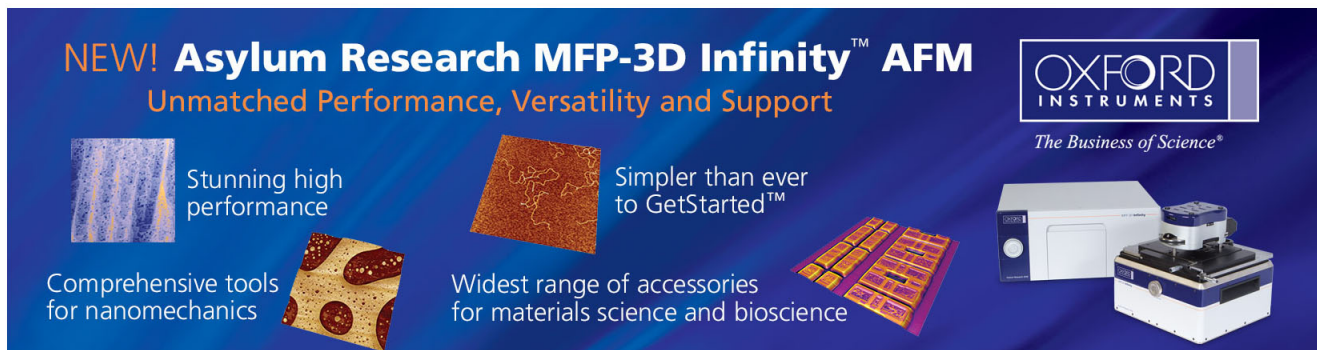
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## Suppression of interfacial reaction for HfO<sub>2</sub> on silicon by pre-CF<sub>4</sub> plasma treatment

Chao Sung Lai<sup>a)</sup>

*Department of Electronic Engineering, Chang Gung University, 259 Wen-Hwa 1st Road, Kwei-Shan, Tao-Yuan 333, Taiwan*

Woei Cherng Wu and Tien Sheng Chao

*Department of Electronic Physics, National Chiao Tung University, 1001 Ta Hsueh Rd., Hsinchu 300, Taiwan*

Jian Hao Chen

*Department of Electronics Engineering, National Chiao Tung University, 1001 Ta Hsueh Rd., Hsinchu 300, Taiwan*

Jer Chyi Wang

*Nanya Technology Corporation, Hwa-Ya Technology Park, 669 Fu-Hsing 3rd Rd., Kueishan, Taoyuan 338, Taiwan*

Li-Lin Tay and Nelson Rowell

*Institute for Microstructural Sciences, National Research Council of Canada, 1200 Montreal Road, Ottawa, Ontario K1A 0R6, Canada*

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In this letter, the effects of pre-CF<sub>4</sub> plasma treatment on Si for sputtered HfO<sub>2</sub> gate dielectrics are investigated. The significant fluorine was incorporated at the HfO<sub>2</sub>/Si substrate interface for a sample with the CF<sub>4</sub> plasma pretreatment. The Hf silicide was suppressed and Hf-F bonding was observed for the CF<sub>4</sub> plasma pretreated sample. Compared with the as-deposited sample, the effective oxide thickness was much reduced for the pre-CF<sub>4</sub> plasma treated sample due to the elimination of the interfacial layer between HfO<sub>2</sub> and Si substrate. These improved characteristics of the HfO<sub>2</sub> gate dielectrics can be explained in terms of the fluorine atoms blocking oxygen diffusion through the HfO<sub>2</sub> film into the Si substrate. © 2006 American Institute of Physics. [DOI: 10.1063/1.2337002]

Hafnium oxide gate dielectrics are considered to be the most promising candidates to meet the future ultralarge-scale-integration (ULSI) technology.<sup>1,2</sup> However, it is still a big challenge to introduce the high-*k* materials into the complementary metal oxide semiconductor process, especially because of difficulties in controlling the film thickness. During the film growth and postprocessing, the formation of an interfacial layer (IL) is likely and limits the reduction of the effective oxide thickness (EOT).<sup>3,4</sup> The reasons for this unwanted layer are the presence of excess oxygen during the film growth that initially oxidizes the Si surface<sup>5</sup> and Si diffusion into the film producing a silicate layer.<sup>6</sup> Therefore, methods such as cosputtering of silicon and aluminum with hafnium to deposit hafnium silicate and aluminate dielectrics<sup>7,8</sup> and the use of nitric gas for chemical vapor deposition<sup>9</sup> or oxidizing sputtered metal nitride such as HfN to form hafnium oxynitride (HfON) films<sup>10</sup> are used to improve dielectric quality. However, the nitridation technique induces positive interface charges<sup>11</sup> leading to higher hysteresis and lower channel mobility. In this work, a CF<sub>4</sub> plasma pretreatment approach is proposed which improves the interface between the HfO<sub>2</sub> gate dielectric and the Si substrate. The transmission electron microscopy (TEM) and Fourier transform infrared (FTIR) spectroscopy were employed and it was found that the growth of the interfacial layer had been inhibited by fluorine passivation of the silicon surface for

oxidation and by the blocking of oxygen diffusion into the silicon. It was also observed that the capacitance EOT was much decreased for the CF<sub>4</sub> plasma pretreated sample.

A standard RCA clean was performed on all samples in the beginning. Then, some samples were treated by CF<sub>4</sub> plasma for 1 min in a plasma enhanced chemical vapor deposition system. Further samples not subjected to the plasma treatment but otherwise identical were the as-deposited samples. In order to analyze the surface of the all samples, attenuated total reflection FTIR spectroscopy was used to inspect the variation of the native oxide formation on the silicon substrate. Then HfO<sub>2</sub> films with three different thicknesses of 5, 7, and 9 nm were deposited by reactive rf sputtering. Finally, a TaN metal gate of 50 nm was also deposited by rf sputtering and aluminum films of 300 nm were evaporated on both the top and bottom of the silicon wafer to form metal oxide semiconductor capacitors. High frequency (100 kHz) capacitance-voltage (*C-V*) characteristics were measured with an HP4284A analyzer. The physical thicknesses of HfO<sub>2</sub> thin film and the interfacial layer were measured with TEM. In addition, the fluorine distribution was obtained with the secondary ion mass spectroscopy (SIMS), and the Hf-O, Hf-Si, and Hf-F bondings were characterized by electron spectroscopy for chemical analysis (ESCA).

Figure 1 shows the SIMS analysis of the HfO<sub>2</sub> films with pre-CF<sub>4</sub> plasma treatment. It is apparent that fluorine atoms have accumulated mainly at the interface between the HfO<sub>2</sub> thin film and the silicon substrate after the CF<sub>4</sub> plasma pre-

<sup>a)</sup>Electronic mail: cslai@mail.cgu.edu.tw

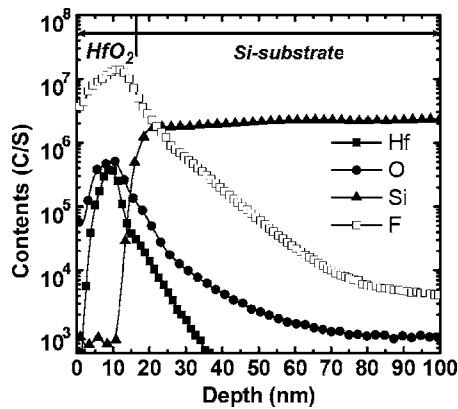


FIG. 1. SIMS depth profile of the  $\text{HfO}_2$  gate dielectrics. The fluorine atoms accumulated mainly at the  $\text{HfO}_2$ /silicon substrate interface after  $\text{CF}_4$  plasma pretreatment.

treatment. This observation indicated that fluorine atoms first are distributed at the surface of the silicon substrate after the  $\text{CF}_4$  plasma pretreatment, and are then incorporated into the  $\text{HfO}_2$  thin film during the hafnium dioxide deposition to form fluorinated  $\text{HfO}_2$  gate dielectrics. In addition, these fluorine atoms also terminate the dangling bonds of silicon substrate and accumulate at the interfacial layer region.<sup>12</sup>

Figure 2 shows the Hf 4f ESCA spectra of as-deposited and  $\text{CF}_4$  treated samples, respectively. A take-off angle (TOA) of  $90^\circ$  was used to measure the ESCA spectra. For the as-deposited sample, as shown in Fig. 2(a), two distinct peaks of the Hf–O bonding at 18.7 and 20.3 eV are clearly visible. In addition, Hf–Si bondings at 14.7 and 16.8 eV are also observed, meaning that Hf silicide was formed during the  $\text{HfO}_2$  film deposition. Fortunately, this formation of Hf–Si bonding was effectively suppressed for the  $\text{CF}_4$  plasma pretreated samples as shown in Fig. 2(b). This observation is a clear indication that the fluorine atoms accumulated at the Si/ $\text{HfO}_2$  interface were responsible for the reduction of the amount of Si participating in Hf silicide formation.<sup>13</sup> From the inset of Fig. 2, we can see that the fluorine atoms were only incorporated into the  $\text{HfO}_2$  thin

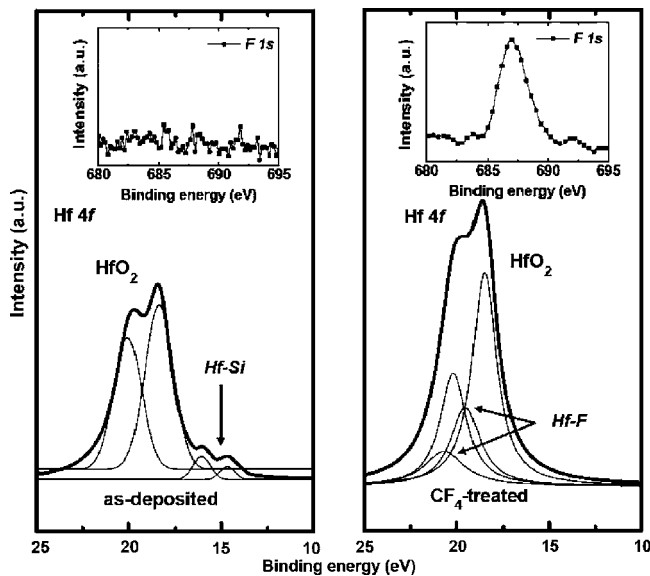


FIG. 2. Hf 4f ESCA spectra of (a) as-deposited sample and (b)  $\text{CF}_4$  plasma treated sample. The inset figures are the fluorine 1s ESCA spectra. A take-off angle (TOA) of  $90^\circ$  was used to measure the ESCA spectra.

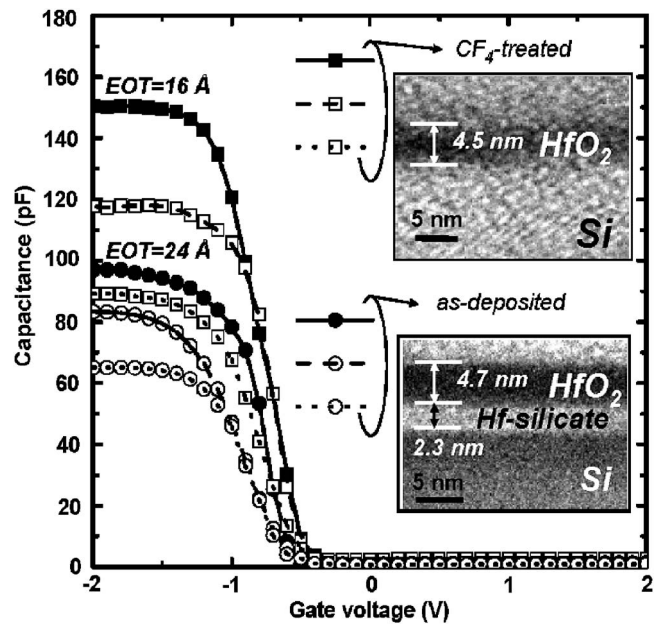


FIG. 3.  $C$ - $V$  characteristics for the samples with and without  $\text{CF}_4$  plasma pretreatment for various  $\text{HfO}_2$  thin film thicknesses. The inset figures are the TEM images for the as-deposited and  $\text{CF}_4$  plasma pretreated samples.

film when  $\text{CF}_4$  plasma pretreatment was employed. Furthermore, the intensity of the Hf 4f spectra of  $\text{HfO}_2$  dielectrics after  $\text{CF}_4$  plasma pretreatment was much larger although the original peak value at 20.3 eV was less distinct than for the as-deposited sample. This indicates that the Hf–F bonds were formed.<sup>14</sup> It is induced by the fluorine incorporation into the  $\text{HfO}_2$  thin film after  $\text{CF}_4$  plasma pretreatment, as shown in the SIMS profiles in Fig. 1 and the inset in Fig. 2.

Figure 3 presents the capacitance-voltage ( $C$ - $V$ ) characteristics of  $\text{HfO}_2$  gate dielectrics with and without the  $\text{CF}_4$  plasma pretreatment. The EOTs of as-deposited samples were 2.4, 2.9, and 3.8 nm, respectively. As discussed before, for the sample without  $\text{CF}_4$  plasma pretreatment, the sputtered  $\text{HfO}_2$  thin films tended to have ILs at the  $\text{HfO}_2$ /Si interfaces that have relatively small dielectric constants<sup>5,6</sup> as shown in the inset TEM image [Fig. 3(b)]. The composition of the interfacial layer is believed to be hafnium silicate because the estimated dielectric constant of the interfacial layer is higher than that of  $\text{SiO}_2$ . After the  $\text{CF}_4$  plasma pretreatment, the IL was effectively suppressed as shown in Fig. 3(a). As a result, the EOTs decreased from 3.8 to 2.9, 2.9 to 2.0, and 2.4 to 1.6 nm, respectively. Figure 4 shows the FTIR absorbance spectra of the Si wafer with and without  $\text{CF}_4$  plasma treatment. The FTIR method employed in this work utilizes a high-index Ge hemisphere in intimate contact with the samples. As depicted in Fig. 4, there is a dominant band at  $939\text{ cm}^{-1}$ , which is the GeO reference peak associated with the hemisphere. In addition, the broad features that appear at  $1100\text{ cm}^{-1}$  are the characteristic bulk interstitial Si–O–Si vibrations. For the sample without  $\text{CF}_4$  plasma treatment, one observes a strong and sharp band at  $1221\text{ cm}^{-1}$  from the Si– $\text{O}_x$  surface layer on Si. The peak is very typical of the native oxide on silicon. After  $\text{CF}_4$  plasma treatment, the native oxide band at  $1221\text{ cm}^{-1}$  has disappeared and been replaced by a significantly weaker absorption band centered near  $1180\text{ cm}^{-1}$  which is similar to that observed for amorphous  $\text{SiO}_2$  films on Si.<sup>15</sup> It is believed that the reduction of native oxide regrowth on  $\text{CF}_4$  plasma

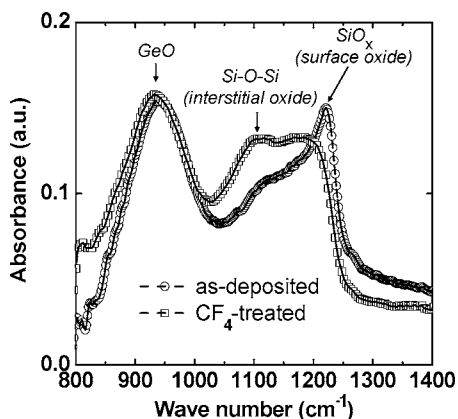


FIG. 4. FTIR spectra of the Si wafers with and without the  $\text{CF}_4$  plasma treatment. Whereas the as-deposited sample showed a characteristic native oxide band at  $1221\text{ cm}^{-1}$ , the  $\text{CF}_4$  plasma treated sample did not. The latter sample had a broader weaker oxide feature at  $1180\text{ cm}^{-1}$  indicating whatever  $\text{Si-O}_x$  material was present was more amorphous and with lower surface coverage than in the as-deposited sample.

treated Si substrates resulted from the fluorine passivation of the silicon surface. From these results, it seems reasonable that for the as-deposited sample, the excess oxidizing species such as oxygen radicals, ions, and molecules in the plasma diffuse into the silicon substrate and contribute to the interfacial layer growth. On the other hand, for the  $\text{CF}_4$  plasma treated  $\text{HfO}_2$  gate dielectrics, the growth of interfacial layer is inhibited by fluorine passivation of the Si substrate and the blocking of oxygen diffusion into the Si. This hypothesis is supported by both the TEM imaging and FTIR spectroscopy.

In summary, the characteristics of  $\text{CF}_4$  plasma pretreated  $\text{HfO}_2$  gate dielectrics were investigated. After the  $\text{CF}_4$  plasma pretreatment, the fluorine atoms were distributed at the interface between the  $\text{HfO}_2$  thin film and silicon substrate, effectively inhibiting the formation of an interfacial layer between the  $\text{HfO}_2$  thin film and Si substrate. The fluo-

rine passivation also plays a role in blocking oxygen diffusion into the Si, resulting in an EOT reduction for the  $\text{HfO}_2$  gate dielectrics. The  $\text{CF}_4$  plasma pretreatment technology can be used in device fabrication with high- $k$  gate dielectrics for future ULSI applications.

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