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Citation: Journal of Vacuum Science & Technology B **24**, 308 (2006); doi: 10.1116/1.2163894 View online: http://dx.doi.org/10.1116/1.2163894 View Table of Contents: http://scitation.aip.org/content/avs/journal/jvstb/24/1?ver=pdfcov Published by the AVS: Science & Technology of Materials, Interfaces, and Processing

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Morphology control of silicon nanotips fabricated by electron cyclotron resonance plasma etching

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(Received 9 May 2005; accepted 12 December 2005; published 20 January 2006)

Formation of well-aligned silicon nanotips etched monolithically from a silicon substrate has been demonstrated. The effect of the process temperature on the physicochemical etching of silicon and subsequent fabrication of these nanotips has been investigated. 2.2- μ m-long nanotips were formed at the process temperature of 250 °C and then decreased in length with increasing process temperature. Above 800 °C, the formation of the silicon nanotips was inhibited. Spectroscopic evidence attributes this fact to the efficient formation of silicon carbide thin film at higher process temperatures, instead of discontinuous nanomasks at lower process temperatures that prevent etching of the substrate. Another reason for this inhibited formation of nanotips is the reduced etching rate of the silicon by agents such as atomic H at higher process temperatures. © 2006 American Vacuum Society. [DOI: 10.1116/1.2163894]

I. INTRODUCTION

Among the widely published nanotubes, nanorods, and nanowires there have also been several reports on one dimensional (1D) conical structures. Starting from metallic,¹ silicon,² and diamond³ nanotips, a wide variety of conical, needlelike structures has been reported in recent years. These include silicon nanotip,² nanocone,⁴ and nanoneedle,⁵ carbon nanotips,⁶ ZnO nanoneedle arrays,⁷ AIN nanotips,⁸ and so forth. The sharp apex angle and relatively high aspect ratio make the nanotips promising for many applications. The fascinating structures of 1D nanomaterials and their unique properties are opening up applications in nanodevices,⁹ such as field-emission displays,¹⁰ laser diodes,¹¹ scanning probe microscopy,¹² solar cells,¹³ biosensors,¹⁴ and so on. In our previous work,¹⁵ we have demonstrated a self-

In our previous work,¹⁵ we have demonstrated a selfmasked dry-etching technique for fabricating silicon (Si) nanotips in one step. These Si nanotips exhibited a low turn-on field during field-emission measurements and inherent Si process compatibility.¹⁶ Such tips have also been used in solar cells,¹⁷ imprint lithography,¹⁸ and molecular sensing.^{19,20} However, the control of tip morphology is crucial for such wide-ranging applications. This letter reports the influence of process temperature on the mechanism of a self-masked dry-etching technique of Si, giving rise to the nanotip morphology.

II. EXPERIMENT

The processes were carried out in an electron cyclotron resonance (ECR) plasma reactor using a gas mixture of silane (SiH₄), methane (CH₄), argon (Ar), and hydrogen (H₂) as dry-etching gases. The details of the process can be found elsewhere.^{15,16} The starting substrates were boron (B)-doped *p*-type silicon (100) wafers with a resistivity of 1–10 Ω cm. In this study, a set of experiments were performed with various process temperatures ranging from 250 to 850 °C by keeping a constant flow rate ratio of SiH₄/CH₄/Ar/H₂ =0.2:2:5:8, a total pressure of 5.8 mTorr, and a power of 1200 W. The morphology of the silicon nanotips has been imaged with a JEOL 6700 field-emission scanning electron microscope (SEM). The x-ray photoelectron spectroscopy (XPS) was carried out in a PHI 1600 system. The Fourier transform infrared (FTIR) spectra were obtained with a Bomem (Hartmann-Braun) MB series FTIR spectrometer in the reflection mode. The resolution of the spectrum was 4 cm⁻¹.

III. RESULTS AND DISCUSSION

Figure 1(a) shows a typical cross-sectional SEM image of a well-aligned Si nanotip array fabricated at 300 °C with a uniform distribution and a high density of $\sim 3 \times 10^{10}$ /cm². Figures 1(b)-1(d) show cross-sectional SEM images of nanotip arrays grown at 400, 600, and 800 °C, respectively. A typical high-resolution transmission electron microscopy (HRTEM) image of a Si nanotip indicates that all of these nanotips were capped at the apex with SiC nanomasks.¹⁶ We observed that the length and density of the nanotips decreased with increasing process temperature. At process temperatures exceeding 800 °C, for example, 850 °C, there was hardly any nanotip visible. Instead a thick-film-like morphology was observed. Figure 2 shows a plot of the nanotip length as a function of the process temperature. From 2200 nm at a process temperature of 250 °C, the length of these nanotips decreased to less than 300 nm at a process

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FIG. 1. Cross-sectional SEM images of Si nanotips prepared under process temperatures of (a) 300, (b) 400, (c) 600, and (d) 800 °C.



FIG. 3. FTIR spectra of the Si nanotips grown at different process temperatures (marked on each plot).

temperature of 600 $^{\circ}$ C and the formation of the nanotips were totally inhibited at temperatures above 800 $^{\circ}$ C.

FTIR spectroscopy was done to determine the bonding characterization of these nanotips as the process temperature was varied. As shown in the FTIR spectra (Fig. 3), the samples fabricated at low process temperatures did not exhibit any significant SiC signal until the process temperature exceeded 700 °C. A peak evolved at 800 cm⁻¹ corresponding to the 3C–SiC (TO) mode.²¹

The weak peak located at approximately 600 cm⁻¹ was due to the diffusion of excess carbon into the Si substrate during process.²² XPS results corroborated the FTIR findings, qualitatively, by showing that the carbon content of the surface of the samples produced at 800 °C was much higher than those produced at 250 °C [Fig. 4(a)], whereas the silicon content at the surface was much lower for the sample fabricated at 800 °C [Fig. 4(b)].

At lower process temperatures, well-separated nanomasks of SiC may have existed on the Si substrate but remained undetectable due to minimal volume. Such SiC nanomasks, being harder to etch than silicon, however, still prevented



FIG. 2. Length of Si nanotips as a function of process temperature.

JVST B - Microelectronics and Nanometer Structures

uniform etching of the silicon substrate and gave rise to the nanotips. As the process temperature was increased, methane dissociation became more efficient in the vicinity of the silicon substrate and, instead of well-separated SiC nanomasks, a continuous layer of SiC or a carbon-rich SiC layer was formed. The formation of a SiC layer decreased the etching rate of silicon and finally inhibited further lengthening of nanotips. We estimated that the rate constants for dissocia-



FIG. 4. (a) XPS C (1s) spectra of Si nanotip samples prepared at (—) 250 and (----) 800 °C. (b) XPS Si (2p) spectra of Si nanotip samples prepared at (—) 250 and (----) 800 °C.

tion of CH₄ were enhanced by two orders of magnitude from 1×10^9 at 250 °C to 1×10^{11} at 700 °C,²³ whereas the rate constants for the dissociation of SiH₄ increased from 1 $\times 10^{12}$ at 250 °C to 3×10^{12} at 700 °C. Moreover, at a process temperature of 700 °C, the dissociation rate constant for CH₄ is only one order smaller than that of SiH₄, which would predict an efficient pathway for the formation of SiC. In a plasma system the addition of SiH₄ can greatly enhance the deposition of SiC.²⁴ As SiH₄ can be dissociated into SiH₂ and SiH₃ radicals, CH₄ can similarly be dissociated to CH₃ or CH₂ radicals with the release of H through thermal dissociation or electron or Ar* (excited Ar) impact dissociation in the ECR plasma. These radicals can react via a series of neutral-neutral reactions to result in radicals such as C₂H₂.²⁵ The silyl radicals can then react with C₂H₂ or CH₃ following a series of H abstraction to form SiC.²⁶

For the surface-etching process, we apply the ion-assisted dry-etching theory. From our previous investigation, only physicochemical sputtering, instead of pure physical sputtering,²⁷ can contribute to a significant change in morphology when applied to different materials as substrates.¹⁵ Therefore, ion-assisted chemical etching must occur through hydrogen atoms as etchant, for instance. Several other species are also involved in this etching, such as H, H⁺, H⁺₂, H⁺₃, Ar⁺, Ar⁺₂, and ArH⁺. It is known that ion bombardment can activate the surface, changing the surface reaction rates and mobility of reactive species. In the ECR plasma, the surface is bombarded by heavy ions causing a sputtering effect or molecular desorption from the Si substrate.

One can visualize the process as follows: First, H radicals are absorbed on the Si surface to form a layer of SiH. The formation of SiH₂, SiH₃, and subsequent gas-phase SiH₄ follows through reactions with H₃⁺ and ArH⁺ ions and reduction by H atoms. This mechanism becomes less probable due to the low sticking coefficients of these radicals on the Si substrates at higher temperatures, leading to a decrease of etching rate which is in accordance with our observation. Furthermore, Strass et al.²⁸ reported that there are two temperature regimes of Arrhenius dependence with different activation energies occurring in the hydrogen etching process. For temperatures above 300 °C the activation energy is -21 kcal/mol and that below 300 °C is only -1.7 kcal/mol. This leads to a decrease in etching rate when temperature is beyond 300 °C. Assuming the growth rate (R), in nm/h, of the nanotips to be proportional to the rate of any chemical reaction, including etching, taking place, we can plot the $\ln(R)$ as a function of inverse process temperature. This plot (Fig. 5) also reveals two activation energies of -134.2 and -19.9 kJ/mol above and below 600 °C, respectively. The difference in the activation energies in this case as against the values reported in Ref. 28 may have been caused by the fact that we are etching a partially masked (with SiC) silicon substrate, whereas Strass et al.²⁸ were etching silicon or silicon dioxide samples. In other words, the presence of both silane and methane will increase and decrease, respectively, the activation energies by forming the nanomasks and producing more etchants in the form of reactive H species.



FIG. 5. Arrhenius plot of the etching rate as a function of inverse process temperature.

From Fig. 5, it is clear that our samples with process temperatures above 600 °C were much shorter than those produced below 600 °C. The experimental evidence of reduced nanotip length with increasing process temperature, along with the FTIR and XPS data put forward to support the formation of a continuous SiC film at higher process temperatures, clearly establishes the role of the SiC nanomasks in the formation of these Si nanotips and elucidates the dry-etching mechanism in Si.

IV. CONCLUSIONS

In summary, a temperature-controlled plasma etching of silicon substrates producing monolithic silicon nanotips has been described. The silicon nanotip originates from the non-uniform etching of the silicon substrate protected by SiC nanomasks in the reactive microwave plasma. The length of the Si nanotips decreased with increasing process temperature, and beyond 800 °C, the nanotip formation was inhibited. The observed length reduction and ultimate extinction of the nanotips with increasing process temperature are due to the growth of a continuous SiC film at high temperatures preventing effective etching. On the other hand, a low sticking coefficient of the hydrogen-related species reduced the effective chemical etching rate at higher process temperatures.

ACKNOWLEDGMENTS

The work was carried out with financial assistance from National Science Council, Ministry of Education, Taiwan, Air Force Office of Scientific Research, USA, and Asian Office of Aerospace Research and Development. One of the authors (S.C.) would like to acknowledge a fellowship from the Institute of Atomic and Molecular Science, Academia Sinica, Taiwan.

This paper was presented at the First International Workshop on One-Dimensional Materials, January 10–14, 2005, National Taiwan University, Taipei, Taiwan.

311

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