Interface Study of Metal-Semiconductor Systems

by

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Interface effect and carrier transport in metal-semi-systems have been studied theoretically and experially to give a generalized and quantitative presentation. The
monic and tunneling processes have been analyzed in terms
most accurate quantum transmission coefficients. The efets of image-force lowering, interface states, temperature, and
mo-dimensional statistical variation of impurity concentration
are also been incorporated in the study.

The ohmic or rectifying behavior of a given metal-semicon-box system depends strongly on the temperature and doping contration. The saturation current density $J_{\rm S}$ is found to reach minimum at a particular doping, e.g. for PtSi-Si system at 300 K ith a barrier height of 0.85eV, $J_{\rm S}$ is 80 na/cm² at 10^{14} cm⁻³, reaches a minimum of 60 na/cm² at 10^{16} cm⁻³, then rapidly increases to 10^3 amp/cm² at 10^{20} cm⁻³. At high doping range the average saturation current density will be considerably increased due to the effect of two-dimensional impurity variation.

The room-temperature transition doping for breakdown in metal-silicon system occurs at 8 x 10¹⁷ cm⁻³; for lower dopings the breakdown is due to avalanche multiplication, and for higher dopings due to tunneling of carriers from metal Fermi level to semiconductor band edges.

The metal-silicon diodes are fabricated by planar technology with guard-ring structures to eliminate edge effects. Extensive experimetal studies including current-voltage, and photoelectric messurements covering doping range from 10¹⁴ to 10²⁰ cm-³ and

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temperature range from 77°K to 373°K give good agreement with theoretical predictions. Differentiation measurement at 4.2°K also reveals scatterings of phonons with interface states and phonon-phonon interaction between different valleys along the <100 axis.

I. Introduction

Metal-semiconductor diode is one of the most important solidstate devices. It behaves similarly to a one-sided abrupt p-n junction, and yet it can be operated as a majority-carrier device with inherent fast response and radiation resistances. It has been extensively used in communication systems and digital computers. It has also shown its potential as useful microwave generator e.g. metal-semiconductor IMPATT diode (1), optoelectronic devices (e.g. Schottky barrier avalanche photodiode (2), and field - effect devices (e.g. Schottky-barrier-gate FET (3), and Schottky-contact IGFET (4).

The investigation of metal-semiconductor systems can be dated back to 1874 when Braun (5) first noted the dependence of the total resistance on the applied voltage. In 1938 Schottky (6) suggested the formation of surface barriers between metal and semiconductor contacts. In 1942 the basic thermionic emission theory was proposed by Bethe (7). The analysis of rectifying contacts and experimental work prior to 1957 were summarized by Henisch (8). Recent theoretical and experimental developments have been reviewed by Sze (9).

In the analysis of carrier transport in a metal-semiconductor system, both the diffusion theory by Schottky [6] and the thermionic emission theory by Bethe (7) have neglected the contribution of the tunneling current. Recently Padovani and Stratton (10) considered the tunneling component in terms of a field-emission model and obtained results for low-temperature condition. Crowell and Rideout (11) also considered the transport processes based upon the Boltzmann distribution and WKB approximation for the quantum transmission coefficient. In the above approaches [11] a combined parameter (T/\sqrt{N}) is used where T is the temperature and N is the doping concentration of the semiconductor; and their results are applicable for the case of low doping and moderately high temperatures. Crowell and Sze (12) proposed a thermionic emission-diffusion theory which gave a synthesis of Schottky's diffusion theory and Bethe's thermionic emission theory; result is applicable for low and moderete doping cases.

The present study gives a generalized approach which covers for the first time the complete spectrum of temperature from low

to high ranges and impurity concentration from weakly doped to degenerate cases. The present study differs from the previous approaches in the following aspects: (1) an accurate quantum transmission function based upon numerical computation is used for the complete energy range, (2) the Fermi-Dirac distribution function is used to take into account the degenerate cases, (3) the temperature and the doping concentration are considered as two independent parameters since the combined parameter T/\sqrt{N} is used to limited ranges, and (4) a two-dimensional statistical variation of impurities is incorperated in the transport study.

The detailed transport analysis is presented in Sec. II which studies of tunneling breakdown, ohmic behavior under condition, electron-phonon scattering, and interface the experimental results including differentiation ment of interface states are presented in Sec. III. Discustant conclusion are given in Sec. IV.

∏. Basic Transport Analysis

We shall consider in this section (1) the quantum transmission coefficient, (2) the carrier transport equation, (3) the two-dimensional impurity variation, and (4) the breakdown voltage as a function of doping and temperature.

The basic energy-band digram of a metal-n-type semiconductor system is shown in Fig. 1. The basic set of equations for the system can be given as follows (9):

$$\phi_{Bn} = V_{bo} + V_{n} - \triangle \phi \tag{1}$$

$$E_{\rm m} = \sqrt{2qN \left(V_{\rm bo} - V - kT/\varepsilon_{\rm s}\right)}$$
 (2)

$$W = \sqrt{\frac{2 \varepsilon_{B}}{q N} (V_{bo} - V - \frac{kT}{q})}$$
 (3)

$$\triangle \phi = \sqrt{\frac{qE_m}{4\pi \varepsilon_o}} \tag{4}$$

and

$$1/c^2 = 2(V_{bo} - V - \frac{kT}{q})/(q\varepsilon_s N)$$
 (5)

where $\phi_{\rm Bm}$ is the barrier height, $V_{\rm bo}$ the built-in potential at zero bias, $V_{\rm n}$ the potential difference between the Fermi energy and the bottom of conduction band, $E_{\rm m}$ the maximum electric field, N the doping concentration, V the applied voltage, W the depletion width, and C the differential capacitance per unit area.

1. Quantum Transmission Coefficient

The exact transmission coefficient can be obtained by solving the Schrodinger equation for the potential profile of a metal-semiconductor system:

$$-\frac{k^2h^2}{2m^*} \psi + \left(\frac{-q^2N}{2\varepsilon_s} (Wx - \frac{x^2}{2}) - \frac{q^2}{16\pi\varepsilon_s x}\right) \psi = E\psi$$
 (6)

where the first two terms in the square bracket give the diffusion potential energy while the last term is the image force potential energy. We shall use the potential energy maximum $q \phi_{Bn}$ as a reference; and designate ζ as energy measured downward from the reference, and ζ as energy measured upward (see Fig. 1.) The transmission coefficients have been computed based on the Tayler expansion method (13). The results of the transmission coefficients are shown in Fig. 2. For electron energy less than that of the barrier maximum, the transmission function T(7) is a monotonically increasing function of electric field, while for electron energy in excess of the barrier maximum the transmission function, $T(\zeta)$ is a monotanically decreasing function of field, For electron energy exactly corresponds to the barrier maximum, or $\eta = \zeta = 0$, the transmission coefficient is not unity, but varies from 6% at 10^3 V/cm to 17% at 10^6 V/cm.

The exact transmission coefficient will be used in the transport analysis. The transmission coefficient shown in Fig.2 covers the energy range near the potential maximum where the WKB approximation can not be used. For energies far below the potential maximum where the potential varies slowly with distance, however the WKB approximation can adequately give the transmission coefficient. The WKB transmission coefficient incorporating the image force correction is given by

$$T(7) = \exp\left\{-R\left(V_{b}^{\frac{1}{2}}(\eta + \triangle \phi)^{\frac{1}{2}} - (V_{b} - q \triangle \phi) \ln \frac{(V_{b} - q \triangle \phi)^{\frac{1}{2}}}{(V_{b} - q \triangle \phi + \eta)^{\frac{1}{2}}}\right)\right\}$$

where

$$R = \sqrt{\frac{4m \cdot \varepsilon_*}{h^* N}} \tag{7a}$$

the diffusion barrier potential at the diffusion barrier potential at the work are measured in units of eV.

For electron energies in excess of the potential maximum the coefficient (as shown in Fig. 2) is fitted by numerical

a polynomial of order of ten.

The result is also extrapolated to $\eta=0$ in order to comparison with the exact transmission function: We note to copings higher than 10^{16} cm $^{-3}$ (i.e. for R \leq 1000eV $^{-1}$) reement is good, so that a match between the exact transfunction and the WKB result can be made at relatively transmission coefficient near the potential maximum. At dopings (i.e. R > 1000eV $^{-1}$) the agreement is poor hence the exact transmission function should be used until the transmission coefficient becomes vanishingly small.

2. Carrier Transport Equation

In order to compare the present theory with the previous approaches[10-12], we shall divide the transport processes into thermionic and tunneling components.

The current density equation of carriers traversing from the semiconductor to the metal is proportional to the transmission coefficient multiplied by the occupation probability in the semiconductor and the unoccupied probability in the metal, i.e.

$$J_{\text{SM}} = \frac{A*T}{k} \int_{0}^{V_{b} - \triangle \phi} F_{\text{s}}(V) T(n) (1 - F_{\text{m}}) dn$$

$$+ \frac{A*T}{k} \int_{0}^{\infty} T(\zeta) \exp{-\frac{(V_{b} + V_{n} + \zeta + \triangle \phi)}{kT}} d\zeta$$
(8)

where the first term corresponds to the tunneling component and the second term the thermionic component. $F_{\rm S}$ and $F_{\rm m}$ are the Fermi-Dirac distribution functions for the semiconductor and the metal respetively

$$F_{m} = \frac{1}{1 + \exp\left(\frac{V_{bo} - \eta + V_{n}}{kT}\right)}$$
 (9)

$$F_{s} = \frac{1}{1 + \exp\left(\frac{V_{bo} - \eta + V_{n} - V}{kT}\right)}$$
(10)

and A* is the effective Richardson constant, T the temperature, K the Boltzmann constant, and $T(\eta)$ and $T(\zeta)$ the transmission coefficients below and above the potential maximum respectively.

The calculated results of J_{SM} versus V are shown in Fig. 4. The experimental data are also presented but will be discussed in Sec. III.

The constants used in the calculation are: $A*=240~\rm{amp/cm^2}$ / OK, $m*=0.3~\rm{m_0}$, $\epsilon_s=11.8~\epsilon_o$ and $q\phi_{\rm Bn}=0.8\rm{eV}$. (for Au-nSi). At $300^{\rm O}$ K, the curve for $10^{16}\rm{cm^{-3}}$ is the lowest.

The current density at $10^{18} \rm cm^{-3}$ is about 20 times larger than that for $10^{16} \rm cm^{-3}$. At 77°K, the curve for $10^{16} \rm cm^{-3}$ is also the lowest one. The current density at $10^{18} \rm cm^{-3}$ is now more than 5000 times greate than that at $10^{16} \rm cm^{-3}$.

The current density equation of carriers traversing from the metal to the semiconductor is

$$J_{Ms} = \frac{A^*T}{k} \int_{0}^{V_b - \triangle \phi} F_m T(\eta) (1 - F_s) d\eta + \frac{A^*T}{k} e^{-\frac{q \phi_B}{kT}} \int_{0}^{\infty} T(\zeta) e^{-\zeta/k} d\zeta$$
(1)

The thermionic component in J_{MS} i.e., the second term in Eq. (9), is almost constant independent of voltage since the barrier height ϕ_B is essentially independent of voltage. However, due to the image-force lowering, this term will slightly increase when the reverse biase is increased.

At very high dopings and low temperatures, the tunneling component will dominate, and the current $J_{\mbox{MS}}$ will increase more rapidly with respect to the voltage.

The I-V characteristics at $300^{\rm o}{\rm K}$ under reverse biased condition are shown in Fig. 5 for dopings of $10^{14}{\rm cm}^{-3}$ to $10^{17}{\rm cm}^{-3}$. Experimental results are also presented which will be discussed

later.

The total current density is given by the difference of Eqs.(8) and (11):

(12)JO.E. = JSM-JMS

and the saturation current density can be obtained by setting V=0im Eq. (8) or (11):

$$J_{s} = \frac{A^{*}T}{k} \int_{0}^{V_{bo} - \triangle \phi} F_{m}T(\eta) (1 - F_{s} (V = 0)) d\eta$$

$$= \frac{A^{*}T}{k} \int_{0}^{\infty} T(\zeta) e^{-(V_{bo} + V_{n} + \zeta - \triangle \phi)/kT} d\zeta$$
(13)

The values of $J_{\rm S}$ versus N are shown in Fig. 6 for Au-n-type Si and PtSi-n-type Si systems (in which $q\phi_{\rm BO}=0.85{\rm eV}$).

The values of the saturation current density Js as a function of are shown in Fig. 6 for Au-Si and PtSi-Si systems. It is seesting to note the variation of J_s with doping. For example Au-Si system at 300°K, J_s is 4×10^{-7} amp/cm² for N= 1014 cm⁻³, reaches a minimum value of 3.6×10^{-7} amp/cm² at 1015 cm⁻³, then increases monotonically to 4×10^{-3} amp/cm² at 1019 cm⁻³ and 1.2×10^3 at 10^{20} cm⁻³.

The ratio of tunneling to thermionic current component is

$$S = \frac{J_{t}}{J_{th}} = \frac{\int_{0}^{V_{b} - \triangle \phi} T(\eta) (F_{s}(V) - F_{m}) d\eta}{\int_{0}^{\infty} T(\zeta) e^{-(V_{b} + \zeta + V_{n} - \triangle \phi)/kT} d\zeta}$$
(14)

A plot of S versus N is shown in Fig. 7. The value of S varies over 18 orders of magnitude for samples with doping of 1014cm-3 at 375°K to 1018 cm-3 at 77°K. Increasing of doping and decreasing of termperature will always increase the tunneling component.

From the slope of the current-voltage characteristics, one can

easily define a parameter n such as

$$n \equiv \frac{q}{kT} \frac{1}{(\frac{d \ln J}{dV})}$$

A plot of the parameter n is shown in Fig. 8. The value of n increases with doping concentration but decreases with temperature. For example for a doping of 10^{18} cm⁻³, the value of n is 1.08 at 300° K and increases to 1.57 at 77° K.

3. Two Dimensional Statistical Effect

Because of the statistical nature of impurity distribution in the sample, the measured saturation current density for a given average doping concentration will deviate from the ideal values obtainable from Eq. 13.

One can define a deviation width in doping by $\triangle\,N_O$ where \triangle is the deviation percentage, N_O the average doping concentration obtained from resistivity measurement. If one assumes a Gaussian distribution for the variation of impurity concentration, one obtains the elementary area dA as

$$dA = \frac{A}{\sqrt{\pi} \triangle N_o} \exp\left(-\left(\frac{N - N_o}{\triangle N_o}\right)^2\right) dN$$
(15)

Where A is the total device area.

The expectation value of the saturation current density is given by

$$J_{s} = \frac{1}{A} \int_{-\infty}^{\infty} J_{s}(N) dA = \int_{-\infty}^{\infty} \frac{1}{\sqrt{\pi} \triangle N_{o}} J_{s}(N) \exp\left(-\left(\frac{N - N_{o}}{\triangle N_{o}}\right)^{2}\right) dN \quad (16)$$

where $J_{\rm S}$ (N) is the variation of the saturation current with doping for the ideal case. The functional form of $J_{\rm S}$ (N) can be obtained from Fig. 6 (solid and dashed lines), by numerical mothod. The computed fesult for $J_{\rm S}$ versus N for Au-Si system at 300°K is also shown in Fig. 6 (dotted lines). When $\triangle=0.1$ the values of $\langle J_{\rm S} \rangle$ are essentially the same as that for the ideal case. When $\triangle=0.5$, however $\langle J_{\rm S} \rangle$ becomes 10% larger than the ideal $J_{\rm S}$ at 10^{18} cm⁻³ and is over 100% larger then the ideal $J_{\rm S}$ at 10^{19} cm⁻³

4. Breakdown Voltages

For Schottky barriers, the breakdown mechanism is due to avalanche multiplication for weakly doped samples and due to tunneling for heavily-doped samples. The avalanche breakdown is essentially the same as that for one-sided abrupt junctions and the breakdown voltage is given by

$$V_{B} = \frac{\varepsilon_{B}E_{m}^{2}}{2qN} - \phi_{Bn} - V_{n}$$
 (17)

where Em is the maximum field strength at breakdown which is slightly dependent on doping and temperature. [14,15] For heavely-

doped samples, the tunneling component will dominate the transport process. We shall define the breakdown voltage at which the reverse current density becomes $5000~J_{\rm smin}$. Where $J_{\rm smin}$ is the minimum saturation current density for the metal-semiconductor system e.g. for Au-Su system at 300°K Jsmin is 3.6 x 10-7

amp/cm2

The treoretical result of breakdown voltage for Schottky bar-There is shown in Fig. 9. For the avalanche multiplication process the breakdown voltage increases with increasing tempera - $N = 10^{16} \text{cm}^{-3}$. VB is 56 volts at 250°K and increases to 50 miles at 3500K. On the other hand, for tunneling process, the woltage decreases with increasing temperature, e.g. for Tale 10 18 cm-3, VB is about 0.7V at 300°K and decreases to 0.1V W 350°K

previous section we have implicitely assumed that the mean free path is infinite so that collisions in the deplelayer can be neglected. In this section we shall derive resport equations which incorporate the effect of carrier scatterings within the depletion layer.

III. Quantum Emission-Diffusion Theory and Related Scattering Processes

1. Quantum Emission-Diffusion Theory

In quantum emission-diffusion (QED) theory the following assumptions are made: (1) there is no interaction between the quantum emission and the diffusion mechanism, (2) the total current is determined by the combined conductance of the two series conductances resulting from the above two machanisms, (3) the diffusion mechanism occurs only within the range from the emitted band edge to the barrier minimum.

Based on the assumptions (1) and (2), one obtains for the total

current density:

$$J = \frac{J_{QE} J_D}{J_{QE} + J_D} \tag{18}$$

where J QE is the quantum-emission current, and JD is the diffusion current. The diffusion velocity can be obtained from assumption (3), and the result is similar to that given by Crowell and Sze: (12)

$$V_{D} = \left(\int_{X_{E}}^{W} \overline{D}^{-1} \exp\left(-qV(\chi)/kT\right) d\chi \right)$$
 (19)

where \bar{D} is the average diffusion coefficient, V (χ) the barrier potential, and χ_{E} as shown in Fig. 1 is the position at which maximum amount of carrier transport occurs. The χ_{E} is given as: (10)

$$\chi_{E} = W(1 - \cosh^{-1}(\frac{1}{RKT}))$$
(20)

According to Crowell and Sze, (12) the ratio of $J_{\rm QE}$ and $J_{\rm D}$ can be approximated by the ratio of the recombination velocity and the diffusion velocity, i.e. $J_{\rm QE}/J_{\rm D}$ = $V_{\rm R}/V_{\rm D}$. Hence the total current density becomes

$$J = \frac{J_{QE}}{1 + V_{R}/V_{D}} \tag{21}$$

where the recombination velocity is

$$V_{D} = \frac{A^*T^2}{qN_{C}} \tag{22}$$

It is obvious that when χ_E equals zero, i.e. at the potential barrier maximum, equation (18) will automatically reduce to the result of Crowell and Sze's thermionic emission-diffusion theory. However, when tunneling dominates, x_E will occur at the lower band edge. When RKT tends to zero (i.e. tunneling dominates) χ_E will tend to W and thus the diffusion mechanism becomes less important.

2. Electron-phonon Scattering

In an electron-phonon scattering event the conservation of wave vector should be held:

$$q = k' - k - g \tag{23}$$

where q is the phonon momentum, k the electron momentum before scattering, and k' the electron momentum after scattering. g is the reciporcal lattice vector. Inan electron-phonon scattering event there are two kinds of processes: (1) the N-process with g=0 which makes the scattering matrix element vanishes (16) for transverse wave thus only the longitudinal phonon scattering is possible, and (2) the U-process with $g \neq 0$, hence both the transverse and the longitudinal phonons are involved.

It is believed that when the period of a lattice vibration is much larger than the electron relaxation time, τ , then the scattering of electrons by phonons, giving rise to conductance (or resistance) peaks will occur. From this creterion (16) one obtains

$$T < K/kT$$
. (24)

At low temperatures this condition is fulfilled. At extremely low temperature, e.g. at liquid helium temperature (see Sec. IV - 4), excitation of phonons can be made by biasing or neutron diffraction [17, 18].

In order to observe the excited phonons, measurements of dif-

ferential conductance as well as $\frac{d^2I}{dV^2}$ of the metal-semiconductor

systems have been performed at liquid helium temperature.

3. Interface State Emission and Recombination

The capture and emission of majority carriers by the interface states will also contribute to the differential conductance when a c. bias is applied on the metal-semiconductor system similar to take as in the MOS structure. According to Nicollan and Geotzberger's results (19) for MOS structure, the interface-state conductance is given by

$$G = \frac{q N_t F_o \frac{\partial F_o}{\partial E} W^{2 \tau^2}}{1 + W^{2 \tau^2}}$$
 (25)

where N_t is the interface-state density, F_0 the distribution function in equilibrium, E the electron energy, τ the time constant, and ω the angular frequency of the applied small a.c. signal. The varia-

tion $\frac{\partial F_o}{\partial E}$ is a maximum when the Fermi level coincides with the

energy of the interface state E_t . Thus the differential conductance peak gives the position of interface state in the semiconductor band gap.

W. Experimental Procedure and Results

1. Device Fabrication

The metal-semiconductor devices are fabricated on n-type,<111> criented, one-side polished, low-dislocation-density silicon wafers. For samples with doping concentrations above 10¹⁶ cm⁻³, single crystal wafers are used; for lower dopings epitaxial wafers of n on n+ are used in order to reduce the series resistance. To eliminate the edge effect, a guard ring structure has been used. The device geometry is shown in Fig. 10. The fabrication procedure is essentially the same as that described by Lepselter and Sze(20)

using plannar technology.

The sample is cleaned first. After oxidation to grow a oxide of 5000 Å, a guard-ring window is opened in the oxide by the use of photoresist techique. Boron is then diffused into the window to form a p+ guard ring with junction depth of 5 μ m. (an ohmic contact is made on the back side of the sample by alloy method.) A window inside the guard ring is opened in the oxide, and gold film of about 1000 Å is evaporated onto the window to form the metal-semiconductor contact. The effective areas of the contacts are 4×10^{-5} , 3×10^{-4} , and 2×10^{-3} cm². Another photoresist process is used to isolate the metal contacts from one another. For PtSi-Si devices, the platinum silicide is formed by deposition of Pt using back sputtering method and then sintering at 600°C inside the vacuum system. Most of the devices are bonded by thermal compression method on TO-5 packages so that they can be measured at various temperatures.

2. Current-Voltage and Capacitance-Voltage Measurements The current-voltage measurement setup includes an HP 425 micro-volt-ammeter and a United System Corporation Model 211 digital voltmeter. A Boonton 74D capacitance bridge is used for the capacitance voltage measurement.

The measured forward-biased I-V curves are shown in Fig. 4(a) for 296°K and Fig. 4(b) for 77°K. We note that there is general agreement between the experimental results and the theoretical predictions. The deviations of current at lower voltages at 77°K is presumbly due to recombination-generation effect in the depletion region. The effect of two-dimensional statistical variation of impurities can be clearly seen from these figures, i.e. for samples of 10¹⁸ cm⁻³ the experimental data are considerably higher than the theoretical curves for uniformly doped ideal samples. The variation of the saturation current density J_S with doping is plotted in Fig. 6 for Au-Si devices measured at room temperature. There is also excellent agreement between theory and experiment.

The reverse-biased curves are shown in Fig. 5. The measured reverse currents are fairly close to the theoretical values except the sample of $10^{14}~\rm cm^{-3}$ in which a large contribution of recombination-generation current component is expected. The values of the breakdown voltages, at which the reverse current increases rapidly with bias, are also in reasonable agreement with the predicted values as shown in Fig. 8.

The measured capacitances follow the expression given by Eq. (5). From the plots of $1/C^2$ versus V, the built-in potential $V_{\rm bo}$

can be determined from the intercept on the voltage axis. The ionized impurity concentration can also be determined from the slope of the plot. Once the built-in potential V_{bo} is determined and the voltage V_{n} corresponding to the difference between the Fermi level and the conduction band edge is deduced from the doping concentration, the barrier hight is given as

$$\phi_{Bo} = V_{bo} + V_a - kT/q. \tag{26}$$

is the intrinsic barrier height since in the capacitance built-in potential is not affected by the image force.

The values of o for Au-Si system at room temperature obtained measurement are listed in Table I. The result the intrinsic barrier height is about 0.8V and is indepmentation.

3. Photoelectric Measurement

well known that the photoelectric measurement is the accurate method in determining the barrier height. When the photoelectric measurement is the photoelectric measu

A Shimatzu spectrophotometer type GE 120 is used for the mement. Some adjustment of the grating system is made in orestend the wavelength to larger values. A 500W tungsten is used as the light source. The measured result of ϕ_{Bn} is issed in Table II. Also listed are the barrier lowering due to mage-force effect. (21) The intinsic barrier height which is the sum of ϕ_{Bn} and is given in the last column. It is obvious that the intrinsic barrier height ϕ_{Bn} over the doping range is essentially a constant; and the result is consistant with the capacitance measurement.

4. Differentiation Measurement

In order to study the scattering events in the transport processes, one of the most useful methods is the measurement of the first and the second derivatives of the current - voltage characteristics. The setup of the measurement is identical to that described by Patterson and Kuhn (22) in which a lock in detection system is used and the differential peaks are plotted on an X-Y recorder. The devices are mounted in TO-5 package which can be immersed directly into liquid helium.

The measured second derivatives are shown in Fig. 11(a) for PtSi-Si barrier and Fig. 11(b) for Au-Si barrier. It is interesting to observe that there are several peaks over the bias range of

about 100mV. The result is summarized in Table III. Of particlar interest are the peaks at 22 meV and 56 meV which are present for both systems and under forward and reverse biasing conditions.

V. Discussion and Conclusion

The present study incorporating the accurate quantum transmission coefficients gives for the first time a generalized and quantitative result for the carrier transport in metal-semiconductor systems. Special emphasis has been placed on the importance of the impurity concentration whose effects are threefolds:

- 1. For a given metal-semiconductor system the saturation current density $J_{\rm S}$ depends strongly on temperature and on the impurity concentration especially at dopings higher than $10^{17}~{\rm cm}^{-3}$. Therefore even for a high-barrier system its rectifying or ohmic behavior can not be determined unless the impurity concentration is specified. For example the barrier height of PtSi-Si is about 0.85eV; it is a rectifying contact for lower dopings. However the value of $J_{\rm S}$ at room temperature approaches $1.2 \times 10^3~{\rm amp/cm^2}$ at $10^{20}~{\rm cm}^-$ due to tunneling effect. Such a large current density can well explain the ohmic-contact behavior between PtSi and a degenerate n-type silicon.
- Becuase of the dependence of J_S on doping, the evaluation of the barrier height from I-V measurement may result in considerable error. Therefore the photoelectric and capacitance measurements should be used for the determination of the barrier height.
- 3. Due to the two-dimensional statistical variation of impurity concentration, the measured current density for a given average doping is always larger in comparison to that computed for the ideal condition of uniformly doped sample. The difference becomes greater as the doping increases.

The experimental current-voltage characteristics as shown in Fig. 4, 5 and 6 are in quantitative agreement with the theoretical predictions. These results also show the importance in the use of the accurate quantum transmission coefficient and the Fermi-Dirac distribution function in the transport analysis.

We have considered the doping concentration N and the lattice temperature T as two independent parameters rather than a combined parameter (RKT) which is proportional to T/\sqrt{N} . A comparison of the present theory with that of Crowell and Rideout's theory (11) using the combined parameter is shown in Fig. 12. Where J_f is the forward current density and $J_m \equiv A*T^2 \exp(-qVn/m)$

KT) corresponding to the thermionic current density at flat band condition. We note that for RKT = 1.5 the two theories are in reasonable agreement. For RKT = 0.1, however, there are considerable difference between the two theories. In addition for a given RKT, one obtains different I-V characteristics for different combinations of T and N. It is thus clear that the combined parameter RKT is only applicable over limited range of Tand N, and can not be used as a universal parameter especially for tunneling cases.

From the C-V measurement and photoelectric measurement it is confirmed that the intrinsic barrier height ϕ_{BO} of Au-n-type Si system is a constant independent of doping concentration over the range 10^{14} to 10^{20} cm⁻³.

In the differentiation measurement, a peak corresponding to the 56 meV transverse optical phonon is always observed in both forward and reverse biases for PtSi-Si and Au-Si diodes. However, the peak corresponding to the 18.4 meV transverse acoustic phonon is missing in the PtSi-Si diodes and in the Au-Si diode under forward bias, It is conceivable that the conservation of momentum in a metal-semiconductor system is not strictly valid, since phonons may interact with energy states which exist at the metal-semiconductor interface.

It is believed that because of the sintering process the crystalline structures of PtSi-Si interface are more orderly oriented, therefore there are less energy states present at the interface. For Au-Si diodes, however, because of the deposition process, one expects to have more interface states. The differential conductance peaks are listed in Table III. As expected, the Au-Si diode under forward bias shows the largest number of conductance peaks.

It is interesting to note that peak at 22 meV is present in both kinds of diodes and in forward and reverse bias conditions. But this peak does not exist in the conventional phonon spectra of p-n junction devices. According to Dumke's theory (23) deduced from Haynes optical data, the longitudinal acoustic phonon energy corresponding to scattering between valleys along the <100> axis is 23 meV. Therefore it is apparent that this kind of Scattering can be observed in metal-semiconductor systems.

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Table I. Capacitance Measurement of Intrinsic Barrier Height of Au-Si (at 300°K)

Doping (cm ⁻³)	V _{bo} (Volts)	V _n (volts)	φ _{Bo} (volts)
8.6 x 10 ¹⁴	0.54	0.269	0.809
1.4 x 10 ¹⁶	0.61	0.197	0.807
1.0 x 10 ¹⁷	0.66	0.146	0.806
3.6 x 1018	0.70	0.11	0.81

Table II. Photoelectric Measurement of Intrinsic Barrier Height of Au-Si System (at 300°K)

Doping (cm-3)	Resistivity (Ω-cm)	φ _{Bn} (volts)	$\triangle \phi$ (volts)	φ _{Bo} (volts)
1.0 x 10 ¹⁵	4.3	0,780	0.017	0.797
3.6 x 10 ¹⁷	0.025	0.740	0.053	0.793
1.1 x 10 ¹⁸	0.01	0.738	0,073	0.811

Table III. Differential Conductance Peaks (at 4.2°K)

System	Biasing Condition	Energy (meV)
Au-Si	Forward	6, 13, -, 22, -, 42, 46, 56, -, 74
	Reverse	18, 22, -, 42, -, 56,
PtSi-Si -	Forward	22, -, 42, -, 56, 72.6, 93
	Reverse	22, 35, -, -, 56, 68

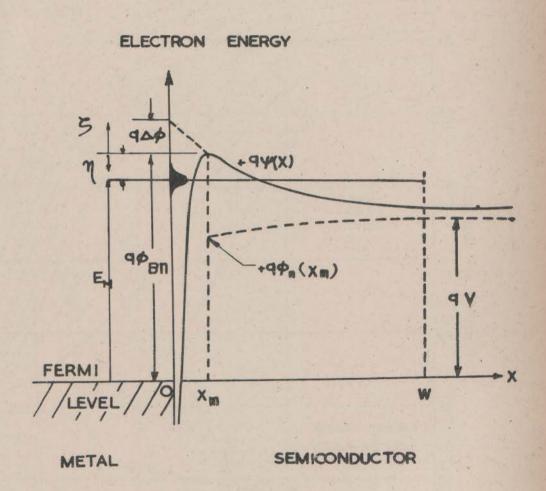


Fig. 1. Band diagram of a metal-semiconductor system.

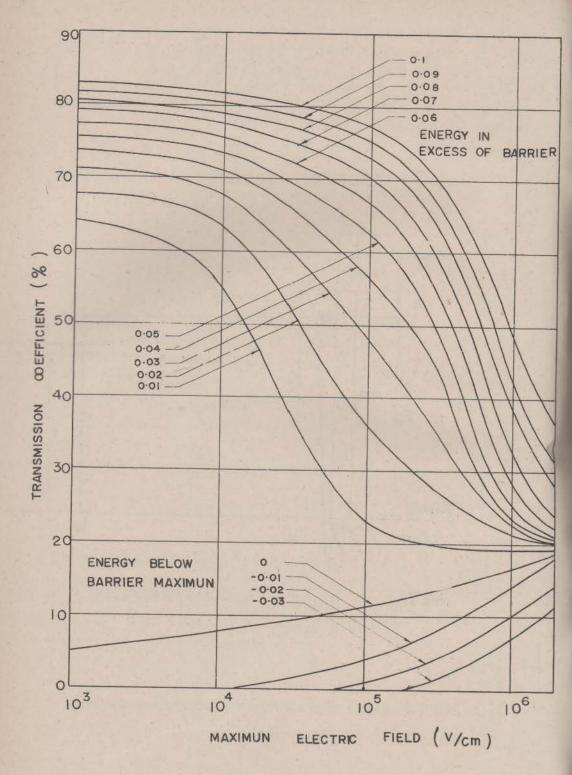


Fig. 2. Transmission coefficient versus electric field with electron energy as a parameter.

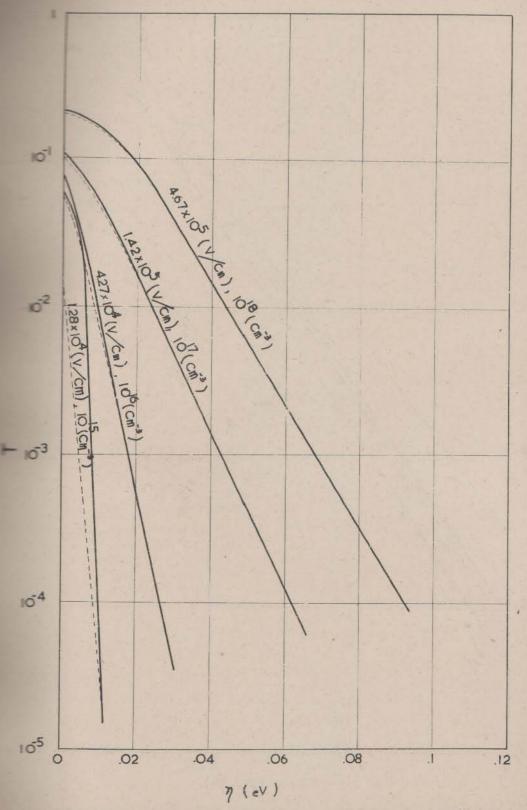


Fig. 3. Comparison of $T(\eta)$ with the exact quantum transmission coefficient for energies below the potential maximum.

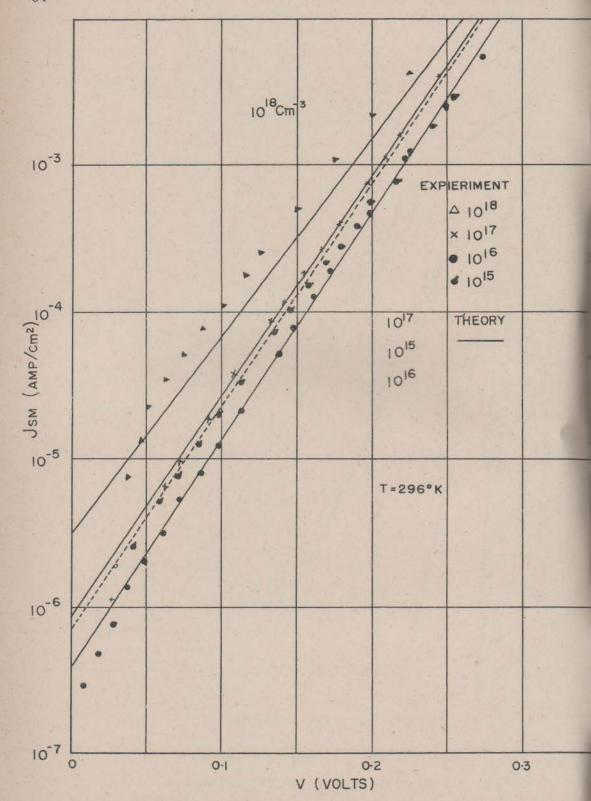


Fig. 4. Theoretical and experimental values of $J_{\rm SM}$ versus forward applied voltage. Solid lines are the theoretical results and the data points are obtained experimentally. (a) for $T = 296^{\circ}K$, and

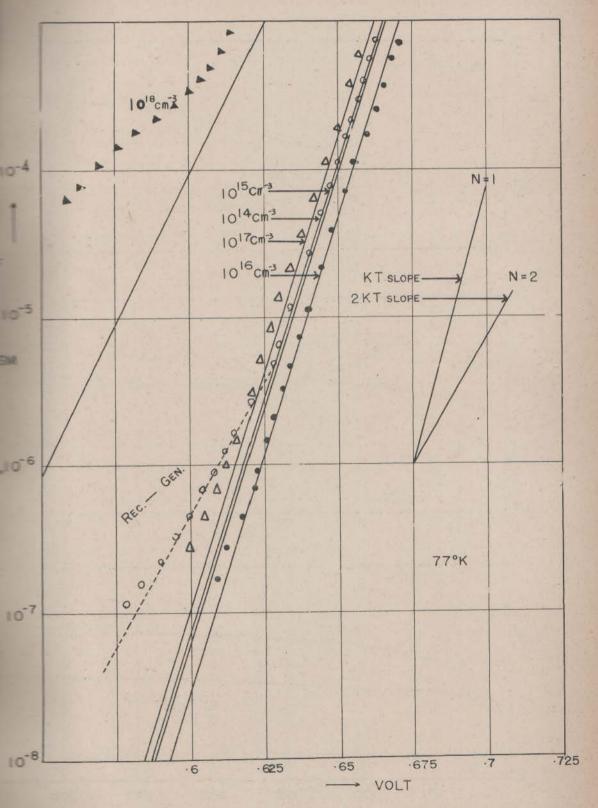


fig. 4. (b) for $T = 77^{\circ}K$.

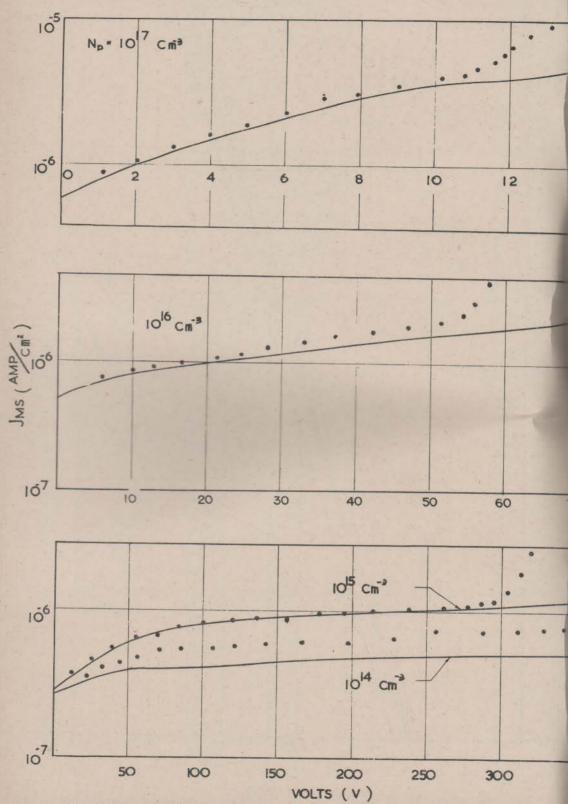


Fig. 5. Theoretical and experimental values of J_{MS} versus reverse bias voltage. Solid lines are the theoretical results while the data points are obtained experimentally.

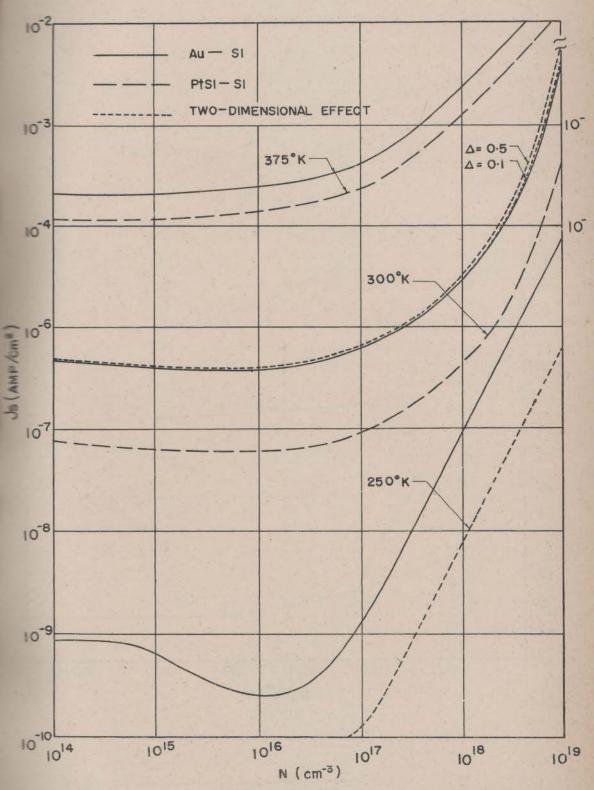


Fig. 6. Saturation current density versus doping with temperature as a parameter. Solid lines are for Au-Si system, dashed lines for PtSi-Si system, and dotted lines are for statistical variation of impurity.

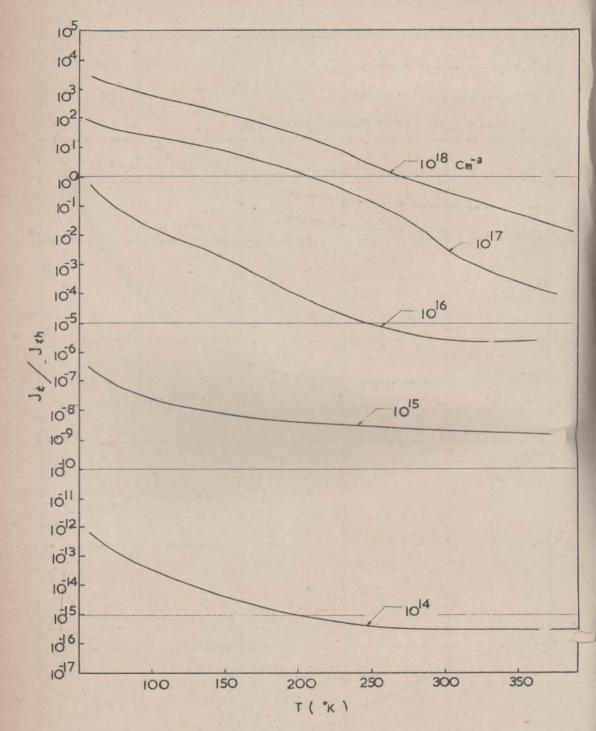
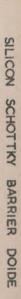


Fig. 7. Theoretical ratio of the tunnel current to thermionic current density.



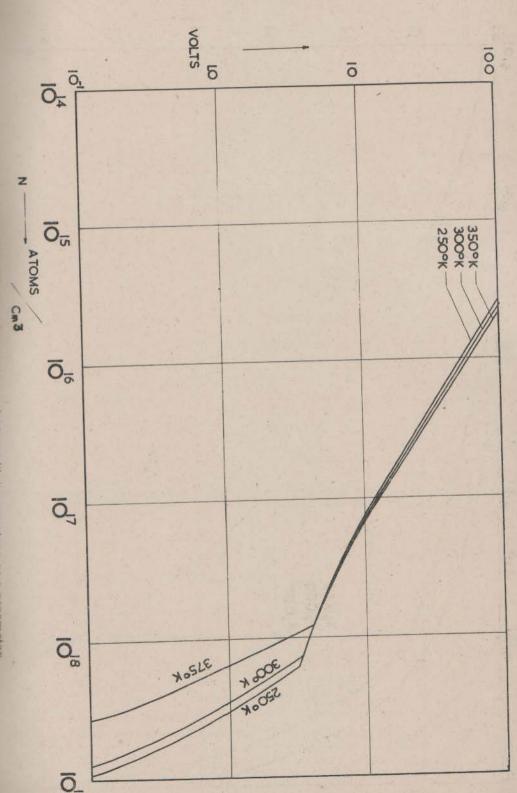
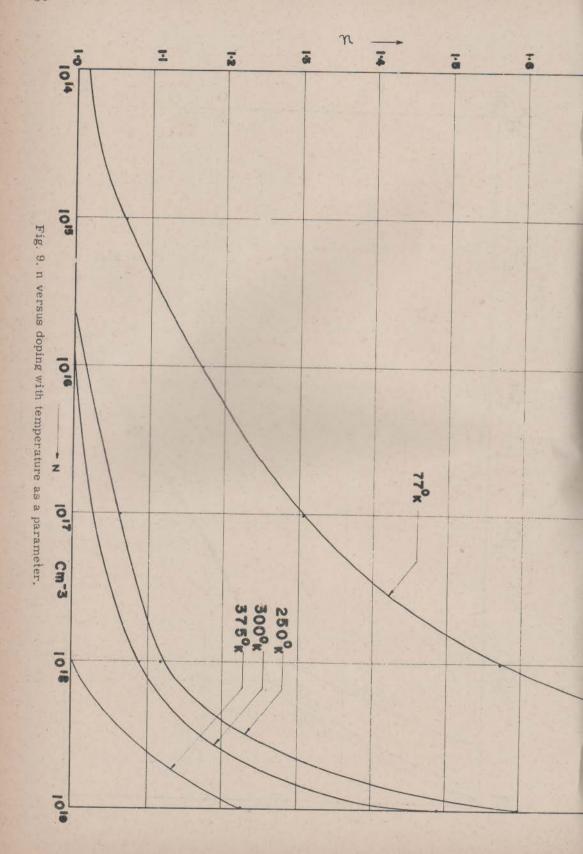
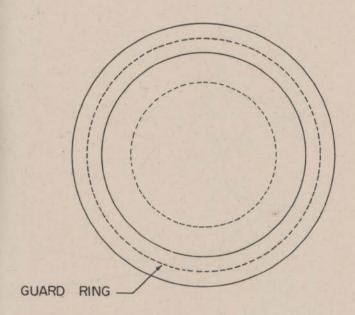


Fig. 8. Breakdown voltage versus doping with temperature as a parameter.





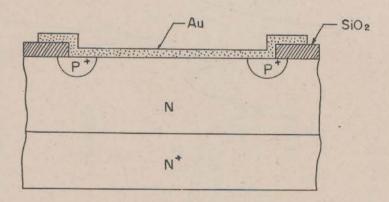


Fig. 10. Device geometry of a Schottky barrier diode.

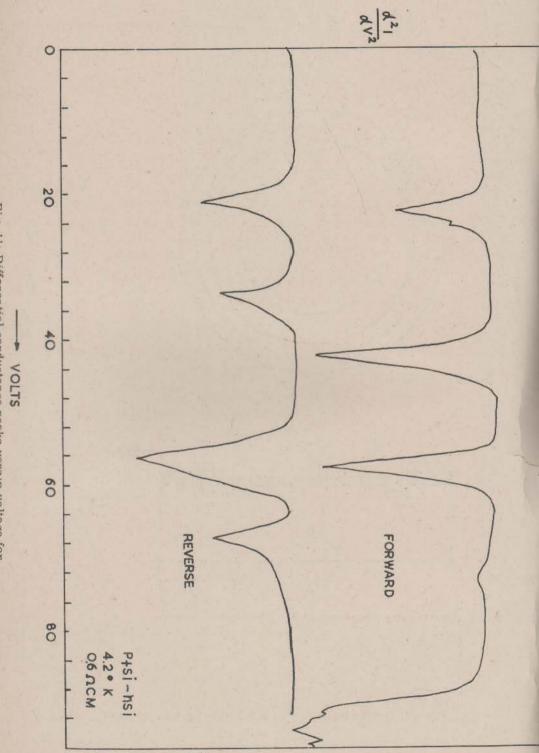
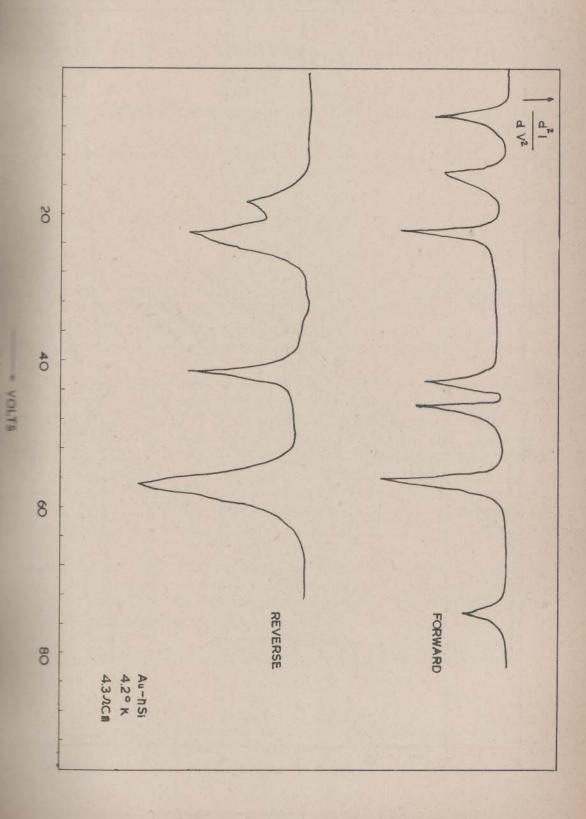


Fig. 11. Differential conductance peaks versus voltage for (a) PtSi-Si diode and



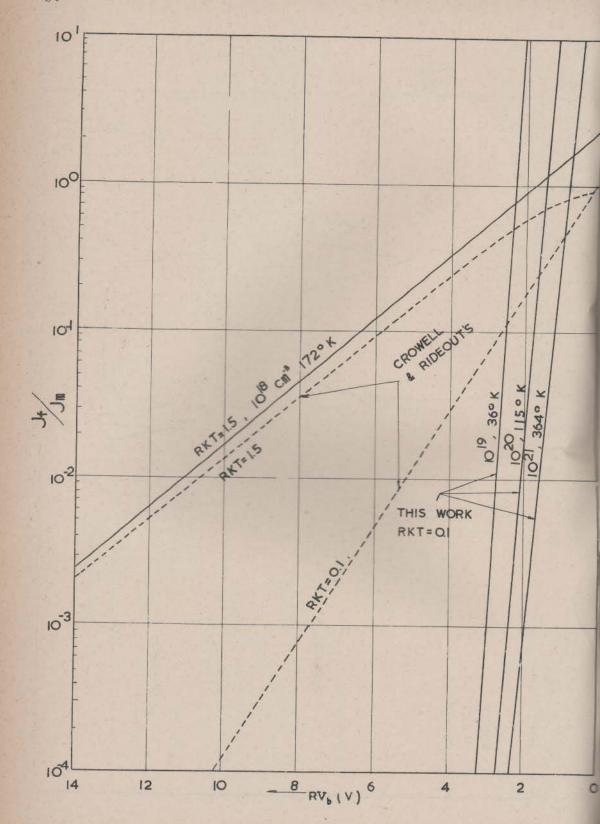


Fig. 12. Comparison of the present theory with that of Crowell and Rideout's