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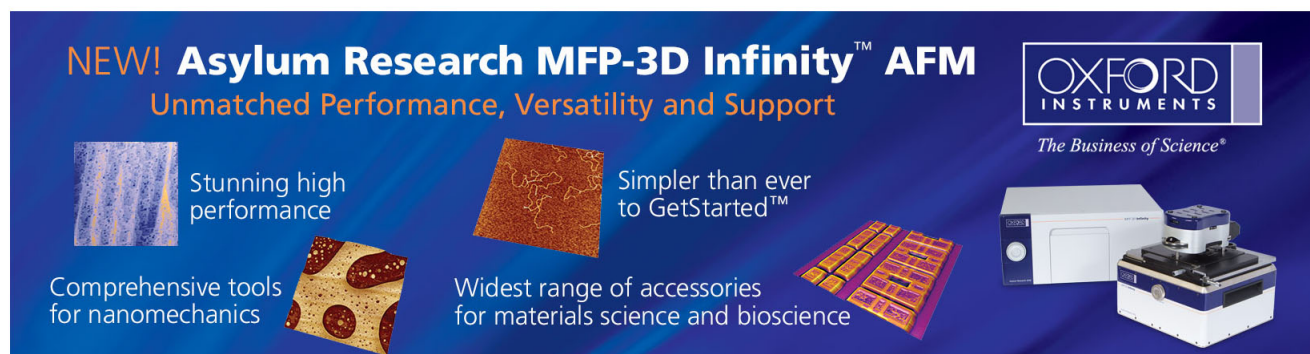
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Role of measurement voltage on hysteresis loop shape in Piezoresponse Force Microscopy

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The dependence of field-on and field-off hysteresis loop shape in Piezoresponse Force Microscopy (PFM) on driving voltage, V_{ac} , is explored. A nontrivial dependence of hysteresis loop parameters on measurement conditions is observed. The strategies to distinguish between paraelectric and ferroelectric states with small coercive bias and separate reversible hysteretic and non-hysteretic behaviors are suggested. Generally, measurement of loop evolution with V_{ac} is a necessary step to establish the veracity of PFM hysteresis measurements. © 2012 American Institute of Physics. [<http://dx.doi.org/10.1063/1.4764939>]

Hysteresis loop measurements in Piezoresponse Force Microscopy (Piezoresponse Force Spectroscopy, PFS) have emerged as a powerful tool for probing polarization switching in nanoscale volumes.^{1–3} In PFS, a slow (1–10 s), large amplitude bias sweep applied to the scanning probe microscopy tip in contact with the surface induces the nucleation and growth of a ferroelectric domain below the tip. A simultaneously measured high-frequency (10 kHz–3 MHz) dynamic response to the small amplitude bias voltage provides information on the degree of switching. The resultant hysteresis loop provides local information on the kinetics and thermodynamics of tip-induced switching and can be analyzed similarly to macroscopic hysteresis loops. The presence of imprint bias or vertical loop shift has been used to analyze the ferroelectric behavior of ferroelectric nanostructures.^{4–6} The introduction of spectroscopic imaging modes based on rapid hysteresis loop measurements⁷ or stroboscopic imaging at different biases^{8–12} has enabled fundamental studies of domain nucleation and growth processes. In certain cases, polarization switching mechanisms were resolved on the level of single structural defects.^{13,14}

Despite multiple experimental studies of polarization dynamics using Piezoresponse Force Microscopy (PFM) voltage spectroscopy (local hysteresis loop measurements), the factors affecting the measured response are not well understood. The dominant among these is the role of driving voltage, V_{ac} , on the hysteresis loop shape. Indeed, the measured signal in PFM is directly proportional to V_{ac} and hence large biases improve the signal to noise ratio. At the same time, the high frequency voltage can be expected to affect polarization switching induced by the slow switching waveform. Note that similar studies in the context of other voltage modulated techniques such as Kelvin Probe Force Microscopy have revealed non-trivial insights into the microscope operation and role of topography and topographic cross-talk.^{15–17}

In PFM, the role of driving voltage is directly linked to the fundamental dynamics of polarization switching. If domain switching is slow, as limited by, e.g., strong pinning in the bulk or surface screening charge dynamics, the high frequency signal will not significantly affect the hysteresis loop measurements. In the opposite limit, the switching and probing waveforms will result in polarization dynamics equally. The hysteresis loop shape will hence be strongly affected by these relaxation processes. This behavior is particularly of interest in the context of recent work on ultrathin (below 3–10 nm) ferroelectric films^{18,19} in which the intrinsic switching biases can be comparable to ~ 1 V.

To explore these effects systematically, we studied domain switching using band-excitation PFM (BE-PFM)^{20,21} implemented on Asylum Research Cypher platforms. For each pixel (locus) of the surface, a bipolar triangular waveform is applied to the tip. The waveform consists of the field-on and field-off DC (direct current) steps, during which AC (alternating current) driving voltage is applied and hysteresis loops are collected (see Figure 1(a)). The typical parameters we used were: bandwidth of 80 kHz, central frequency *ca.* 300 kHz, frequency interval *ca.* 19.5 Hz, the bias sweep rate *ca.* 50 V/s, the sampling rate was 4 MHz. As a first model system, we have chosen a nanocapacitor sample formed by (001) Pb(Zr_{0.2}Ti_{0.8})O₃ (PZT) film, which was grown epitaxially by pulsed laser deposition on top of a (001) SrRuO₃/SrTiO₃ (SRO/STO) heterostructure (Fig. 1(b)). The SRO film acts as a bottom electrode, the role of the top electrode is played by Au/Cu nanocaps fabricated on the PZT via anodic aluminum oxide masks using an electron beam.^{22,23} The large surface area (relative to that of the PFM tip) of the Au/Cu nanocaps ensures uniformity of the electric field in the PZT film and, hence, enhances switching behavior of the system. To ensure statistical averaging, the BE-PFM measurements were performed over 180 × 180 nm² regions centered at the Au/Cu caps each constituting a 3 × 3 grid of nine spatial locations (Fig. 1(c)). The piezoresponse (PR) signal was averaged over the grid and normalized to the

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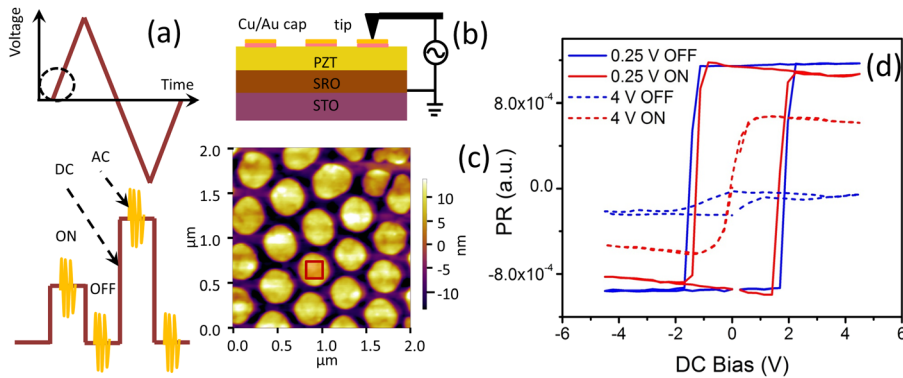


FIG. 1. (a) Voltage waveform applied to the tip: overall (top) and zoomed-up area (bottom). Field on and off measuring states are shown. (b) Schematics of nanoscale capacitor and measurement setup. (c) Topographic map of capacitor sample with location of one of the 180-by-180 nm measurement regions highlighted in red, (d) Piezoresponse (PR) hysteresis loops averaged over a 3×3 ($180 \times 180 \text{ nm}^2$) grid. Shown are loops measured in the on and off states at 0.25 V and 4 V driving biases.

AC driving voltage. Little variation has been seen in the switching behavior between different nanocaps except those with some defects and shape deformations (which are not considered below).

The typical hysteresis loops for the caps are shown in Fig. 1(d) and demonstrate an almost ideal rectangular shape for small biases (in previous studies²⁴ the switching parameters were found to be uniform with respect to the probing position relative to the nanocap center). For field-on vs. field-off loops, note the presence of a (small) linear slope (the field-on PR baseline is tilted relative to that of the field-off), presumably due to electrostatic cantilever-surface interactions.^{25–27} The increase in AC driving voltage from 0.25 V to 4 V results in drastic changes in the hysteresis loops shape, as illustrated in detail in Figure 2. The field-off loops become progressively narrower with increasing V_{ac} , while the height remains almost constant for low voltages. The coercive and nucleation biases (which coincide for a rectangular loop) are approximately 1.5 V for 0.25 V_{ac} , and the loop width decreases almost linearly with the slope close to unity as a function of V_{ac} both for field-on and field-off loops. We further refer to the values of these parameters extrapolated to zero driving voltage as *static*. This observation suggests that for this system the driving voltage and switching voltage

affect polarization dynamics equally regardless of the 5 orders of magnitude difference in frequencies.

The remarkable difference between field-on and field-off loop behavior is observed when driving voltage exceeds the static coercive bias. For the field-off measurements, the hysteresis loop collapses and exhibits only weak residual hysteresis. This behavior can be easily rationalized by the fact that under these conditions the material is switching within the measurement cycle and hence static hysteresis is effectively zero.

The behavior of the field-on loop is surprisingly different. For high biases, the loop narrows down and becomes reminiscent of that for the superparaelectric state. We ascribe this behavior to the fact that in the field-on state the average (over measurement cycle) polarization in the system is maintained by the (small) DC bias.

We note that the observed bias dependence of the loop shape is non-universal. Shown in Figure 3 is the bias evolution of a hysteresis loop measured on 50 nm, monodomain PZT film grown on a SrRuO₃/SrTiO₃ (001) substrate (no top electrode used). Here, BE-PFM data were collected from 100 points on a 10×10 grid ($1.5 \times 1.5 \mu\text{m}^2$) and the obtained piezoresponse was averaged and normalized by V_{ac} . In this case, the field-off loops exhibit the classical shape, Figure

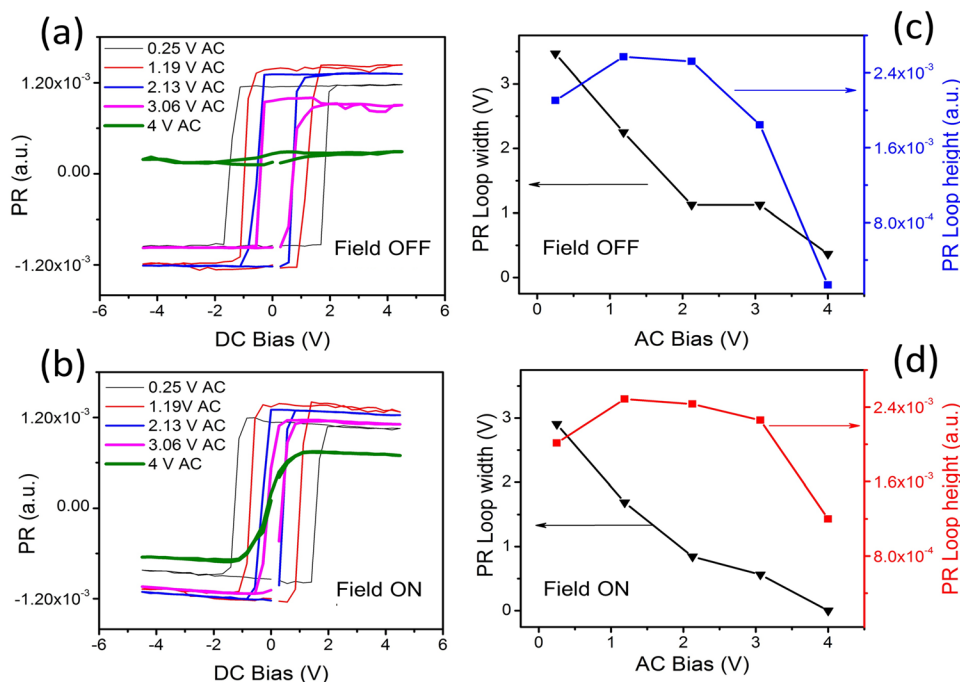


FIG. 2. Evolution of piezoresponse hysteresis loops in nanocapacitors with increasing AC driving voltage for (a) field-off and (b) field-on states. Bias dependence of the effective hysteresis loop parameters for field-off (c) and field-on (d) loops.

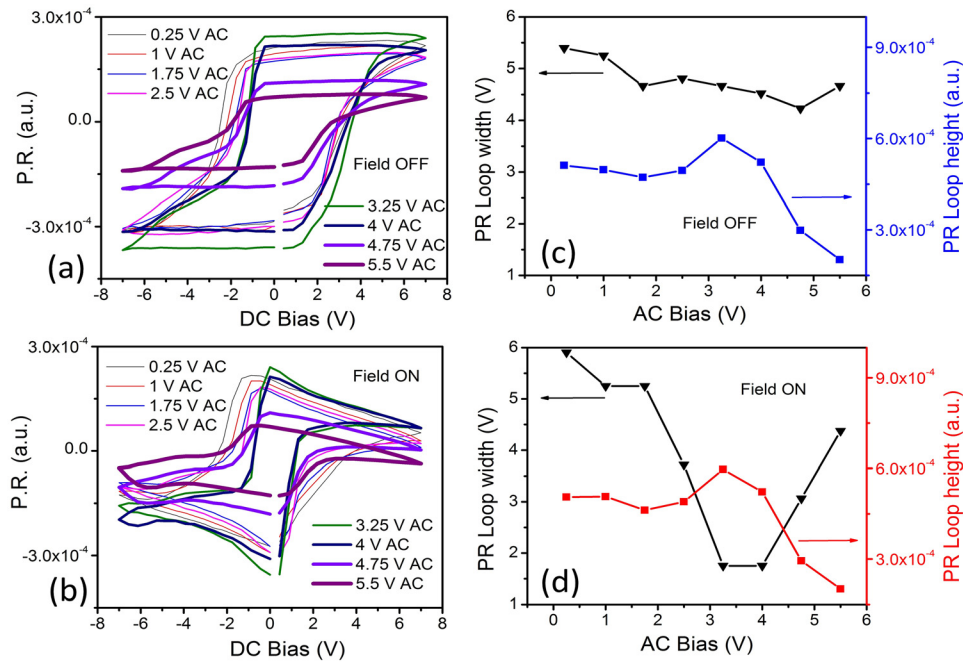


FIG. 3. Normalized piezoresponse hysteresis loop averaged over 10×10 ($1.5 \times 1.5 \mu\text{m}^2$) grid for PZT thin film. Shown are (a) field-off and (b) field-on loops for different driving biases and (c), (d) loop height and width vs. driving bias.

3(a). An increase of the driving voltage above 3 V leads to reduction of the height of the loops, while the corresponding width remains almost constant (Fig. 3(c)). In comparison (Fig. 3(b)), the field-on loops show anomalous (but reproducible across all locations and driving voltages) shape. Their aspect ratio keeps increasing up to 3 V of driving bias due to narrowing, after which the loops significantly widen while simultaneously collapsing in height (Fig. 3(d)). The use of the driving voltage beyond 5.5 V brings about topological changes and the formation of 50-100 nm hillocks at some locations on the grid, suggesting the onset of irreversible electrochemical transformations.²⁸ This process is associated with the emergence of a wide “tail” at negative DC biases. Thus, the sharp difference in the field-on loop shapes below

and above 4 V of driving bias is due not to the specific aspects of polarization switching but should rather be attributed to partial decomposition of the PZT film.

A much more complicated behavior is observed when a complex ferroelectric-ferroelastic domain structure is present. One such case is shown in Figure 4 for the bias evolution of hysteresis loops measured on 200 nm BiFeO₃ films grown on a SrRuO₃/DyScO₃ substrate (no top electrode used). These films have characteristically dense 71° wall structures and hence combine ferroelectric and ferroelastic behavior.

At driving voltages less than 3.25 V the field-off piezoresponse is stable, between 3.25 and 4 V loops start to collapse, and above 4 V the film degrades. Field-on loops, although being very anomalous in shape, show similar

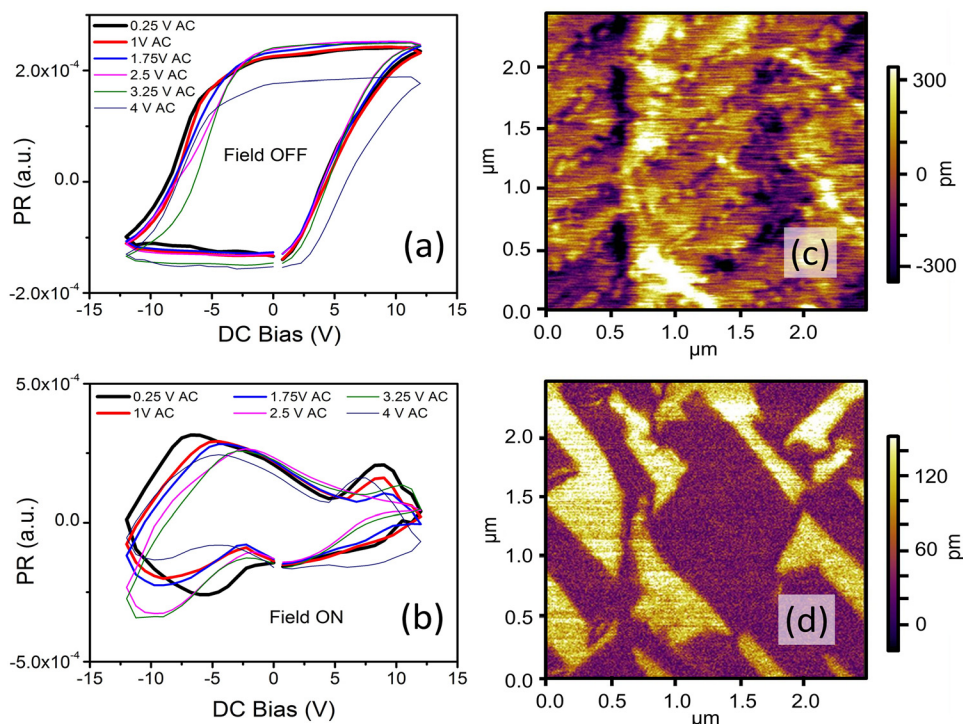


FIG. 4. BiFeO₃ sample: Piezoresponse loops for field-off (a) and -on (b) states recorded on a 10×10 ($2 \times 2 \mu\text{m}^2$) grid, (c) typical topography of the sample, and (d) in-plane domain structure.

changes. A possible explanation of this behavior may be activation of non-hysteretic ferroelastic polarization switching, including wall twists.^{29–31} The field-off loop behavior is then controlled by the collective interactions between the ferroelastic and ferroelectric subsystem.

To summarize, we have explored the effects of AC driving voltage on the measurements local hysteresis loops using PFM. In certain cases, the driving voltage and measurement voltage affect polarization switching equally. The measurements of both field-on and field-off loops then suggest a strategy to elucidate this behavior and distinguish paraelectric materials from ferroelectrics with low coercive biases (for a closed loop V_{ac} gives a top estimate of the coercive bias), as well as from electrostrictive (a V-shaped dependence of PR amplitude on bias in the region of electrostriction). At the same time, for materials with complex domain structures, the field-on and field-off loops contain complementary information on reversible and irreversible polarization dynamics. In all cases, measurements of loop evolution with V_{ac} are a necessary step to establish the veracity of PFM hysteresis measurements.

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