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Citation: Applied Physics Letters **103**, 042902 (2013); doi: 10.1063/1.4816269 View online: http://dx.doi.org/10.1063/1.4816269 View Table of Contents: http://scitation.aip.org/content/aip/journal/apl/103/4?ver=pdfcov Published by the AIP Publishing

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## Enhancement of the stability of resistive switching characteristics by conduction path reconstruction

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(Received 27 May 2013; accepted 5 July 2013; published online 23 July 2013)

In this study, a Pt/BiFeO<sub>3</sub>/TiN device was fabricated and the resistance switching characteristics were investigated. After the first forming process, the conduction path was formed and exhibited unstable bipolar switching characteristics. Subsequently, the original conduction path was destroyed thoroughly by high negative bias. By reconstructing the conduction path after a second forming process (re-forming process), the device exhibits stable bipolar switching characteristics. Transmission electron microscopy analysis indicates that the stability of switching behavior was enhanced because of the joule heating effect, and is an easy way to improve the resistance switching characteristics. © 2013 AIP Publishing LLC. [http://dx.doi.org/10.1063/1.4816269]

With the development of portable electronic products, the thin film transistor for display<sup>1,2,16,17</sup> and nonvolatile memory for storage has been widely utilized.<sup>3</sup> However, traditional nonvolatile floating gate memory device has confronted some physical limits as devices continuously scaled down,<sup>3,18,19</sup> and resistance random access memory (RRAM) is considered one of the best potential candidates for development of the next generation of nonvolatile memory because of advantages such as low operating power consumption, high density due to simple device structure, and a fast switching speed of about several ns.<sup>4,5</sup> A number of materials have been investigated for their resistive switching characteristics, such as HfO<sub>2</sub>, ZnO, and InGaZnO.<sup>4,6–10</sup> Information can be stored in the RRAM devices by the various resistance states,<sup>4</sup> and therefore resistive switching property is very important. Hence, ways of enhancing the resistance switching characteristics have been proposed. For example, a thermal process has been used to anneal the resistive switching layer, enhancing the resistive switching property.<sup>10</sup> However, the fabrication of a RRAM device is a back-end-of-line (BEOL) process, and the material property of the TiN bottom electrode might be affected by the thermal process.

BiFeO<sub>3</sub> (BFO) is a multiferroic material which has been applied to ferroelectric random-access memory<sup>11</sup> and magnetoresistive random-access memory<sup>12</sup> devices because of its ferroelectricity. Additionally, the resistive switching characteristics of BiFeO<sub>3</sub> have been investigated. In this letter, we employ BFO as the resistive switching layer because BFO film exhibits special characteristics that can enhance the stability of resistive switching characteristics.

The proposed RRAM device was fabricated on a TiN/ SiO2/Si substrate. We deposited the 25 nm-thick BFO film as a resistance switching layer by RF sputtering a BiFeO<sub>3</sub> target in Ar (30 sccm) ambient at room temperature. Subsequently, we sputtered 100 nm-thick Pt on the BFO film as the top electrode, completing the RRAM device as a Pt/BFO/TiN structure. We investigated the resistive switching properties and measured the I-V curves with an Agilent B1500 semiconductor parameter analyzer and the structural characteristics were investigated by transmission electron microscopy (TEM, FEI Tecnai G2 F20).

During the measurement process, the bias was applied on the TiN electrode and the Pt electrode was ground, as shown in Fig. 1(a). Figure 1(b) shows the forming process necessary to produce a conduction path. After the first forming process, the conduction path is formed, and the resistance value can be switched repeatedly by applying positive or negative voltage, as shown in Fig. 1(c). During the forming process, the electrons receive sufficient energy to break the metal-oxygen bonds, and the oxygen will migrate to the TiN because of the electric field direction. As negative bias is applied, the oxygen ion will oxidize the conduction path near the TiN bottom electrode, and the conduction path will rupture.<sup>13,14</sup> Subsequently, the positive voltage can break the metal-oxygen bond again, and the conduction path near the TiN electrode is re-formed. The ruptured conduction path is defined as high resistance state (HRS) and the unbroken

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FIG. 1. (a) Structure of BiFeO<sub>3</sub> based RRAM device. (b) I-V curves of forming and re-forming process. Resistive switching characteristics of BiFeO<sub>3</sub> after (c) forming process and (d) after re-forming process.

conduction path is defined as low resistance state (LRS). Subsequently, a higher negative bias sweeping from 0 V to -2.5 V was applied to destroy the original conduction path thoroughly, as shown with the red line in Fig. 1(b), and the conduction path is reconstructed by second forming process (re-forming process). Figure 1(d) shows the resistance switching characteristics after the re-forming process. A comparison of Figures 1(c) and 1(d) clearly shows the resistive switching behavior after the re-forming process is more stable than the initial resistive switching characteristics (after the forming process).

In order to investigate the change of BFO resistance switching layer before and after the higher negative bias sweeping, we analyzed the structure by TEM images. Figure 2(a) shows the TEM image of the Pt/BiFeO<sub>3</sub>/TiN structure which has undergone the first forming process and one hundred DC cycle sweeps, with the BFO layer in an amorphous state. After the higher negative bias sweeping and re-forming process, the BFO film will crystallize, as shown in Fig. 2(b). This phenomenon indicates that the higher negative bias sweeping or re-forming process provides large thermal energy and the stability of resistive switching behavior is enhanced by the thermal effect.



FIG. 2. TEM images of BFO film after (a) forming process and (b) re-forming process.

From the equation of the joule heating effect, P (power) = I (current)  $\times$  V (voltage), the large thermal energy can be supplied during the reset process with the higher negative bias sweeping. From these results, the switching mechanism of BFO film can be proposed. At the initial state, there are many defects in the BFO film, as shown in Fig. 3(a). In order to form the conduction path, a higher positive voltage is applied and the electrons receive sufficient energy to break the metal-oxygen bond. After the metaloxygen bond is broken, the oxygen ions migrate to the TiN electron and the conduction filament (or path) is formed along defects, as shown in Fig. 3(b). As the higher negative bias is applied, the joule heating effect supplies large thermal energy and anneals the area near the filament, as shown in Fig. 3(c). Because of the localized thermal effect, the area surrounding the filament crystallizes. Hence, the defects can be reduced and the conduction path will be thoroughly destroyed, as shown in Fig. 3(d). Subsequently, the conduction path can be reconstructed by the re-forming process.

In order to investigate the difference in resistive switching characteristics after the forming process and after the re-forming process, we extracted the resistance value of LRS and HRS by DC sweep cycles at 0.2 V, as shown in Figs. 4(a) and 4(b). According to the LRS/HRS statistics, the on/ off ratio after forming process is unstable because the defect affects the migration of oxygen ions during the set/reset process. Figures 4(c) and 4(d) shows set voltage (Vset) and reset voltage (Vreset), with the variation of Vset and Vreset decreasing after the re-forming process. This is because the conduction path is dominated by the quantity of defects in the resistance switching layer. The more defects which exist, the more conduction paths which will form during each set/ reset cycle.<sup>15</sup>

In summary, because the fabrication of RRAM device is a back-end-of-line (BEOL) process, it is difficult to anneal by a thermal process, so we propose a simple way to enhance

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FIG. 3. Diagram of (a) initial state, (b) formation of conduction path, (c) annealing process, (d) the destroyed conduction path, and (e) the conduction path reconstruction.

FIG. 4. HRS/LRS statistics of switching characteristics (a) after forming process and (b) after re-forming process. (c) Set voltage and (d) reset voltage statistics.

the stability of such a nonvolatile resistance switching memory device. After a high negative bias stress, the  $BiFeO_3$  film will be annealed by the joule heating effect and will crystallize. Hence, the quantity of defects in the  $BiFeO_3$  film can be reduced, and the stability of resistive switching characteristic can be enhanced.

This work was performed at National Science Council Core Facilities Laboratory for Nano-Science and Nano-Technology in Kaohsiung-Pingtung area, NSYSU Center for Nanoscience and Nanotechnology, and was supported by the National Science Council of the Republic of China under Contract Nos. NSC-102-2120-M-110-001.

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