Precoder/two-stage equaliser for block-based single-carrier transmission with insufficient guard interval

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Proposed is a zero-inserting precoder and a two-stage linear equaliser, to shorten the guard interval in block-based single-carrier modulation. The first-stage equaliser consists of a linear single-tapper-subcarrier frequency-domain equaliser. The second-stage equaliser maximises the SINR, in the time-domain, based on the interference-plus-noise estimated from the zero-padded sub-intervals of the single-carrier modulation. This proposed scheme is applicable even without cyclic prefixing.

Review of block-based cyclically-prefixed singe-carrier transmission through time-dispersed channel: The information-bearing symbols $\{u(j), \forall j\}$ are segmented at the transmitter into blocks of N symbols. Represent the kth block as an N-element vector, $u(k) = [u_{-N/2+1}(k), \ldots, u_0(k), \ldots, u_{N/2}(k)]^T$, where $u_n(k) = u(kN + N/2 - 1 + n)$, for $n = -N/2 + 1, \ldots, N/2$. Prefix u(k) with a length-G guard interval, which could be a cyclic prefix (CP), i.e. a replication of the last v entries of u(k). Mathematically, this cyclic-prefixing operation equals the multiplication of u(k) into an $(N + G) \times N$ cyclic-prefix-insertion matrix $T_{cp} = \begin{bmatrix} 0_{G \times (N-G)} & I_G \\ I_N \end{bmatrix}$, to produce the (N+G)-element vector, $\tilde{u}(k) = T_{cp}u(k)$. This CP serves to reduce or to eliminate up to G taps of inter-block interference (IBI), caused by a frequency-selective fading-channel. The guard interval need not be a cyclic prefix as above, but could be entirely zero-energy symbols, or some mix of the two.

Consider a frequency-selective but time-invariant channel of order Q, with the discrete-time impulse-response taps of h(0), h(1), ..., h(Q). This channel's output is modelled as corrupted by additive noise, symbolised by the (N+G)-element noise-vector $\eta(k)$, which is zero-mean, characterised by a prior known temporal correlation matrix of $R_{\eta(k)\eta(k)}$, and is statistically independent from u(k). Hence, the received data have the *k*th symbol-block equal to $\tilde{r}(k) = H_0 \underbrace{T_{cp}u(k)}_{\tilde{u}(k)} + H_1 \underbrace{T_{cp}u(k-1)}_{\tilde{u}(k-1)} + \eta(k)$, where $H_0 \in \mathbb{C}^{N+G} \times {}^{(N+G)}$ rep-

resents a lower triangular Toeplitz matrix, with its first column being $[h(0), h(1), \dots, h(Q), 0, \dots, 0]^T$; and $H_1 \in \mathbb{C}^{(N+G) \times (N+G)}$ denotes an upper triangular Toeplitz matrix, with its first row as $[0, \dots, 0, h(Q), \dots, h(1)]$.

The receiver removes the cyclic prefix, via $R_{cp} = [\mathbf{0}_{N \times G} \mathbf{I}_{N \times N}]$, from the received signal to yield the *N*-element vector

$$\mathbf{x}(k) = \mathbf{R}_{cp} \underbrace{\left[\mathbf{H}_{0} \tilde{\mathbf{u}}(k) + \mathbf{H}_{1} \tilde{\mathbf{u}}(k-1) + \mathbf{\eta}(k) \right]}_{=\tilde{\mathbf{r}}(k)}$$

$$= \mathbf{R}_{cp} \mathbf{H}_{0} \mathbf{T}_{cp} \mathbf{u}(k) + \underbrace{\mathbf{R}_{cp} \mathbf{H}_{1} \mathbf{T}_{cp}}_{=\mathbf{H}_{IBI}} \mathbf{u}(k-1) + \mathbf{R}_{cp} \mathbf{\eta}(k)$$

$$= \underbrace{\left(\mathbf{R}_{CP} \mathbf{H}_{0} \mathbf{T}_{cp} + \mathbf{H}_{ISI} \right)}_{=C} \mathbf{u}(k)$$

$$- \mathbf{H}_{ISI} \mathbf{u}(k) + \mathbf{H}_{IBI} \mathbf{u}(k-1) + \mathbf{R}_{cp} \mathbf{\eta}(k)$$
(1)

with the $N \times N$ inter-block interference (IBI) matrix $H_{IBI} \triangleq R_{cp}H_1T_{cp}$, the $N \times N$ inter-symbol interference (ISI) matrix $H_{ISI} = H_{IBI}P$, and the permutation matrix $P = \begin{bmatrix} \mathbf{0}_{G \times (N-G)} & \mathbf{I}_G \\ \mathbf{I}_{N-G} & \mathbf{0}_{(N-G) \times G} \end{bmatrix}$. The $N \times N$ matrix $C = W_N^H D W_N$ in (1) is circulant, regardless of the relative magnitudes of *G* and *Q*. Moreover, the $N \times N$ matrix *D* signifies the channel transfer-function matrix, which is diagonal for $G \ge Q$, with its (k, k)th entry equal to the *k*th DFT coefficient of the channel impulse response $\{h(0), h(1), \dots, h(Q)\}$ appended by (N - Q - 1) zeros, i.e. $[D]_{k,k} = \frac{Q}{2} h(a) e^{-j(2\pi/N)kq}$ $k = 0 \dots N - 1$

$$\sum_{q=0} h(q) e^{-j(2\pi/N)kq}, k = 0, \cdots, N-1.$$

Proposed zero-inserting precoder: To suppress the ISI and IBI, but with a length-G insufficient cyclic prefix: [1] proposes inserting 2(Q - G) zero-energy symbols to correspond to the Q - G non-zero columns in H_{IBI} plus the Q - G non-zero columns in H_{ISI} . The present scheme will not incur this 2(Q - G)-symbol overhead, but deploys a guard interval (comprising zero-energy symbols, plus an

optional cyclic prefix) that may be shorter than the channel impulse response. From the data received during the zero-energy symbol intervals, the proposed scheme estimates the combined effects of the signal-of-interest's self-interference, of any multiple-access-user interference, of any overlaid interference, and of the additive noises. These denigrating effects are then 'subtracted' from the information-bearing parts of the symbol block, via a SINR-maximiser in the receiver. This interference-suppression approach philosophically resembles the null-subcarriers-based methods in [2, 3] for OFDM, though the system architectures and the algorithmic details are very different. The present scheme can operate with any non-zero number of zero-energy symbols, with or without a cyclic prefix.

This zero-inserting precoding can be realised by an $N \times (N - P)$ precoding matrix \mathbf{T}_{zero} , formed by inserting P number of all-zero rows into an $(N - P) \times (N - P)$ identity matrix. For example, appending all these zeros would require a precoding matrix of $\mathbf{T}_{zero} = \begin{bmatrix} \mathbf{I}_{(N-P)\times(N-P)} \\ \mathbf{0}_{P\times(N-P)} \end{bmatrix}$.

Proposed two-stage equaliser: At the receiver, (1) remains valid despite the zero-inserting precoder, but now has $u(k) = T_{zero}s(k)$. The proposed linear equaliser involves a post-FFT linear single-tap-per-subcarrier frequency-domain equaliser (FDE) W, followed by a post-IFFT signal-to-interference-and-noise (SINR) maximiser in the time-domain. These are shown in Fig. 1.



Fig. 1 Proposed zero-inserting precoder and proposed two-stage equaliser

The first stage is a single-tap-per-subcarrier frequency-domain linear equaliser (FDE):

$$\boldsymbol{W} = \boldsymbol{D}^{H} \left(\boldsymbol{D} \boldsymbol{D}^{H} + \frac{1}{\text{SNR}} \mathbf{I}_{N} \right)^{-1}$$
(2)

where superscript H denotes complex-conjugate transposition, SNR $\stackrel{\text{def}}{=} \sigma_s^2 / \sigma_n^2$, σ_s^2 refers to the signal power, and σ_n^2 symbolises the noise power. The $N \times N$ diagonal W of (4) reduces the signal-of-interest's energy in the zero-energy symbol-intervals. (This W would constitute a linear minimum-mean-square-error (LMMSE) equaliser, if no interference existed and if $G \ge Q$.) The output of W equals

$$\mathbf{y}(k) = \mathbf{W}_{N}^{H} \mathbf{W} \mathbf{W}_{N} \underbrace{\{ \mathbf{C} \mathbf{u}(k) - \mathbf{H}_{ISI} \mathbf{u}(k) + \mathbf{H}_{IBI} \mathbf{u}(k-1) + \mathbf{n}(k) \}}_{=\mathbf{x}(k)}$$
(3)

For the second stage:

(a) Form a $P \times N$ 'zero-selection' matrix, to block all informationbearing symbol-intervals (which have non-zero energy at transmission); e.g. $J_{zero} = [\mathbf{0}_{P \times (N - P)} | \mathbf{I}_{P \times P}]$ would be compatible with the earlier defined T_{zero} .

(b) Also form a $(N - P) \times N$ 'zero-removal' matrix, to remove the precoder-inserted zeros; e.g. $R_{zero} = [\mathbf{I}_{(N-P) \times (N-P)} | \mathbf{0}_{(N-P) \times P}]$ would be compatible with the earlier defined T_{zero} and J_{zero} .

Next, form the $(N - P) \times P$ matrix U, to minimise the mean-squared error ξ between the signal-output from R_{zero} and J_{zero} , i.e.

$$\xi_{\min} = \min_{U} \underbrace{E[\|\boldsymbol{i}(k) - \boldsymbol{U}\boldsymbol{J}_{zero}\boldsymbol{y}(k)\|_{2}^{2}]}_{\triangleq_{\xi}}$$
(4)

where $i(k) \triangleq R_{zero} W_N^H W W_N [-H_{ISI} u(k) + H_{IBI}(u)(k-1) + n(k)]$ represents the interference and noise in the information-bearing symbol durations. The optimisation in (6) can be solved via the principle of orthogonality, i.e. $E[UJ_{zero}y(k)(i(k) - UJ_{zero}y(k))]^H] = 0$, to yield $U = R_{zero}R_{i(k)i(k)}J_{zero}^H [J_{zero}R_{y(k)y(k)}J_{zero}^H]^{-1}$ which may be pre-calculated

ELECTRONICS LETTERS 23rd June 2011 Vol. 47 No. 13

offline, using the prior knowledge that

$$R_{i(k)i(k)} \triangleq W_N^H WW_N \{H_{ISI} R_{u(k)u(k)} (W_N^H WW_N H_{ISI})^H + H_{IBI} R_{u(k-1)u(k-1)} (W_N^H WW_N H_{IBI})^H + R_{n(k)n(k)} (W_N^H WW_N)^H \}$$
(5)

Lastly, the $(N - P) \times 1$ transmitted symbol vector s(k) is estimated by the receiver as $\hat{s}(k) = (\mathbf{R}_{zero} - UJ_{zero})\mathbf{y}(k)$.

The real-time computational complexity of this proposed precoder/ equaliser scheme is compared in Table 1 against the customary LMMSE-FDE (i.e. (4) alone, without the precoder and without the SINR-maximiser) in terms of N and P. As W and U may be pre-computed offline, while T_{zero} , R_{zero} and J_{zero} involve no multiplication nor addition, these do not contribute to the real-time computational load.

 Table 1: Proposed scheme's computational complexity against that of the customary LMMSE-FDE

Number	LMMSE-based FDE	Proposed two-stage equaliser
Number of complex-value multiplications	$N \log_2 N + N$	$N\log_2 N + N + (N - P)P$
Number of complex-value additions	$2N \log_2 N$	$2N\log_2 N + (N-P)P$



Fig. 2 *BER* performance of proposed algorithm with P zero-energy symbols inserted at end of symbol-block against MMSE-FDE with length of G = 6 *CP* inserted where $P \le G$

Channel has exponential decay with $T_s/T_{rms} = 1/4$

Monte Carlo simulations: The information-bearing symbols $\{s(k)\}$ are modulated with equi-probable QPSK-symbols. The transmitted signal has N = 64. The channel impulse response has Q + 1 = 11 complex-valued taps, each randomly generated and not cross-correlated among themselves. Each tap's real-value part and imaginary-value part are not cross-correlated. Each tap is Gaussian, zero-mean. The *q*th tap has an exponentially decaying variance of $\sigma_q^2 = (1 - e^{-T_s/T_{RMS}})e^{-qT_s/T_{RMS}}$, $\forall q = 0, \ldots, Q$, where T_s denotes the sampling period, and T_{RMS} symbolises the root-mean-square delay-spread of the channel. The additive noise is complex-value, temporally uncorrelated, zero-mean, Gaussian, with a noise power of $\sigma_{\eta(k)\eta(k)}^2$.

Consider these two curves in Fig. 2:

- (i) the curve at G = 6 and P = 0 (i.e. an insufficient CP but no zeroinserting precoding), against
- (ii) the curve at G = 0 and P = 6 (i.e. no CP but six zeros inserted by the precoder, as proposed in this Letter).

These two curves both incur the overhead of P + G = 6 symbols, but the proposed scheme lowers the BER by $1 - 9 \times 10^{-4}/3.8 \times 10^{-3} =$ 76% at SNR = 15 dB, and by $1 - 3.2 \times 10^{-5}/1.4 \times 10^{-3} =$ 98% at SNR = 25 dB. Alternatively, if the transmission overhead is lightened to just four inserted zeros (i.e. (6 + 64) - (4 + 64)/6 + 64 = 2.9%reduction overhead on the data-rate) but no cyclic prefix, then the proposed scheme can still lower the BER by $1 - 2.9 \times 10^{-3}/3.8 \times 10^{-3} = 24\%$ at SNR = 15 dB, and by $1 - 7.8 \times 10^{-4}/1.4 \times 10^{-3} =$ 44% at SNR = 25 dB.

In terms of computational complexity, the proposed scheme would increase the popular LMMSE-FDE method's number of complex-value multiplications by 60% and the number of complex-value additions by 45%, at N = 64 and P = 6 as for the bottom curve in Fig. 2.

Conclusion: For cyclic-prefixed block-based single-carrier-based communication systems, this proposes a zero-inserting time-domain precoder and an accompanying two-stage equaliser, to allow an insufficient guard interval, in order to reduce the transmission overhead. This proposed precoder is predefined offline and requires no iteration, no feedforward, and no decision feedback.

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