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## High-performance resistive switching characteristics of programmable metallization cell with oxidized Cu-Ti electrodes

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Programmable metallization cell (PMC) memory devices with oxidized Cu-Ti alloy films as the bottom electrodes have been shown to exhibit a superior on/off state current ratio (memory window) of as high as  $10^3$  and endurance of 3000 cycles as compared to conventional pure copper and unoxidized Cu-Ti alloy electrodes. It was conjectured that the Cu-Ti alloy electrodes could obtain the appropriate amount of copper atoms to format and rupture the conductive filaments in the resistive switching layer. Furthermore, the oxidized Cu-Ti alloys could control the Cu cations from the Cu and Cu<sub>2</sub>O to the appropriate amountto achieve the most favorable PMC characteristics. © 2013 AIP Publishing LLC. [http://dx.doi.org/10.1063/1.4823818]

In recent years, resistive random access memory (RRAM) has received significant attention owing to its simple fabrication process, low energy operation, and high stackability for use in the next-generation of nonvolatile memory (NVM) technology. RRAM devices fabricated with electrochemical active metal electrodes, such as Ag or Cu, are also known as programmable metallization cells (PMCs) or conductive bridging RAM (CBRAM). 1-3 The resistive switching phenomena during the set and reset process of the PMC devices is attributed to the formatting and rupturing of the nanoscale metallic conductive filaments (CFs) in the resistive switching layer, respectively.<sup>2-6</sup> During the reset process of the PMC, CFs are ruptured via the reduction-oxidation (redox) process, and most of the metal cations drift to the active electrode. CFs can also be broken and lead to the presence of fragmented metal clusters or atoms in the resistive switching layer. 7-9 An excessive amount of copper atoms in the resistive switching layer can reduce the resistance causing the reset failure of PMCs after a number of resistive switching cycles. <sup>3,10,11</sup> Consequently, many methods have been proposed to limit the amount of metal atoms injected from the active electrode, such as the use of metal compounds as the active electrode, 12-16 adding dissolved metal to the insulating layer, <sup>17,18</sup> and putting metal oxide layers on top of the active electrodes. 19 On the other hand, oxidized Cu in form of CuO makes it more difficult to release the Cu atoms via the redox process than Cu2O and metallic Cu.<sup>20–23</sup> It is thus feasible that oxidizing copper can limit the release of Cu atoms.

This study proposes using oxidized Cu-Ti alloy oxide films as the bottom electrodes of PMCs in order to limit the injection of copper atoms into the resistive switching layer, resulting in the good reliability through repeated operations, and a large memory window.

First, a 50 nm-thick Pt layer was deposited on the oxidized silicon wafer as the contact metal using a direct-current (DC) sputtering system. Next, a 300 nm-thick Cu-Ti alloy metal layer was deposited on the Pt contact as the bottom electrode with varied Cu:Ti co-sputtering power. Cu-Ti alloys were produced with the following co-sputtering

powers of Cu and Ti, 10W:170W, 30W:170W, and 50 W:170 W, and these are denoted as 10 W Cu-Ti, 30 W Cu-Ti, and 50 W Cu-Ti, accordingly. The samples were subsequently thermal oxidized at 600 °C for 5 min in oxygen ambient with a rapid thermal annealing (RTA) system. The resulting oxidized Cu-Ti specimens are denoted as 10 W Cu-Ti ox, 30 W Cu-Ti ox, and 50 W Cu-Ti ox, respectively. After the oxidation process, a 200 nm-thick TiO<sub>2</sub> layer was reactively sputtered on the bottom electrode as the resistive switching layer, with the Ti target at a power of 190 W, a total pressure of  $5 \times 10^{-3}$  torr, and the flow rates of 22.4 and 9.6 sccm for the Ar and O2 gases, respectively. Finally, a 50 nm-thick Pt layer was deposited as the disk-shaped top inert electrodes with the diameter of  $356.9 \,\mu m$  (area of approximately 100 042  $\mu$ m<sup>2</sup>) through the shadow mask. For comparison purposes, a conventional pure Cu bottom electrode called Pure Cu, and an oxidized pure Cu one denoted as Pure Cu ox were also fabricated. The compositions of the Cu-Ti layers and the chemical bonding states of the Cu-Ti oxide layers were examined with x-ray photoelectron spectroscopy (XPS). The resistive switching characteristics were measured at room temperature in the dark with the unique electrode area by an Agilent 4156 C precision semiconductor parameter analyzer.

The atomic concentrations of Ti and Cu for the asdeposited Cu-Ti alloy layers have been calculated with XPS via the areas of the binding energy peaks of Ti 2p and Cu 2p<sub>3</sub>, as listed in Table I. 13,16 The ratio of Cu to Ti was increased along with the co-sputtering power of Cu. The DC sweep I-V curves of the unoxidized electrode specimens 10 W Cu-Ti, 30 W Cu-Ti, 50 W Cu-Ti, and Pure Cu were started in the reverse bias of 0 V to -4 V for the set process, and then in the forward bias of 0 V to +3 V for the reset process, as shown in Figure 1(a). The current compliance  $(I_{Comp})$  was set at 0.01 A to protect the device from breaking down. A schematic diagram of the I-V measurement is shown in the inset of Figure 1(a). It can be seen that the DC set voltages of 10 W Cu-Ti, 30 W Cu-Ti, 50 W Cu-Ti, and Pure Cu specimens were -1.9 V, -1.7 V, -1.6 V, and -1.5 V, defined by the currents abruptly increased to the compliance valve during the reverse

TABLE I. The compositions of the Cu and Ti atoms in atomic ratios for the as-deposited Cu-Ti alloys at different co-sputtering powers for the Cu components and the pure Cu specimen based on the ratios of the XPS peak areas corresponding to Cu 2p3 and Ti 2p.

Composition	10 W Cu-Ti	30 W Cu-Ti	50 W Cu-Ti	Pure Cu
Cu 2p <sub>3</sub>	36%	41%	52%	100%
Ti 2p	64%	59%	48%	0%

DC bias sweep, respectively. In order to fairly compare the repeated reliability, the operating parameters with the set/reset pulse voltages of -8 V/+3 V for setting/resetting process, the read voltage of 0.1 V for reading the ON-state/OFF-state currents, with the pulse width of  $1 \mu s$ , and the  $I_{Comp}$  of 0.01 Awere applied for all the samples. The endurance measurements were performed after the DC bias sweep measurements for the same sample and according to the following sequences of operating cycle: setting, reading for ON-state current, resetting, reading for OFF-state current, and back to setting. The repeated resistive switching characteristics for the 10 W Cu-Ti samples were exhibited 1000 endurance cycles with an initial on/off current ratio (memory window) of 2.3 orders, 800 endurance cycles and 2.2 orders for the 30 W Cu-Ti ones, 600 endurance cycles and 2.1 orders for the 50 W Cu-Ti ones, and 400 endurance cycles and 1.7 orders for the Pure Cu ones, as shown in Figure 1(b). For the unoxidized electrode specimens, the DC set voltages, endurances, and on/off currents were negatively related to the Cu compositions for the pure Cu and Cu-Ti alloys, as seen in Figures 1(a) and 1(b), and listed in Table I. It was conjectured that the increased Cu cations of the electrodes would diffuse and drift into the TiO<sub>2</sub> resistive switching layers, and thus the driving electrical fields needed to format the CFs could be reduced, lowering the DC set voltages. On the other hand, the excessive amount of Cu atoms residing in the TiO<sub>2</sub> resistive switching layers after the reset process would increase the off state currents and the reset failure probability, thus resulting in shorter resistive switching cycles and the lower memory windows than seen with a lower Cu composition.

As for the oxidized electrode samples, the DC sweep I-V curves of the 10 W Cu-Ti ox, 30 W Cu-Ti ox, 50 W Cu-Ti ox, and Pure Cu ox were obtained with the same measurement parameters as the unoxidized electrode ones, as shown in Figure 2(a). It can be seen that all the 30 W Cu-Ti ox, 50 W Cu-Ti ox, and Pure Cu ox specimens had resistive switching properties, except the 10 W Cu-Ti ox ones. The DC set voltages of 30 W Cu-Ti ox, 50 W Cu-Ti ox, and Pure Cu ox samples were  $-2.2 \,\mathrm{V}, -1.6 \,\mathrm{V}, \text{ and } -3.5 \,\mathrm{V}, \text{ respectively.}$  The repeated resistive switching characteristics for the 30 W Cu-Ti ox specimens achieved 3000 endurance cycles with the initial memory window of 3.8 orders, 800 endurance cycles and 3 orders for the 50 W Cu-Ti ox ones, and 1000 endurance cycles and 3 orders for the Pure Cu ox ones, as shown in Figure 2(b). The 30 W Cu-Ti ox samples obviously had the best repeated resistive switching characteristics, including the greatest endurance and the largest memory window of all these specimens. In order to better understand the significant differences among the resistive switching characteristics for the oxidized pure Cu and Cu-Ti samples, the chemical bonding states of the Cu elements of the oxidized electrodes, including the Cu-Ti 10 W ox, Cu-Ti 30 W ox, Cu-Ti 50 W ox, and Pure Cu ox ones, were analyzed based on the XPS data, as shown in Figures 3(a)-3(d), respectively. The XPS results for the Cu 2p<sub>3</sub> core level can be divided into three sub-peaks approximately centered at 932.2 eV, 932.7 eV, and 933.8 eV by Gaussian fitting, respectively, for the metallic Cu, the copper ions (cations) with the oxidation state of Cu<sup>+</sup> (Cu<sub>2</sub>O), and the copper cations with the oxidation state of Cu<sup>2+</sup> (CuO). In addition, the XPS results for the Ti 2p core level can also be divided into three sub-peaks approximately centered at 460.1 eV, 462.0 eV, and 464.7 eV, respectively, for the metallic Ti, Ti<sup>3+</sup> (Ti<sub>2</sub>O<sub>3</sub>), and Ti<sup>4+</sup> (TiO<sub>2</sub>). The proportions of these sub-peaks can be calculated from the peak areas. <sup>16</sup> The compositions of the metallic Cu, Cu<sup>+</sup>, Cu<sup>2+</sup>, metallic Ti, Ti<sup>3+</sup>, and Ti<sup>4+</sup> for the oxidized Cu-Ti alloy and oxidized pure Cu were assessed, and these are listed in Table II.

According to previous reports, <sup>20–23</sup> Cu<sub>2</sub>O can release Cu atoms via the redox process, which can be described with the following reaction equation:

$$Cu_2O \rightarrow CuO + Cu$$
. (1)

Therefore, both the metallic Cu and Cu<sub>2</sub>O could contribute the majority of the Cu cations to format the CFs for the oxidized Cu-Ti alloys and oxidized pure Cu electrodes. In contrast, CuO is the higher oxide of copper, and thus the Cu atoms have stronger bonds to the oxygen atoms, making it more difficult to release the Cu cations in comparison to the metallic Cu and Cu<sub>2</sub>O. On the other hand, the metallic Ti and Ti oxides (Ti<sub>2</sub>O<sub>3</sub> and TiO<sub>2</sub>) are not the chemically active metal sources, and thus cannot format the CFs. Consequently,

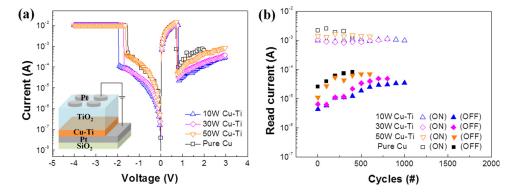


FIG. 1. (a) The DC sweep I-V curves and structure diagrams of the unoxidized electrode specimens. (b) Endurances of the unoxidized electrode specimens.

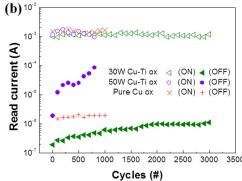


FIG. 2. (a) The DC sweep I-V curves and structure diagrams of the oxidized electrode samples. (b) Endurances of the oxidized electrode samples.

as demonstrated in Table II and Figures 2(a) and 2(b), the 10 W Cu-Ti ox samples had the least Cu and Cu<sub>2</sub>O contents among all the oxidized Cu and Cu-Ti alloy samples, and thus exhibited no resistive switching property. In contrast, the 50 W Cu-Ti ox ones possessed the highest Cu and Cu<sub>2</sub>O contents, and thus it is easier to format the CFs using these during the set process, with the lowest DC set voltage, although these are also more likely to lead to Cu atoms being present in the TiO<sub>2</sub> resistive switching layer during the reset process, resulting in the smallest final memory window and the worst endurance among all the oxidized specimens examined in this work. On the other hand, the Pure Cu ox ones have the second most Cu and Cu<sub>2</sub>O contents, with a moderate final memory window and endurance. As for the 30W Cu-Ti ox ones, they had the most appropriate amount of the Cu and Cu<sub>2</sub>O, and thus the greatest final memory window and endurance. Since it is difficult for CuO to release the Cu cations needed to format the CFs, and the Pure Cu ox specimen has the most CuO, it also has the largest DC set voltage among all the oxidized electrodes. It is worth noting that the  $30\,\mathrm{W}$  Cu-Ti ox specimens could obtain the appropriate quantity of Cu cations from the metallic Cu and Cu<sub>2</sub>O, and thus have excellent set and reset properties. As compared to the unoxidized Cu-Ti and pure Cu electrodes, the oxidized ones could significantly reduce the amount of Cu cations released from the metallic Cu and Cu<sub>2</sub>O, thus improving the memory window and endurance.

In summary, high-performance PMC devices with oxidized 30 W Cu-Ti alloys as the active electrodes have been demonstrated to achieve a memory window of 3.8 orders and endurance as long as 3000 cycles, as compared to 1.6 orders and 400 cycles for the conventional pure Cu electrode samples, and 2.3 orders and 1000 cycles for the unoxidized 10 W Cu-Ti ones. It was conjectured that the oxidized Cu-Ti alloys

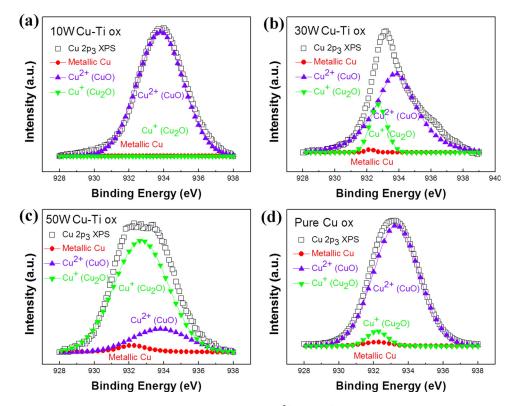


FIG. 3. XPS data of the Cu  $2p_3$  core level divided into three peaks (Metallic Cu,  $Cu^{2+}$ , and  $Cu^{+}$ ) by Gaussian fitting for the sample of (a) 10 W Cu-Ti ox, (b) 30 W Cu-Ti ox, (c)50 W Cu-Ti ox, and (d) Pure Cu ox.

TABLE II. The compositions of the metallic Cu,  $Cu^{2+}$ ,  $Cu^{+}$ , metallic Ti,  $Ti^{4+}$ , and  $Ti^{3+}$  in atomic ratios for the oxidized Cu-Ti alloys at different cosputtering powers for the Cu components and the oxidized pure Cu specimen based on the ratios of the XPS sub-peak areas corresponding to Cu  $2p_3$  and Ti 2p.

Bonding states	10 W Cu-Ti ox	30 W Cu-Ti ox	50 W Cu-Ti ox	Pure Cu ox
Metallic Cu	0.4%	0.8%	3.6%	4.0%
Cu <sup>2+</sup> (CuO)	35.2%	34.9%	8.9%	90.0%
Cu <sup>+</sup> (Cu <sub>2</sub> O)	0.4%	5.3%	39.5%	6.0%
Metallic Ti	1.2%	0.6%	0.2%	0%
Ti <sup>4+</sup> (TiO <sub>2</sub> )	60.7%	56.9%	47.2%	0%
$Ti^{3+}\left(Ti_{2}O_{3}\right)$	2.1%	1.5%	0.6%	0%

could significantly control the release of Cu cations from the metallic Cu and Cu<sub>2</sub>O by achieving an appropriate amount for formatting and rupturing the CFs in the resistive switching layers. PMCs with oxidized Cu-Ti alloys as the active electrodes are thus promising for the next-generation of non-volatile memory devices.

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