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Citation: *Applied Physics Letters* **103**, 232904 (2013); doi: 10.1063/1.4838297

View online: <http://dx.doi.org/10.1063/1.4838297>

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Evolution of the conductivity type in germania by varying the stoichiometry

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(Received 16 September 2013; accepted 17 November 2013; published online 2 December 2013)

Information regarding the conductivity type of Si/GeO_x/Ni structures with various stoichiometry has been obtained using experiments on injection of minority carriers from *n*- and *p*-type silicon. Results show that non-stoichiometric GeO_x films exhibit bipolar conductivity, that is, holes as well as electrons contribute to the charge transport. Stoichiometric GeO₂ films exhibit unipolar electron conductivity. © 2013 AIP Publishing LLC. [<http://dx.doi.org/10.1063/1.4838297>]

Amorphous GeO₂ films are used as gate insulators in high mobility field effect transistors based on Ge.¹ Germania films also show resistive switching effect, which is promising for the next generation of high performance non-volatile flash memory.^{2,3}

The energy diagram of the Ge/GeO₂ structure was calculated in Ref. 4, using quantum chemical simulation. According to these calculations, highly asymmetric electron and hole barriers on the Si-GeO₂ interface are predicted. The barrier for the electrons is 1.3 eV and the hole's barrier is equal to 3.2 eV. The disordered GeO_x gap value of 5.6 eV is less than the 6.1 eV calculated for crystalline germania.⁵

One of the most critical parameters of metal-oxide-silicon (MOS) structures is the sign of carriers, which contribute to the current in the insulator. The Hall effect and thermoelectric experiments cannot be performed to determine the carrier's sign in dielectrics. However, two specific methods can be used to determine the carrier's sign in the insulator. These methods are carrier sign determination in an MOS transistor,⁶ and current saturation caused by non-equilibrium minority carrier generation in the depletion layer of the silicon.

Insulators can have two-band (or bipolar) conductivity, as in Si₃N₄,^{6,7} HfO₂,⁸ and TiO₂,⁹ or monopolar charge transport (e.g., by the electrons), as in Al₂O₃,¹⁰ and MOS with thermal SiO₂.¹¹ The knowledge of the carrier sign in the insulator is crucial for the interpretation of charge transport in MOS devices, as well as for the development of correct models for resistive memory switching.

In this letter, we report on the charge carrier sign determination in silicon MOS structures based on germania GeO_x.

Samples were cleaved from the wafers of Si with GeO_x film with a thickness of $d = 20$ nm. The GeO_x films were deposited using physical vapor deposition (PVD) on *p*- and *n*-type Si substrates. Samples from the first group "PDA0" were measured as deposited, that is, they were not treated using post-deposition annealing (PDA), and GeO_x films represented non-stoichiometric germania. The same GeO_x films

are used in ReRAM devices.² The second "PDA5" and the third "PDA20" groups of samples were treated by applying PDA in an O₂ atmosphere at 300 °C for 5 min and 20 min, respectively. The 300 °C annealing is a very low temperature process for SiO₂ sublayer growing.¹² Ellipsometry measurements were used to control the thickness of GeO_x films. Structural analysis shows that the resulting GeO_x films were amorphous.

The samples for transport measurements were equipped with Ni gates of round form and a radius of 70 μm for electrical contact. Current-voltage (*J*-*V*) and capacitance-voltage (*C*-*V*) measurements were recorded at room temperature. *C*-*V* measurements were recorded at a frequency of 100 kHz. A tungsten lamp was used for light illumination. The permittivity ϵ was calculated from *C*-*V* measurements, using the standard formula of a flat capacitor $\epsilon = Cd/\epsilon_0$ in the accumulation mode, where *C* is specific capacitance and ϵ_0 is the electric constant (vacuum permittivity).

Figures 1(a) and 1(b) show *J*-*V* and *C*-*V* dependencies for *n*-Si/GeO_x/Ni MIS structures from the "PDA0" group for depletion and accumulation modes in the dark (solid line) and under illumination (dashed line). In accumulation mode, when a positive potential is applied to the Ni contact, almost all the applied voltage drops across the dielectric material, and the current increases exponentially with the increasing electric field. When a negative potential is applied to the metal contact, that is, in the depletion mode, the current increases exponentially at low voltages. The current saturation appears at a sufficiently large voltage, and the saturation current and its increase under illumination indicate, that in the depletion mode minority carriers are injected from Si into GeO_x. The minority carriers are holes in this case. Independent confirmations of the minority carriers injection include: (1) the existence of capacitance transition from inversion mode to non-equilibrium depletion mode, and (2) capacity increasing in non-equilibrium depletion mode under illumination (Fig. 1(b)).

Dependencies for *p*-Si/GeO_x/Ni MIS structures from the "PDA0" group for depletion and accumulation modes in the dark (solid line) and under illumination (dashed line) are shown for experimental *J*-*V* and *C*-*V* in Figs. 1(c) and 1(d), respectively. In accumulation mode, the current grows

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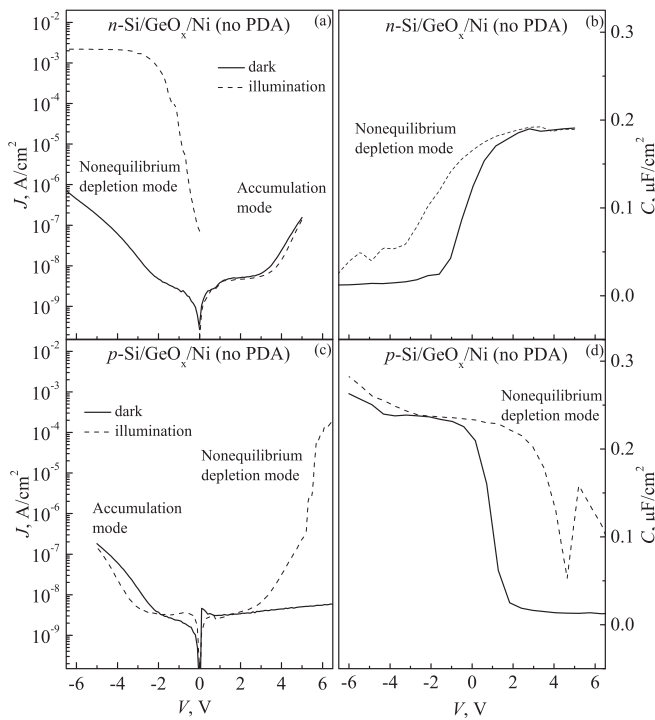


FIG. 1. Experimental J - V and C - V curves for Si/GeO_x/Ni MIS structures of “PDA0” group for depletion and accumulation modes in the dark (solid line) and under illumination (dashed line). (a) J - V and (b) C - V of n -Si/GeO_x/Ni structures. (c) J - V and (d) C - V of p -Si/GeO_x/Ni.

exponentially with increasing voltage. Current saturation appears in depletion mode, and the saturation level increases under illumination. This phenomenon indicates the injection of minority carriers, namely electrons, from the silicon substrate into germanium oxide.

The results demonstrate that GeO_x conductivity is bipolar, similar to Si₃N₄,^{6,7} HfO₂,⁸ and TiO₂.⁹

Energy band diagrams of n, p -Si/GeO₂/Ni structures, based on calculations of GeO₂ electronic band structure,^{2,4} are shown in Fig. 2 in flat band mode (Figs. 2(a) and 2(b)), in accumulation mode (Figs. 2(c) and 2(f)), and depletion mode (Figs. 2(d) and 2(e)). It is shown that the barrier for holes in the Si/GeO₂ interface is much higher than the barrier for electrons. Thus, it is expected that the transport in GeO₂ is unipolar. The difference of theoretical expectations from experimental results might be explained by one of two hypotheses. The first is based on the assumption that GeO_x films are not pure GeO₂, and have different energy structures, that is, the energy barrier for holes in experiments is less than that in calculations. This difference increases injection probability of the holes from the Si substrate into the dielectric medium. The second hypothesis assumes that GeO_x films exhibit hopping conductivity of the holes between hole traps far from the top of the valence band in the middle of the gap. If the trap density is sufficiently high, the holes hop inside of the band gap, as shown in Figs. 2(e) and 2(f).¹³

To investigate evolution of the trap density, J - V and C - V dependencies were measured for Si/GeO₂/Ni MIS structures after low PDA. The samples of Si/GeO₂/Ni after 5 min of PDA present the same behavior as with no PDA. The conductivity of the samples from “PDA5” group is bipolar.

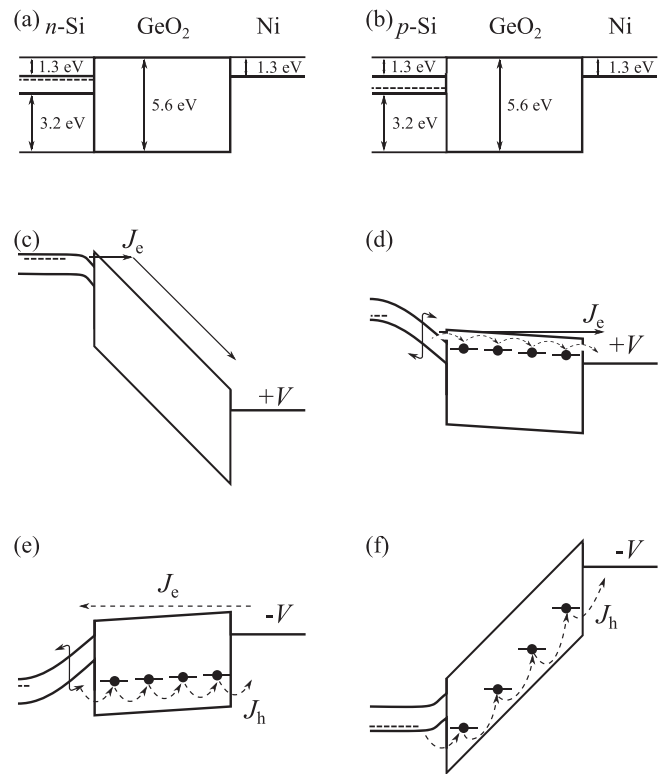


FIG. 2. Energy band diagram of n -Si/GeO₂/Ni (a) and p -Si/GeO₂/Ni (b) structures in flat band mode. The same diagrams in accumulation (c), (f), and depletion mode (d), (e). J_e/J_h are flows of injected electrons/holes from Si into GeO₂. Curved arrows show jumps of electrons (d) and holes (e), (f) between hole traps in the bulk of dielectric.

Further PDA procedures led to the disappearance of current and capacity changes under illumination in n -Si/GeO_x/Ni structures as shown in Figs. 3(a) and 3(b): the capacitance depletion cannot be observed after annealing germania. Despite the dark current is increased, addition photocurrent is not observed in depletion mode. This phenomenon shows that injection of the minority carriers (holes) from Si into GeO_x is suppressed. However, non-equilibrium depletion mode can be observed in p -Si/GeO_x/Ni structures (Figs. 3(c) and 3(d)). These phenomena show that after 300 °C O₂ annealing for long periods, the conductivity of GeO_x evolves from bipolar to unipolar, namely, electronic conduction. In addition, the anomalous capacitance hump shown in Fig. 1(d), which is caused by the existence of deep interface traps, can be eliminated using oxygen annealing for periods longer than 20 min. The capacitance change in accumulation mode after anneal might be caused by the following reasons: (1) densification of poor quality GeO_x (thickness decrease), (2) growing of SiO₂ sublayer, and (3) GeO_x oxidation and changing of GeO_x dielectric constant. Ellipsometry measurements demonstrated that the thickness of GeO_x films with an accuracy of 2% has not changed after PDA. Small thickness increasing took place due to longer Ge–O (than Ge–Ge) bonds were formed by extra O in GeO_x bulk. Additional SiO₂ sublayer should decrease total capacitance of Si/SiO₂/GeO_x/Ni structures. However, the capacitance increase is observed. The higher dielectric constant $\epsilon_0 = 13$ –14 (compared with 4–5 for samples of the “PDA0” and “PDA5” groups) and a low interface state indicates that the well-oxidized GeO₂ can be obtained after 300 °C O₂ annealing for 20 min. These facts

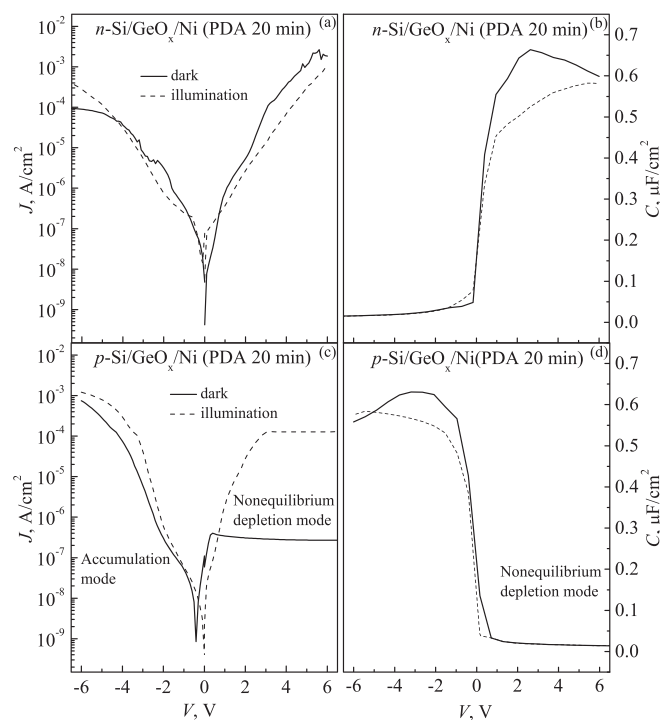


FIG. 3. Experimental J - V (a) and C - V (b) curves for n -Si/GeO_x/Ni MIS structures after 20 min PDA at 300 °C ("PDA20" samples group) in the dark (solid line) and under illumination (dashed line). The depletion mode is not observed in these structures. Experimental J - V (c) and C - V (d) curves for p -Si/GeO_x/Ni MIS structures annealed at the same conditions.

confirm both hypotheses regarding the difference in the energy structures of deposited GeO_x films and pure GeO₂, and the high density of hole traps caused by oxygen vacancies in stoichiometric GeO_x films.

The dielectric constant of pure Ge is 16. The permittivity of tetragonal GeO₂ is evaluated of 12–15.¹⁴ Thus, ϵ_0 of intermediate germania GeO_x is expected to be between 12 and 16. Low ϵ_0 value of 4–5 for non-PDA films does not match the expectations. However, extremely low dielectric constant values of 4.5–11 of germania films were observed earlier.^{15–18} The reason why the dielectric constant values GeO_x do not fall within the expected interval, and ϵ_0 is increased after annealing, is an open question. It is possible

that during PDA treating germania films are recrystallized, and these changes can cause the increase in the dielectric constant.

In summary, this study indicates that the charge carrier sign in GeO_x depends on stoichiometry, that is, on the methods and conditions of MIS device fabrication. Non-stoichiometric GeO_x films exhibit bipolar conductivity, whereas stoichiometric GeO₂ films after post deposition annealing exhibit unipolar electronic conductivity. This study provides a simple and clear method for determining the charge carrier sign.

This work was supported by projects No. 1.13 and 4.18 of the Siberian Branch of the Russian Academy of Sciences and the National Science Council, Taiwan, under Grant No. NSC-100-2923-E-009-001-MY3.

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