

Influence of Oxygen Concentration on Self-Compliance RRAM in Indium Oxide Film

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Abstract—This letter investigates various oxygen concentrations in indium oxide films which induce different resistance switching behaviors, including two self-compliance behaviors and a two-step set process. The accumulated oxygen ions produce an oxygen-rich indium oxide film, which can be considered as a variable series resistor after the forming process. Analyses indicate that the lower self-compliance current can be attributed to this larger variable series resistor from the additional oxygen ions. The more significant oxidation reaction decreases the current of the high resistance state. Hence, power consumption can be reduced effectively.

Index Terms—RRAM, nonvolatile resistance switching memory, indium oxide.

I. INTRODUCTION

RECENTLY, with the advancement of portable electronic products, nonvolatile memories have attracted much attention [1]. With their scaling-down, conventional nonvolatile floating-gate memory has reached its physical limits [2]. Resistive random access memory (RRAM) is considered to replace conventional flash memory due to its advantageous properties of low operating power consumption [3], [4], good scalability, excellent retention, and fast operation speed [5], [6]. In this letter, we investigate the self-compliance behavior of indium oxide film with various oxygen concentrations in the Pt/InO_x/TiN structure. It is notable that such RRAM devices with the indium oxide-based resistance switching (RS) layer including additional oxygen

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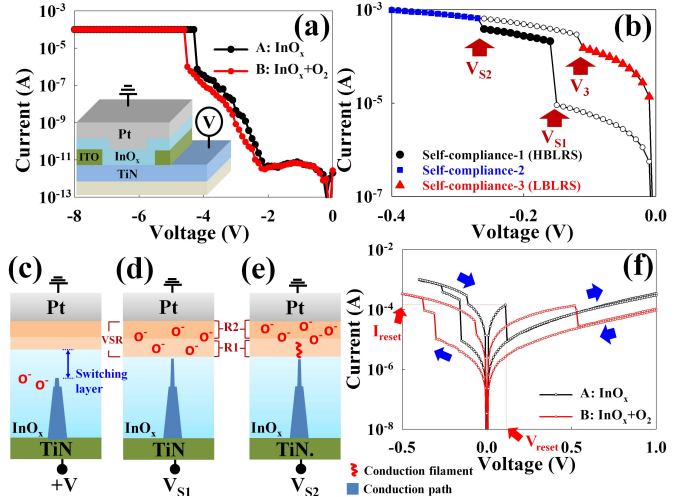


Fig. 1. (a) The forming process of samples A and B, and the inset shows structural diagram. (b) Bipolar resistance switching of sample A during the set process. The resistance switching mechanism during (c) reset process, (d) set process of self-compliance-1, and (e) set process of self-compliance-2. (f) The current-voltage curve for samples A and B.

ions can produce the desirable properties of low power consumption and low operation current.

II. EXPERIMENT

The 200 nm-thick low temperature silicon oxide (LTO) was deposited on TiN/SiO₂/Si structure and the cell area of $2 \times 2 \mu\text{m}$ was defined by reactive ion etching the LTO. The RS layer of 20-nm indium oxide (InO_x) was deposited on the TiN by RF sputtering an In₂O₃ target in Ar (30 sccm) ambience at room temperature, defined as sample A. Similarly, RS layer for sample B was also deposited on the substrate in Ar (30 sccm) and O₂ (10 sccm) ambience at room temperature for comparison. Subsequently, the Pt top electrode was deposited on the InO_x film by DC sputtering. During the measurement process, bias was applied on the TiN bottom electrode and the Pt top electrode was grounded, as shown in Fig. 1(a). All the RS characteristics were measured by an Agilent B1500 semiconductor parameter analyzer and a Cascade M150 probe station. In addition, the compositions of the indium oxide films were analyzed by X-ray photoelectron spectroscopy (XPS).

III. RESULT AND DISCUSSION

During the forming process, the conduction paths were constructed by applying +8 V with a 100 μA current compliance

on both samples A and B, and the resistance state was switched to low resistance state (LRS) after the forming process. The forming voltage is defined as the voltage at the point where the current increases abruptly during the forming process. The forming voltage of sample A is lower than that of sample B, as shown in Fig. 1(a).

Figure 1(b) shows the current-voltage (I-V) curve of sample A during the set process, where the resistance state can be switched from high resistance state (HRS) to LRS with a negative voltage. The set voltage (V_{set}) is defined as the voltage at the point where the current increased abruptly. The conduction path of a metal-like filament is formed and large amount of oxygen ions drift to the Pt electrode due to the electric field direction during the forming process. Since the oxygen ions cannot react with Pt [7], the indium oxide layer with high concentration of oxygen forms a variable series resistor (VSR) between the conduction path and the Pt electrode, and the VSR region can be considered as an oxygen reservoir [8]–[10]. Because of the formation of the VSR, the self-compliance behavior is observed without an external current limit [11]. As the oxygen ions migrate to the conduction path due to the positive voltage, the oxygen ions react with indium atoms at the end conduction path and the resistance state is switched back to HRS (reset process), as shown in Fig. 1(c). The reset voltage (V_{reset}) is defined as the voltage at the point where the current starts its decrease and an insulator layer (switching layer) between the VSR and the end of the conduction path is formed, as shown in Fig. 1(c).

The two-step V_{set} as well as the two self-compliance behaviors are observed in the I-V curve during the set process, shown in Fig. 1(b). The current increases abruptly at V_{S1} , indicating that the conduction path is re-formed and the self-compliance-1 (higher bias LRS; HBLRS, ranging from V_{S1} to V_{S2}) behavior is observed due to the formation of the VSR region, as shown in Fig. 1(d). Furthermore, as the bias continuously rises, the current abruptly increases at V_{S2} since the resistor of the indium oxide of a lower oxygen concentration (R1) in the VSR region undergoes soft breakdown by the higher electric field at end of the conduction path, and the formation of conduction filament (CF) is formed in VSR region, as shown in Fig. 1(e). Moreover, the self-compliance-2 behavior can be observed due to the resistor of a higher oxygen concentration (R2) in the VSR region. Subsequently, the current decreases abruptly and the self-compliance-3 (lower bias LRS; LBLRS, ranging from V_3 to 0 V) behavior occurs at V_3 during the bias sweep back to 0 V, because the CF is oxidized by its surrounding high-concentration oxygen ions, as well as the tip of conduction path also being oxidized by the high concentration of oxygen ions. In addition, although the formation of another parallel path at V_{S2} is possible, it is actually not likely. Since the rupture probability of two conduction path is the same, it is difficult to explain why the same conduction path always ruptures at V_3 if there were parallel conduction paths. Hence, the RS mechanism in this manuscript is dominated by the single conduction path mode.

Figure 1(f) shows the typical bipolar RS behavior of samples A and B, including different self-compliance behaviors and the two-step V_{set} . A comparison of I-V curves

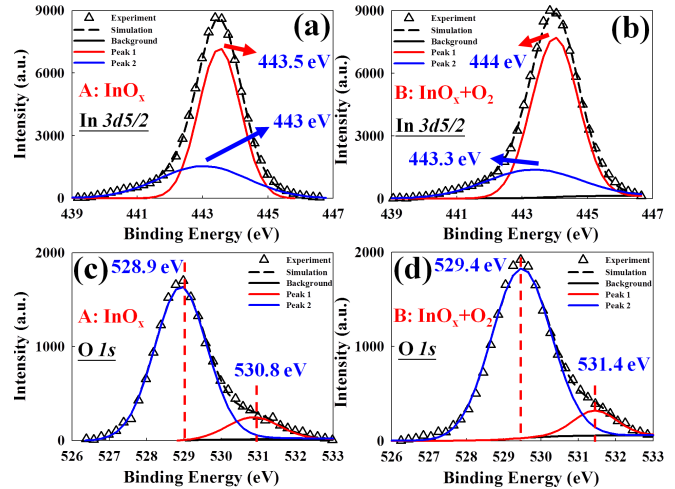


Fig. 2. XPS analysis of (a) In 3d_{5/2} for sample A, (b) In 3d_{5/2} for sample B, (c) O 1s for sample A, and (d) O 1s for sample B.

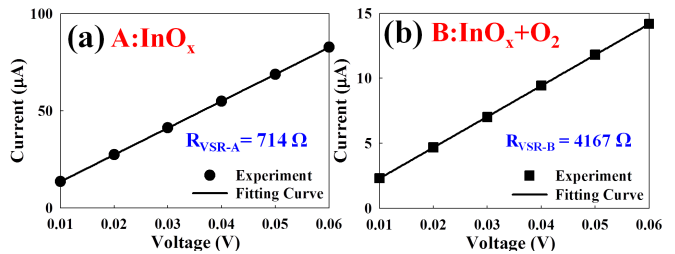


Fig. 3. The current transport mechanism of ohmic conduction (a) for sample A and (b) sample B in LBLRS.

between samples A and B shows that the V_{reset} and V_{set} for sample B is larger than those for sample A, and the operation current for sample B is lower than for sample A. They both exhibit a self-compliance behavior during the set process, with the self-compliance current in sample B being lower than in sample A.

Figure 2 shows the XPS analysis of the compositions of the indium oxide film. The spectrum peak of In 3d_{5/2} and O 1s are separated into two peaks in both samples A and B. In sample A, one peak of In 3d_{5/2} is that of In-O bonding (443.5 eV), and the other peak is In-In bonding (443 eV), as shown in Fig. 2(a). Furthermore, the Fig. 2(c) shows that the peaks of O 1s in sample A are located at 528.9 eV and 530.8 eV, representing In-O bonding and the oxygen ions in the oxygen deficient region [12], respectively.

In order to investigate the chemical states of the indium oxide film in samples A and B, the peak areas are calculated by the Lorentzian-Gaussian function. The composition ratio of O elements to In elements in sample A (0.95:1) is smaller than in sample B (1:1), which indicates that sample B with additional oxygen atoms can effectively enhance the oxygen concentration. Compared to sample A, the composition ratio of In-O to In-In bonding is increased in sample B. This result indicates that the RS layer with additional oxygen ions can reduce the quantity of defects and enhance the oxidation of In atoms.

In order to investigate the current transportation mechanism of LRS, the conduction mechanisms are analyzed [13]. Fig. 3(a) and (b) show that the conduction mechanisms of

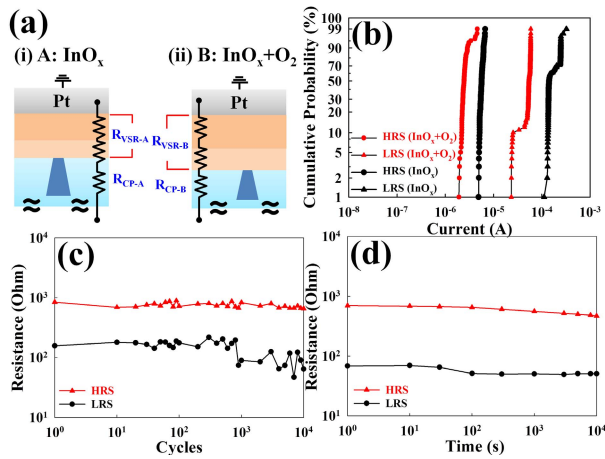


Fig. 4. (a) Schematic diagrams illustrating the formation of VSR and end of conduction path for samples A and B. (b) The cumulative distribution of the HRS and LRS in sample A and B. (c) Endurance for 104 cycles as tested by AC sweeping, and (d) the retention for 104 s at 85 °C.

LBLRS in both samples A and B follow Ohmic law. According to the resistance equation ($R = V/I$), the resistance value can be obtained from the slope of Ohmic fitting. The R , V , and I are resistance value of VSR (R_{VSR}), bias voltage, and current, respectively. The R_{VSR} for sample A ($R_{VSR-A} = 714 \Omega$) is smaller than for sample B ($R_{VSR-B} = 4167 \Omega$).

Figure 4(a) shows the schematic diagram of the switching region for samples A and B, which includes both the VSR and the end of conduction path, considered as the two resistors R_{VSR} and R_{CP} in series. Their different R_{VSR} can be obtained from Fig. 3(a) and (b), whose result has demonstrated that R_{VSR-B} is larger than R_{VSR-A} . The conduction path connects with a series VSR and the voltage crossing in VSR region should also be considered. According to simple voltage divider rule ($V_{VSR} = [R_{VSR} / (R_{VSR} + R_{CP})] V_{bias}$) the voltage crossing R_{VSR-B} is larger than R_{VSR-A} for the same bias conditions. Hence, compared to sample A, sample B requires a larger V_{reset} due to the higher voltage crossing R_{VSR-B} along the conduction path.

According to the electric power equation ($P_{reset} = I_{reset} \times V_{reset}$), P_{reset} , I_{reset} , and V_{reset} are reset power, reset current, and reset voltage, respectively. The driving power of sample B ($70.4 \mu W$) is larger than that of sample A ($15.6 \mu W$) and the result indicates that the higher power induces a more significant oxidation reaction, and the thickness of the switching layer of sample B is larger than that of sample A. Hence, the HRS current of sample B is lower than that of sample A. Besides a higher crossing voltage of R_{VSR-B} , because of the thicker switching layer in sample B, electrons require a higher energy to break the metal-oxide bond of the switching layer by a higher set bias. Hence, the V_{set} in sample B is larger than that sample A.

Figure 4(b) shows the obvious memory window read at 0.1 V during 1000 DC operation cycles for sample A and B. The operation current for sample B is lower than that for sample A. Figure 4(c) shows the endurance of 10⁴ cycles for sample B by AC pulse operation. As applying a -0.8 V with a 300 ns pulse width, the resistance state is switched from HRS

to LRS. Conversely, the resistance state is switched back to HRS by a $+0.8$ V operation voltage with a 600 ns pulse width. The result indicates that the device can be switched at least 104 cycles and still retain approximate 1 order of magnitude of on/off ratio. After the 10⁴ cycles of endurance test, the HRS and LRS retain well without degradation longer than 10⁴ s at 85 °C, as shown in Fig. 4(d).

IV. CONCLUSION

In summary, in order to investigate the influence of different oxygen concentrations in an indium oxide-based RS layer, the indium oxide film is deposited by sputtering in ambient Ar (sample A) and in Ar/O₂ (sample B). According to the presented analyses of the electric power, a significant reset behavior lowers the HRS current. The V_{set} in sample B is larger than that of sample A due to the thicker switching layer and higher R_{VSR-B} . According to the resistance value from VSR analysis, the V_{reset} in sample B is larger than in sample A, since a higher resistance value for the VSR forms in B. Furthermore, this higher resistance value can decrease the self-compliance current and achieve lower power consumption due to its lower operation current.

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