

# The Design and Assembly of Surface-Micromachined Optical Switch for Optical Add/Drop Multiplexer Application

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**Abstract**—An assembly process including: flip-chip bonding, microelectromechanical (MEMS) structure release, and atomic layer deposition (ALD) is proposed to integrate a surface micromachined optical switch for optical add/drop multiplexer (OADM) applications. In the current optical switch designs, pre-stressed beams were used to pop up the micromirror and an electrode (substrate) under the beams was designed to perform ON/OFF function of the optical switch. In order to achieve desired popped-up angle for precise optical switching, a flip-chip bonding technique is applied to a mechanical stopper with an accurate joint height that can be used to constrain the movement of the micromirror. A conformal thin layer of dielectric material ( $Al_2O_3$ ) coated on the surfaces of device through an ALD coating process is used to improve vertical actuation force, as well as electrical isolation. Experiments indicate that the micromirrors fabricated by the present assembly process can achieve desired angle that meet the requirements of the proposed OADM configuration.

**Index Terms**—ALD, flip-chip bonding, micromirror, OADM, optical switch.

## I. INTRODUCTION

OPTICAL switches play an important role in optical communication systems. The information exchange is accomplished by switching and routing optics in the optical networks. To reconfigure the network for preserving the network reliability, optical switches are used to bypass failed nodes. Microelectromechanical system (MEMS) technologies provide a solution to manufacture optical switches that enable the cross-connect of light signals completely in the optical domain. The advantages of MEMS-based optical switches include high reliability, low unit cost/size/weight, low insertion loss, low polarization dependent loss, low polarization mode dispersion, fast switching time, and wavelength independent [1]–[5].

In the dense wavelength division multiplexing (DWDM) networking system, OADM selectively drops a wavelength from a multiplicity of wavelengths in a fiber and adds in the same direction of data flow the same wavelength with different

data content. As a result, the specific data could be extracted from or carried into the DWDM networking system such that information could be received or transmit among each communication nodes. An OADM configuration that integrated volume holographic gratings with  $N \times 2$  back-to-back optical switches had been proposed [6]. As illustrated in Fig. 1, the incident lights with different wavelength are coupled into demultiplexer (DMUX) and diffracted into designed position through localized multiplexing method. The optical switches are designed to extract specific information or add the desired one into multiplexer (MUX) by blocking or bypassing the light signals. With this design, the MEMS-based optical switch must have two-states: “OFF” for deflecting the incident lights with micromirror at  $45^\circ$  as well as “ON” for passing incident lights with micromirror at  $0^\circ$ . Note that, the optical switch with  $45^\circ$  popped-up angle brings a great challenge for the requirement of precisely angle control. Mechanical locks [7], [8], actuator platforms [2], [9]–[11] as well as self-assembly by melting the photoresist [12] or by reflowing the solder [13], [14] are the typical methods used to assemble microstructures to reach required position/angle. However, these methods cannot satisfy the required accuracy for the proposed ODAM configuration due to the limitations of fabrication.

In 1996, *Toshiyoshi et al.* proposed the concept of a mechanical stopper [15]. For the developed optical switch matrix, the angle of the mirror at ON-state ( $90^\circ$ ) is controlled precisely since it touches the stopper on the substrate. In this paper, the concept of mechanical stopper using flip chip packaging technique is proposed and realized to achieve the desired micromirror popped-up angle. Here, the mechanical stopper and mirror were fabricated on two different substrates, and were bonded by using flip chip packaging technology. Precise control of joint height in flip chip bonding guarantees the angle precision of the popped-up mirror. Through this proposed method, the requirement of  $45^\circ$  micromirror popped-up angle for optical switch can be achieved. However, to fabricate a useful optical switch using present configuration, potential problems such as electrical shorting and actuation inefficiency of the pre-stressed beam need to be overcome in advance.

Atomic layer deposition (ALD) for MEMS applications has attracted great interest recently [16], [17]. The fabrication process provides a solution to prevent the electrical shorting and reduce the stiction problem of MEMS structures. Furthermore, in the present study, we observed that the actuating force of the pre-stress beams could be greatly enhanced through thermal control of the ALD process. With this extra actuating

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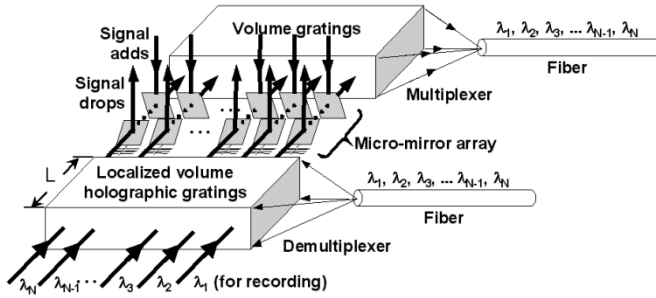


Fig. 1. OADM configuration.

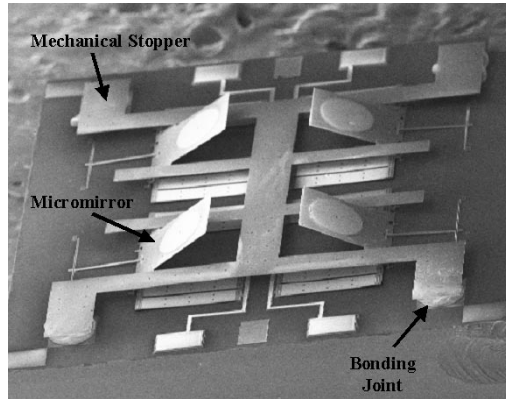


Fig. 2. SEM picture of fabricated back-to-back optical switches.

force from the pre-stress beams, we are able to lift the mirror for more than  $45^\circ$  popped-up angle. Thereafter, a mechanical stopper is used to constrain the movement of micromirror to achieve  $45^\circ$  popped up angle.

The paper is organized as follows. Section II introduces the basic concept that combines surface-micromachining with flip-chip packaging techniques to design optical switches for the proposed OADM configuration. Here, flip chip packaging of MEMS is summarized and investigated. The ALD coating process that is used to improve the performance of optical switches is introduced and discussed in Section III. The preliminary results of the fabricated optical switches are discussed in Section IV. The angle-control precision of the assembled optical switches is also demonstrated here. The dynamic switching characteristics are used to show the feasibility of the design. Finally, conclusions of the present work are given in Section V.

## II. DESIGN OF MEMS-BASED OPTICAL SWITCHES

Fig. 2 shows the SEM of the fabricated optical switches. Electrostatically-driven pre-stressed beams were designed to provide the pop-up forces for the micromirrors. A top-plate is bonded on the top of the pre-stressed beams using flip chip packaging technology to create the mechanical stopper to constrain the popped-up micromirror to reach required angle. The following sections introduced the design and manufacturing process of the optical switches.

TABLE I  
PROCESS FLOW OF FLIP CHIP BONDING

		Bonding pads	$\text{Si}_3\text{N}_4$	PSG
		Poly0: Electrodes	Poly1: Mirror	
<i>Process Flow:</i>				
(1) Remove photoresist on the MEMS devices.	↓	Substrate (Mirror)		
(2) Put solder balls (or gold bumps) on the bonding pads.	↓	Substrate (Multiple Electrodes)		
(3) Preflow (only for solder balls)	↓	Substrate (Mirror)		
(4) Alignment	↓	Substrate (Multiple Electrodes)		
(5) Thermal-Compress Flip Chip bonding	↓	Substrate (Mirror)		
(6) Reflow for misalign-adjustment (only for solder balls)	↑	Substrate (Multiple Electrodes)		
(7) Release using HF	↑	Substrate (Mirror)		

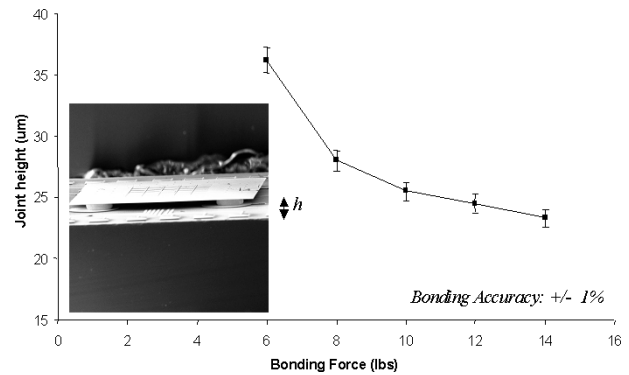


Fig. 3. Joint height versus bonding force of flip chip bonding (with gold bumps at  $140^\circ\text{C}$  bonding temperature).

### A. Flip Chip Packaging

Flip-chip packaging had been developed and applied to many MEMS designs over the last decade [18]–[20]. It is frequently used to transfer MEMS structures to other substrates that provided MEMS designers a new capability to integrate different structures and substrates. A flip-chip packaging process flow developed by CAMPmode, University of Colorado at Boulder, is summarized in Table I [21]. Two designed chips were designed and fabricated in separated substrates. Firstly, solder spheres were deposited on bonding pads using electroplating or manual handling. After aligning top and bottom substrates, chips could be bonded together through applying adequate bonding force and bonding temperature. After the release process, which is used to remove sacrificial layer, the MEMS-chips were transferred to the designed substrate plate.

It is known that bonding with solder spheres provides high bonding accuracy due to the re-flowed process for misalign-adjustment [22]. Solder self-alignment is a predominant technology for electronics assembly and packaging. It is not only used for electrical connections, but also for sub-micron accuracy alignment in many packaging applications [22]–[24]. In addition, a series of work has been done on flip chip bump height control [25], [26]. Here, flip chip bonding with solder spheres and gold bumps were performed to examine corresponding reliability and repeatability. Fig. 3 shows an example of the completed flip-chip bonded optical switches with gold bumps. The joint height of the bonding gap is related directly

TABLE II  
JOINT HEIGHT OF BONDED PLATES WITH DIFFERENT BONDING MATERIALS

Solder: 4mils 63Sn/37Pb			
Device 1	Device 2	Device 3	Bonding Environment
70.416um	69.788um	69.836um	Temp: 100degree Force: 8lbs Time: 3 seconds

(a)

Gold Bump			
Device 1	Device 2	Device 3	Bonding Environment
30.2um	30.5um	29.9um	Temp: 140degree Force: 8lbs Time: 3 seconds

(b)

to the bonding forces with the same bonding temperature (140 °C). The joint height was recorded for each testing case in the same bonding environment. Table II presents the measurement results of joint height for several fabricated optical switches with the same bonding conditions. Three devices were tested for each case. The results showed that the relative errors of the joint height are both within 1%. Experimental results and literature indicated the repeatability in controlling accurate joint height using flip chip bonding is both feasible and reliable.

**B. Designs and Fabrication**

With well-controlled flip chip packaging techniques, the accurate bonding gap could be used to control popped-up angle of the mirror. The conceptual design is illustrated in Fig. 4. As stated above, the bonding gap provides the final deflecting angle of the popped-up micromirrors. The relation is given by

$$\theta = \sin^{-1} \frac{h}{l} \tag{1}$$

where  $h$  is the bonding gap,  $\theta$  is controlled angle, and  $l$  is the length of the micromirror.

In order to realize the OADM configuration for practical applications, the following difficulties must be put into considerations.

- 1) The material of the mechanical stopper (top-plate): In OADM, the light signals that will be dropped by selected optical switches or those will be added into MUX need to pass so-called “nonblocked” optical paths. To achieve this, transparent materials such as quartz can be used to as the mechanical stopper for the proposed optical switches. However, during the release process of the completed flip-chip bonding chip, the etchant HF will attack quartz substrate that causes roughness on the quartz surface.
- 2) The bonding gap with accurate joint height: As listed in (1), we know that the controlled angle  $\theta$  is directly related to the bonding gap  $h$ . This presents a great challenge for flip chip packaging if a larger micromirror size is needed. Using solders with larger diameter could possibly overcome this problem. However, the space of flip

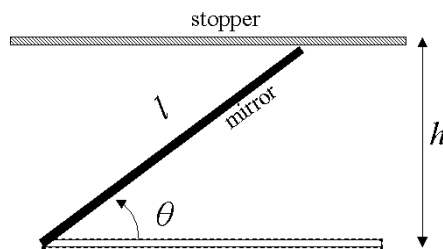


Fig. 4. Conceptual design of the mechanical stopper.

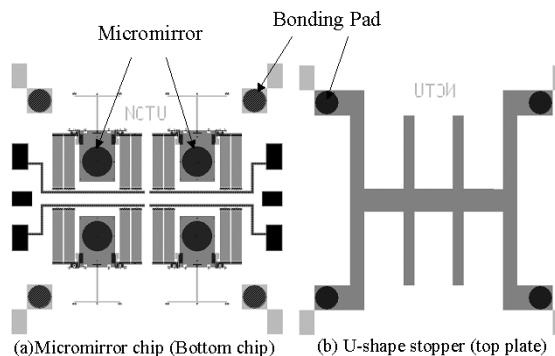


Fig. 5. Layouts of proposed optical switch.

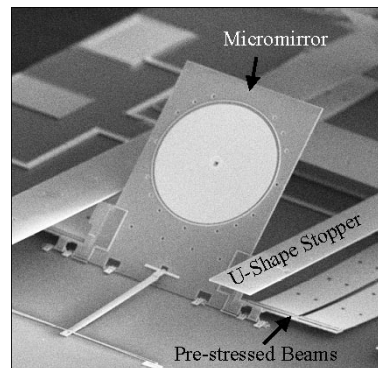


Fig. 6. SEM picture of assembled optical switch.

chip bonding pads that occupied large design space may potentially create the cost and effect problems.

Based on these considerations, a U-shape mechanical stopper is developed to prevent the blockade of the incident light. Furthermore, a side panel that is shorter than the length required in (1) is attached to the both sides of the micromirror is designed to reduce the bonding gap without affecting the original designed deflecting angle. The layouts of the micromirror and the U-shape mechanical stopper of the optical switch are shown in Fig. 5. Four circular pads at each corner of the micromirror and the stopper were designed for flip-chip bonds. After removing the photoresist, these two designs were aligned and bonded together using the flip-chip bonder developed by CAMPmode, University of Colorado at Boulder [21]. By releasing the sacrificial PSG layers, the pre-assembly of the back-to-back optical switches for proposed OADM configuration was realized. Fig. 6 shows an assembled optical switch. The device was fabricated using Cronos/MEMSCAP Poly-MUMPs process [27]. The stopper for flip-chip bonding was fabricated using Poly1 polysilicon layer. The optical switch device consists of a micro-

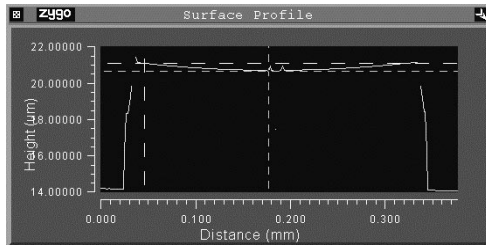


Fig. 7. Surface profile of the fabricated micromirror.

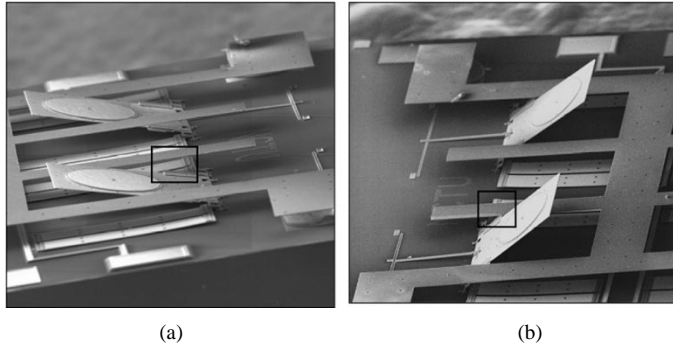


Fig. 8. SEM pictures of fabricated optical switches (a) without heat-treatment and (b) with heat-treatment.

mirror ( $300\ \mu\text{m}$  in diameter) and four pre-stressed beams ( $350\ \mu\text{m}$  in length,  $70\ \mu\text{m}$  in width). Gold (on the top) and Poly2 (on the bottom) layers are chosen as the composite layers of the designed pre-stressed beams [28]. An electrode (substrate) under the pre-stressed beams is used to generate electrostatic forces for the purpose of achieving ON/OFF states of the optical switch. The metal layer (Gold) is deposited on the center of mirror to obtain high reflect efficiency ( $>80\%$ ) [29], [30]. The  $0.75\text{-}\mu\text{m}$  oxide layer (PSG2) was trapped between two polysilicon layers (Poly1 and Poly2) to reduce warpage of the mirror surface due to residual stress [18]. In Fig. 7, the flatness of surface was measured using Zygo interferometer that shows a  $0.5\ \mu\text{m}$  difference from center to edge of the mirror surface.

Upon completing flip chip packaging and release process, the micromirrors were assembled by the pre-stressed beams to reach desired angle. However, the unpredictable residual stress induced in the fabrication of pre-stressed beams occasionally did not have enough actuating force to lift the micromirrors to desired angle. This phenomenon can be observed in Fig. 8. Here, optical switches with different pre-stressed beam sizes have been tested. Note that the ones on the left of Fig. 8 did not reach desired angle while the ones on the right did. The substrate was acted as the bottom electrode to actuate pre-stressed beams, which may potentially introduce problem of controlling the  $N \times 2$  optical switch array individually. This is due to the fact that all the popped-up micromirrors are connected together through the side panels in circuitry that would cause all the mirrors to actuate simultaneously in ON/OFF stage. In order to overcome these difficulties, the ALD coating process was introduced to provide heat-treatment of the pre-stressed beams as well as prevent electrical shorting. The process is summarized in Section III.

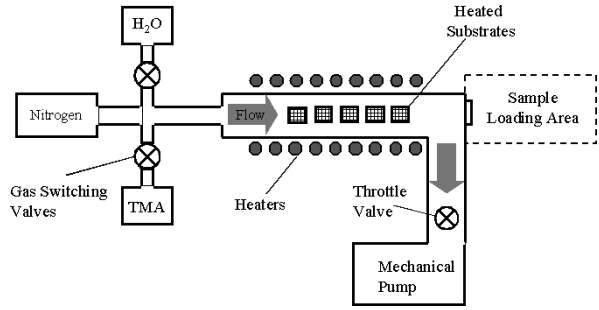
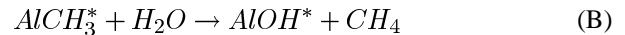
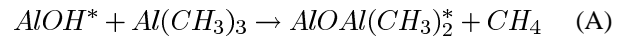


Fig. 9. Schematic of viscous flow reactor of ALD process [16].

### III. ALD COATING PROCESS

ALD coating technique has gained significant attention in many applications. In 1996, *George et al.* [31] proposed a binary reaction sequence of self-limiting chemical reactions with one atomic layer deposited during each cycle. The deposition is conformal and creates ultra-thin films with Angstrom-level thickness control. Materials such as dielectric layers ( $\text{Al}_2\text{O}_3$ ,  $\text{SiO}_2$ ,  $\text{TiO}_2$ , etc.) or single elements (Si, Ge, Cu, etc.) can be deposited on the surface of corresponding substrates. Recently, ALD coating processes have been applied to MEMS devices to prevent electrical shorting, stiction and mechanical frictional wear of MEMS devices [16], [17]. In this paper, ALD coating process is used to complete the post-assembly of the proposed back-to-back optical switches. By depositing dielectric films, the optical switches can be electrically isolated and the actuation force of pre-stressed beams can be improved effectively. Fig. 9 presents the schematic of the viscous flow reactor developed in University of Colorado at Boulder [21]. Here,  $\text{Al}_2\text{O}_3$  is selected to deposit on the pre-assembled back-to-back optical switches. Note that the hard and insulating properties of  $\text{Al}_2\text{O}_3$  were capable of preventing mechanical wear and electrical shorting between contacting parts. Note that  $\text{Al}_2\text{O}_3$  ALD films are deposited using alternating trimethyl aluminum (TMA) and  $\text{H}_2\text{O}$  exposure. The A and B surface reactions that define an AB cycle (binary reaction) for  $\text{Al}_2\text{O}_3$  ALD are [32]



where the asterisks designate the surface species. To perform ALD coating, the chamber is heated and maintained at  $177\ ^\circ\text{C}$  for required temperature of chemical reaction. At this stage, micromirrors will return to original configuration (parallel to the substrate) due to the characteristics of the pre-stressed beams (gold on top, polysilicon on bottom). By controlling the turn-on time of gas switching valves, the AB cycles of the ALD coating process is performed. Here, a  $1000\ \text{\AA}$  thickness of dielectric layer ( $\text{Al}_2\text{O}_3$ ) was deposited on the fabricated optical switches after a numbers of deposition cycles for electrical isolation. The coated dielectric layer overcomes the previous-mentioned problem such that the designed micromirror can be controlled individually.

In addition, since the temperature of ALD coating process ( $177\ ^\circ\text{C}$ ) is above  $110\ ^\circ\text{C}$ , the “neutral temperature” of MUMPs chips, the heat-treatment process [28] can be also accomplished. Once the ALD deposition is finished, the chips can

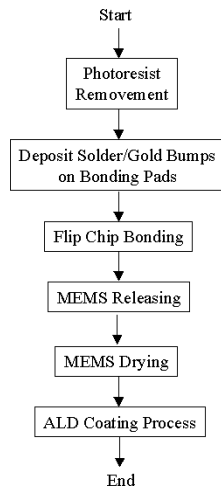


Fig. 10. Block diagram of proposed assembly process.

be “quenched” by removing the chips from reaction chamber to a cold plate. The heat-treatment process will introduce additional stress into fabricated pre-stressed beams for improving actuating efficiency. With the mechanical stopper on top, the micromirrors are popped-up to reach precise designed angle.

Fig. 10 summarized the proposed assembly process that is given as follows.

- 1) Soak MUMPs chips 10 ~ 15 min in acetone to remove photoresist.
- 2) Put solder balls or gold bumps on the designed bonding pads.
- 3) Perform flip chip packaging process to bond top and bottom chips.
- 4) Soak bonded chips 4 ~ 6 min in HF acid to remove sacrificial layers (PSG).
- 5) Soak released chips in methanol and performs typical MEMS drying process.
- 6) Perform ALD coating process to provide electrical isolation and heat-treatment of pre-stressed beams.

Once the assembly process is completed, the fabricated optical switches can reach the desired angle according to the bonding gap.

#### IV. EXPERIMENTS

The popped-up angle of micromirrors during the ON/OFF stage of the applied electrostatic force was under investigation. Fig. 11 illustrates the optical configuration to measure the popped-up angle of the fabricated optical switches. The laser is manually aligned to the popped-up micromirror until the path of incident light coming from laser and reflected light from the micromirror is the identical one. This implies that the popped-up angle of the micromirror ( $\theta$ ) is equal to two times of the tilting angle of the rotation mirror ( $\psi$ ). Table III lists the measurement results of the assembly optical switches. In comparison to the proposed 45° popped-up angle for the OADM configuration, the relative error is within 1.6%, which shows the capability of the present assembly process in controlling the angle of the micromirrors. Finally, Fig. 12 shows the results of dynamic characteristics of the fabricated optical

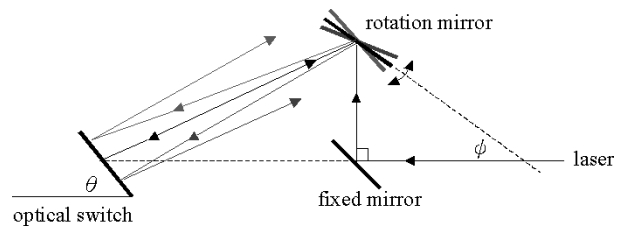
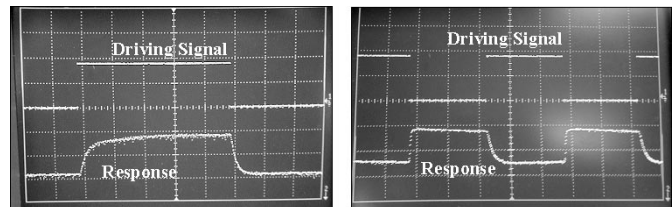


Fig. 11. Optical configuration to measure the popped-up angle of optical switch.



(a) On state (Micromirror is pulled down) (b) Off state (Micromirror is popped up)

Fig. 12. Dynamic characteristic of the fabricated optical switch.

TABLE III  
MEASURED POPPED-UP ANGLE OF THE ASSEMBLED OPTICAL SWITCHES

Popped-up angle measurements			
Device 1	Device 2	Device 3	Device 4
44.6°	45.2°	45.3°	44.3°

switches. The ON/OFF states of the optical switch were tested. The switching speed of the present experiment had reached 100 Hz with 2 ms switching time. The electrostatic pull-down voltage of the pre-stressed beams is about 100 V and the power consumption is estimated to be less than 0.2 mW.

#### V. CONCLUSION

A surface-micromachined optical switch had been developed for OADM applications through the integration of Cronos/MEMSCAP Poly-MUMPs processing, flip chip packaging, and ALD coating. A mechanical stopper was designed to constrain the movement of popped-up micromirrors to reach desired angle. An ALD coating process is applied to enhance the actuating force of the pre-stress beams as well as to accomplish electrical isolation. Experiments have demonstrated the feasibility of the fabricated optical switch using proposed assembly process.

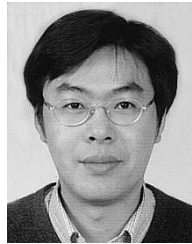
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