

A New 2D Analytic Threshold-Voltage Model for Fully Depleted Short-Channel SOI MOSFET's

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Abstract—The exact solution of the 2D Poisson's equation for the fully depleted SOI MOSFET's is derived by using a three-zone Green's function solution technique. Based on the derived 2D potential distribution, the front and back surface potential distributions in the Si film are analytically obtained and their accuracy are verified by 2D numerical analysis. The calculated minimum surface potential and its location are used to analyze the drain-induced barrier-lowering effect and further to develop an analytic threshold-voltage model. Comparisons between the developed analytic threshold-voltage model and the 2D numerical analysis are made. It is shown that excellent agreements are obtained for wide ranges of device structure parameters and applied biases.

NOMENCLATURE

$\epsilon_{si}(\epsilon_{ox})$	Dielectric permittivity of Si (SiO ₂).
q	Elementary charge.
n_i	Intrinsic carrier concentration of semiconductor.
t_{si}	Thickness of Si film.
$t_{of}(t_{ob})$	Thickness of front (bottom)-gate oxide.
L	Effective channel length.
k_n^i	Eigenvalue of Region i ($i = I, II, III$) ($k_n^I = [n - 1/2]\pi/t_{of}$ for Region I, $k_n^{II} = n\pi/t_{si}$ for Region II, and $k_n^{III} = [n - 1/2]\pi/t_{ob}$ for Region III).
k_m	Eigenvalue in all regions ($k_m = m\pi/L$).
$N_B f(y)$	Doping profile in the Si film, where $f(y)$ is a doping profile function with $f(y) = 1$ for uniform doping concentration.
$V_{bi}(y)$	Built-in potential of the source(drain)/body junctions in Region II.
V_{gs}	Gate-source voltage.
V_{ds}	Drain-source voltage.
V_{BS}	Back gate-source voltage.
$V_{fb,f}(V_{fb,b})$	Flatband voltage of the front (back) gate.

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$V'_{gs}(V'_{BS})$	$= V_{gs} - V_{fb,f}(V_{BS} - V_{fb,b})$.
$D_{sf}(x)(D_{sb}(x))$	Electric displacement at the front (back) Si-SiO ₂ interface.
$\Phi^i(x, y)$	2D potential distribution in Region i ($i = I, II, III$).
$E_y^i(x, y)$	2D vertical electric field distribution in Region i ($i = I, II, III$).
$\Phi_f^{II}(x)(\Phi_b^{II}(x))$	Front (back) surface potential in Region II.
$Q_B^n(Q_B^0)$	Fourier coefficient of the bulk charge density with the integer n ($n = 0$) in Region II

$$Q_B^n = \frac{2}{t_{si}} \int_0^{t_{si}} (-qN_B)f(y) \cos k_n^{II}y dy$$

and

$$Q_B^0 = \frac{1}{t_{si}} \int_0^{t_{si}} (-qN_B)f(y) dy$$

$D_{sf}^m(D_{sb}^m)$	Fourier coefficient of the electric displacement at the front (back) surface
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$$D_{sf}^m = \frac{2}{L} \int_0^L D_{sf}(x) \sin k_m x dx$$

$$D_{sb}^m = \frac{2}{L} \int_0^L D_{sb}(x) \sin k_m x dx$$

$A_n^s(A_n^d)$	Fourier coefficient of the boundary potential at the source (drain) side in Region I
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$$A_n^s = \frac{2}{t_{of}} \int_0^{t_{of}} \phi^I(0, y) \cos k_n^I y dy$$

and

$$A_n^d = \frac{2}{t_{of}} \int_0^{t_{of}} \phi^I(L, y) \cos k_n^I y dy.$$

$B_n^s(B_0^s)$	Fourier coefficient of the source boundary potential with the integer n ($n = 0$) in Region II
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$$B_n^s = \frac{2}{t_{si}} \int_0^{t_{si}} V_{bi}(y) \cos k_n^{II}y dy$$

and

$$B_0^s = \frac{1}{t_{si}} \int_0^{t_{si}} V_{bi}(y) dy$$

$B_n^d (B_0^d)$ Fourier coefficient of the drain boundary potential with the integer n ($n = 0$) in Region II

$$B_n^d = \frac{2}{t_{si}} \int_0^{t_{si}} [V_{bi}(y) + V_{ds}] \cdot \cos k_n^{II} y dy$$

and

$$B_0^d = \frac{1}{t_{si}} \int_0^{t_{si}} [V_{bi}(y) + V_{ds}] dy.$$

$C_n^s (C_n^d)$ Fourier coefficient of the boundary potential at the source (drain) side in Region III

$$C_n^s = \frac{2}{t_{ob}} \int_0^{t_{ob}} \phi^{III}(0, y) \cos k_n^{III} y dy$$

and

$$C_n^d = \frac{2}{t_{ob}} \int_0^{t_{ob}} \phi^{III}(L, y) \cos k_n^{III} y dy.$$

$\phi_{f,inv}$ Front surface potential at strong inversion condition

$$\phi_{f,inv} = 2\phi_{fp}$$

and

$$\phi_{fp} = \frac{k_B T}{q} \cdot \ln \left(\frac{N_B}{n_i} \right)$$

x_{min} Location of the minimum surface potential at the front Si surface.

I. INTRODUCTION

THE MOSFET device fabricated on a thin SOI film and operated in the fully depleted mode has shown to have smaller drain-induced barrier-lowering effect [1] and higher punchthrough voltage [2] when compared to those of bulk MOSFET's. Moreover, its inherent advantages of latch-up-free and high-speed operation may give the best solution to many submicrometer CMOS problems [3]. However, the coupling effects between the front and back gates become complicated for short-channel SOI MOSFET devices. Therefore, it is difficult to derive a simple and accurate model for device design and circuit analysis.

The analytical modeling of short-channel SOI MOSFET's had been reported by several authors [4]–[7]. Veeraraghavan and Fossum [4] used a conventional charge-sharing scheme to develop the threshold-voltage model for the I–V characteristics. Young [5] assumed a trial function with a parabolic-like potential distribution for the 2D Poisson's equation and developed an analytic model for the drain-induced barrier lowering. This simplified assumption underestimates the effects of the source/drain junctions and may cause significant error

when the channel length of the device is very short. Moreover, the effect of the nonuniformly doped profile in the Si film cannot be taken into account by this kind of analysis. Recently, based on the analysis of Ratnakumar and Meindl [6], Woo *et al.* [7] separated the 2D Poisson's equation into a 1D Poisson's equation and a 2D Laplace equation. Moreover, the continuity of the electric field across the Si–bottom oxide interface was assumed instead of the continuity of the electric displacement. This assumption may cause a large error for short-channel SOI MOSFET's and an additional series is introduced to improve the accuracy of the potential distribution. Due to the complicated 2D potential distribution, it was difficult for Woo *et al.* [7] to derive an analytic threshold-voltage model for short-channel SOI MOSFET's.

In order to analytically model the 2D characteristics of short-channel thin film SOI MOSFET's, the 2D Poisson's equation must be solved by incorporating the suitable boundary conditions. The Green's function technique may give an exact solution for the 2D Poisson's equation including the nonuniform doping profile. This advantage was first demonstrated in solving the 2D potential distribution of a bulk MOSFET by Lin and Wu [8]. Recently, the multizone solution using the Green's function technique has been successfully used to describe the electrical characteristics of short gate-length MESFET's by Chin and Wu [9]. In order to avoid the complexity in dealing with the equivalent charge densities between the regions of different dielectric materials, the multizone solutions with mixed boundary conditions are used in this work to analytically solve the 2D Poisson's equation in three regions with different dielectrics.

In Section II, the Green's function solution technique for solving the 2D Poisson's equation in all regions is introduced and the boundary conditions corresponding to each region are also described. The analytic 2D potential distribution in the silicon region is derived exactly and verified by 2D numerical analysis. In Section III, the derived 2D potential distribution is further used to develop the threshold-voltage model, in which the drain-induced barrier lowering is calculated by the derived 2D potential distribution. In addition, the results of the derived threshold-voltage model are compared with those of 2D numerical analysis. It is shown that excellent agreement between the developed threshold-voltage model and the 2D numerical analysis are obtained, and these verify the applicability of the boundary conditions used in our analysis. At last, a concluding remark is given in Section IV.

II. THE BASIC ANALYSIS

The basic structure of a thin-film SOI MOSFET for 2D numerical simulation is shown in Fig. 1, where the simplified domain for analytically solving the 2D Poisson's equation is highlighted by the bolded lines and the boundary conditions used are also listed. In order to avoid the complexity in calculating the equivalent charge density between the regions of different dielectrics, the domain for solving the 2D Poisson's equation is further divided

TABLE I
THE GREEN FUNCTION SOLUTION FOR DIFFERENT REGIONS

Region I	$G_x^I(x, y; x', y') = \frac{2}{L} \sum_{m=1}^{\infty} \sin k_m x \sin k_m x' \frac{\cosh k_m y \sinh k_m (t_{of} + y')}{k_m \cosh k_m t_{of}}, \quad y' < y$
	$G_x^I(x, y; x', y') = \frac{2}{L} \sum_{m=1}^{\infty} \sin k_m x \sin k_m x' \frac{\cosh k_m y' \sinh k_m (t_{of} + y)}{k_m \cosh k_m t_{of}}, \quad y < y'$
	$G_y^I(x, y; x', y') = \frac{2}{t_{of}} \sum_{n=1}^{\infty} \cos k_n^I y \cos k_n^I y' \frac{\sinh k_n^I x \sinh k_n^I (L - x')}{k_n^I \sinh k_n^I L}, \quad x < x'$
	$G_y^I(x, y; x', y') = \frac{2}{t_{of}} \sum_{n=1}^{\infty} \cos k_n^I y \cos k_n^I y' \frac{\sinh k_n^I x' \sinh k_n^I (L - x)}{k_n^I \sinh k_n^I L}, \quad x' < x$
	$G_x^{II}(x, y; x', y') = \frac{2}{L} \sum_{m=1}^{\infty} \sin k_m x \sin k_m x' \frac{\cosh k_m y \cosh k_m (t_{si} - y')}{k_m \sinh k_m t_{si}}, \quad y' < y$
	$G_x^{II}(x, y; x', y') = \frac{2}{L} \sum_{m=1}^{\infty} \sin k_m x \sin k_m x' \frac{\cosh k_m y' \cosh k_m (t_{si} - y)}{k_m \sinh k_m t_{si}}, \quad y < y'$
Region II	$G_y^{II}(x, y; x', y') = \frac{c}{t_{si}} \sum_{n=0}^{\infty} \cos k_n^{II} y \cos k_n^{II} y' \frac{\sinh k_n^{II} x \sinh k_n^{II} (L - x')}{k_n^{II} \sinh k_n^{II} L}, \quad x < x'$
	$G_y^{II}(x, y; x', y') = \frac{c}{t_{si}} \sum_{n=0}^{\infty} \cos k_n^{II} y \cos k_n^{II} y' \frac{\sinh k_n^{II} x' \sinh k_n^{II} (L - x)}{k_n^{II} \sinh k_n^{II} L}, \quad x' < x$
	$c = 1, \quad \text{for } n = 0$
	$c = 2, \quad \text{for } n > 0$
Region III	$G_x^{III}(x, y; x', y') = \frac{2}{L} \sum_{m=1}^{\infty} \sin k_m x \sin k_m x' \frac{\cosh k_m (y - t_{si}) \sinh k_m (t_{ob} + t_{si} - y')}{k_m \cosh k_m t_{ob}}, \quad y' < y$
	$G_x^{III}(x, y; x', y') = \frac{2}{L} \sum_{m=1}^{\infty} \sin k_m x \sin k_m x' \frac{\cosh k_m (y' - t_{si}) \sinh k_m (t_{ob} + t_{si} - y)}{k_m \cosh k_m t_{ob}}, \quad y < y'$
	$G_y^{III}(x, y; x', y') = \frac{2}{t_{ob}} \sum_{n=1}^{\infty} \cos k_n^{III} (y - t_{si}) \cos k_n^{III} (y' - t_{si}) \frac{\sinh k_n^{III} x \sinh k_n^{III} (L - x')}{k_n^{III} \sinh k_n^{III} L}, \quad x < x'$
	$G_y^{III}(x, y; x', y') = \frac{2}{t_{ob}} \sum_{n=1}^{\infty} \cos k_n^{III} (y - t_{si}) \cos k_n^{III} (y' - t_{si}) \frac{\sinh k_n^{III} x \sinh k_n^{III} (L - x)}{k_n^{III} \sinh k_n^{III} L}, \quad x' < x$

at $y = 0$ and $\Phi^{II}(x, y)$ and $\Phi^{III}(x, y)$ with respect to y at $y = t_{si}$, we obtain the continuities of the electric displacement at the two Si-SiO₂ interfaces ($y = 0$ and $y = d$), and the complexity of dealing with the equivalent charge densities at the interfaces is thus avoided. To obtain the exact potential distribution, D_{sf}^m and D_{sb}^m have to be solved first. By equating (1) and (2) at $y = 0$ and (2) and (3) at $y = t_{si}$, D_{sf}^m and D_{sb}^m can be obtained. Note that the above equations are exact and the arbitrary doping profile in the Si film can be treated. It is clearly seen that the second term on the right-hand side of (2) demonstrates the charge-coupling effect between the boundaries of source and drain sides, which was overlooked in [7].

In the following analysis, the uniformly doped Si film is assumed for simplicity. In this case, $f(y) = 1$, and the Fourier coefficients (Q_B^s , B_n^s , and B_n^d) would vanish except for $n = 0$, i.e., $Q_B^0 = -qN_B$, $B_0^s = V_{bi}$, and $B_0^d = V_{bi} + V_{ds}$. The 2D potential distribution in Region II is our major concern and can be written as

$$\begin{aligned} \Phi^{II}(x, y) = & -\frac{qN_B}{2\epsilon_{si}} x(L - x) + V_{bi} + \frac{x}{L} \cdot V_{ds} \\ & + \sum_{m=1}^{\infty} \frac{\sin k_m x}{\epsilon_{si} k_m \sinh k_m t_{si}} [D_{sf}^m \cosh k_m (t_{si} - y) \\ & - D_{sb}^m \cosh k_m y] \end{aligned} \quad (4)$$

where D_{sf}^m and D_{sb}^m have been deduced by equating (4) and (1) at $y = 0$ and (4) and (3) at $y = t_{si}$, which are expressed as

$$\begin{aligned} D_{sf}^m = & \frac{\epsilon_{si} k_m}{d_0^m} \left[\frac{d_2^m}{\sinh k_m t_{si}} - d_1^m \left(\frac{1}{\tanh k_m t_{si}} + \frac{\epsilon_{si}}{\epsilon_{ox}} \right. \right. \\ & \left. \left. \cdot \tanh k_m t_{ob} \right) \right] \\ D_{sb}^m = & \frac{\epsilon_{si} k_m}{d_0^m} \left[d_2^m \left(\frac{1}{\tanh k_m t_{si}} + \frac{\epsilon_{si}}{\epsilon_{ox}} \tanh k_m t_{of} \right) \right. \\ & \left. - \frac{d_1^m}{\sinh k_m t_{si}} \right] \end{aligned}$$

in which

$$\begin{aligned} d_1^m = & -\phi_q^m + \frac{2V'_{gs}}{m\pi} \frac{1 - (-1)^m}{\cosh k_m t_{of}} + h_m^I \\ d_2^m = & -\phi_q^m + \frac{2V'_{BS}}{m\pi} \frac{1 - (-1)^m}{\cosh k_m t_{ob}} + h_m^{III} \\ \phi_q^m = & \frac{2}{m\pi} \left[(1 - (-1)^m) \left(-\frac{qN_B}{\epsilon_{si}} \right) \right. \\ & \left. + (1 - (-1)^m) V_{bi} + (-1)^{m+1} V_{ds} \right] \end{aligned}$$

$$\begin{aligned}
 h_m^I &= \sum_{n=1}^{\infty} 0.5 t_{mn}^I [A_n^s + (-1)^{m+1} A_n^d] \\
 h_m^{III} &= \sum_{n=1}^{\infty} 0.5 t_{mn}^{III} [C_n^s + (-1)^{m+1} C_n^d] \\
 t_{mn}^I &= \frac{4}{m\pi} \left[1 + \frac{L^2(n-0.5)^2}{t_{of}^2 m^2} \right]^{-1} \\
 t_{mn}^{III} &= \frac{4}{m\pi} \left[1 + \frac{L^2(n-0.5)^2}{t_{ob}^2 m^2} \right]^{-1} \\
 d_0^m &= \frac{1}{(\sinh k_m t_{si})^2} - \left(\frac{1}{\tanh k_m t_{si}} + \frac{\epsilon_{si}}{\epsilon_{ox}} \tanh k_m t_{of} \right) \\
 &\quad \cdot \left(\frac{1}{\tanh k_m t_{si}} + \frac{\epsilon_{si}}{\epsilon_{ox}} \tanh k_m t_{ob} \right).
 \end{aligned}$$

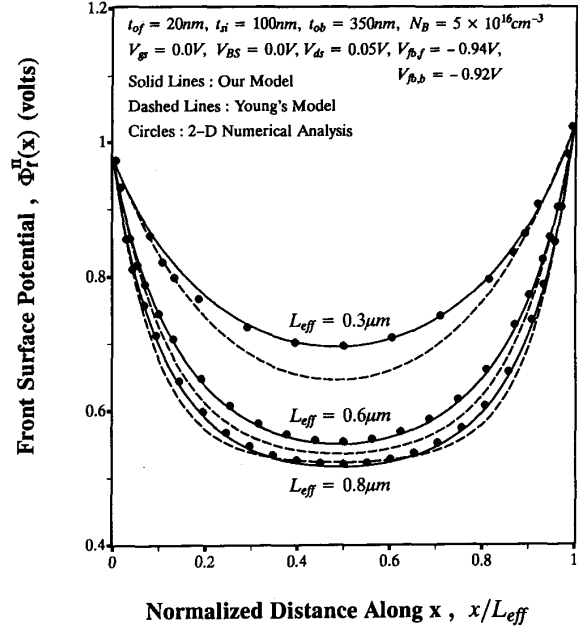
From (4), the potential distribution along the front surface of Si film can be obtained as

$$\begin{aligned}
 \Phi_f^{II}(x) &= -\frac{qN_B}{2\epsilon_{si}} x(L-x) + V_{bi} + \frac{x}{L} V_{ds} \\
 &\quad + \sum_{m=1}^{\infty} \frac{\sin k_m x}{\epsilon_{si} k_m \sinh k_m t_{si}} (D_{sf}^m \cosh k_m t_{si} - D_{sb}^m).
 \end{aligned} \tag{5}$$

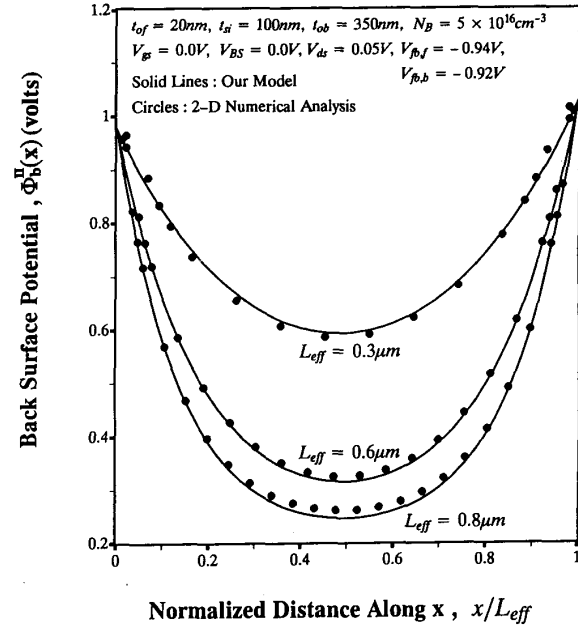
In order to check the accuracy of the derivations, the calculated results using (5) are compared with those calculated by a 2D numerical simulator [10], in which a new discretized Green's theorem was implemented in the simulator to directly solve the Poisson's equations in different dielectric regions and the finite difference scheme was used to discretize the current continuity equations in the semiconductor region.

Fig. 2(a) shows the calculated front surface potential as a function of the normalized distance from the source edge with the effective channel length as a normalization parameter. It is clearly seen that excellent agreements can be obtained even for a device with the effective channel length as short as $0.3 \mu\text{m}$. Note that the number of terms (m) used in (5) can be as small as 10 to attain such an accuracy. Moreover, the calculated results using Young's model [5] are also shown by the dashed lines in the figure for comparisons. It is clearly seen that the parabolic potential distribution along the y direction is insufficient to describe the effects of the source(drain)/body junctions for short-channel SOI MOSFET's. Obviously, the model developed by Young [5] underestimates the drain-induced barrier-lowering effect. Similarly, the back surface potential can also be obtained from (4) and is expressed by

$$\begin{aligned}
 \Phi_b^{II}(x) &= -\frac{qN_B}{2\epsilon_{si}} x(L-x) + V_{bi} + \frac{x}{L} V_{ds} \\
 &\quad + \sum_{m=1}^{\infty} \frac{\sin k_m x}{\epsilon_{si} k_m \sinh k_m t_{si}} (D_{sf}^m - D_{sb}^m \cosh k_m t_{si}).
 \end{aligned} \tag{6}$$



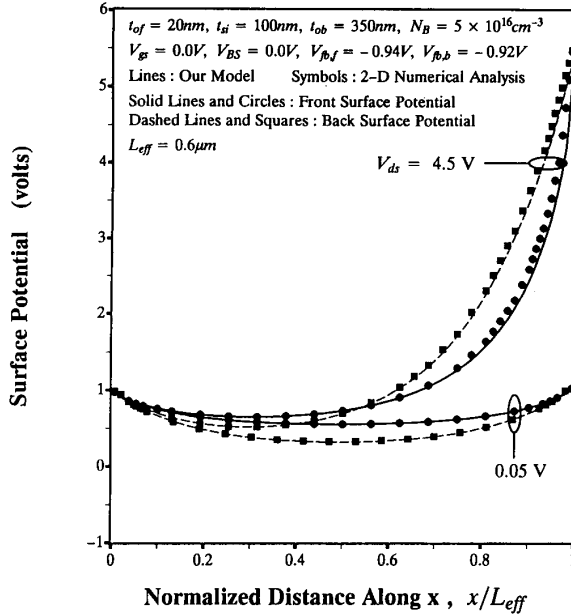
(a)



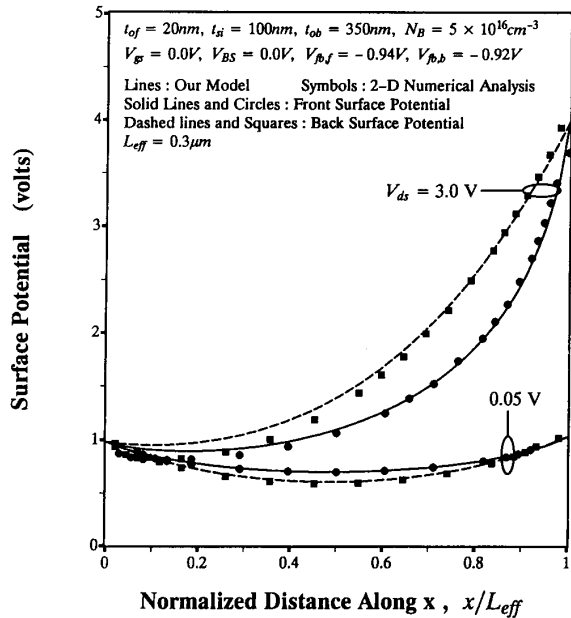
(b)

Fig. 2. The surface potential as a function of the normalized distance from the source edge with the effective channel length as a normalization parameter (a) Front surface potential. (b) Back surface potential.

The back surface potential as a function of the normalized distance from the source edge with the effective channel length as a normalization parameter is shown in Fig. 2(b). It is clearly seen that excellent agreements between (6) and 2D numerical analysis are also obtained. The effects of the drain bias are shown in Fig. 3, in which Fig. 3(a) shows the front and back surface potentials of a device



(a)



(b)

Fig. 3. The surface potential as a function of the normalized distance from the source edge with the drain bias as a parameter (a) Front and back surface potentials for a 0.6- μm SOI MOSFET operated with different drain biases from 0.05 to 4.5 V. (b) Front and back surface potentials for a 0.3- μm SOI MOSFET operated with different drain biases from 0.05 to 3.0 V.

with $L_{\text{eff}} = 0.6 \mu\text{m}$; and Fig. 3(b) shows the case with $L_{\text{eff}} = 0.3 \mu\text{m}$. It is clearly seen that good agreements between our analytic model and 2D numerical analysis are also obtained. Comparison of the calculated vertical potential distribution (y -direction) at the minimum surface potential point between our model and 2D numerical analysis for different channel lengths and drain biases are

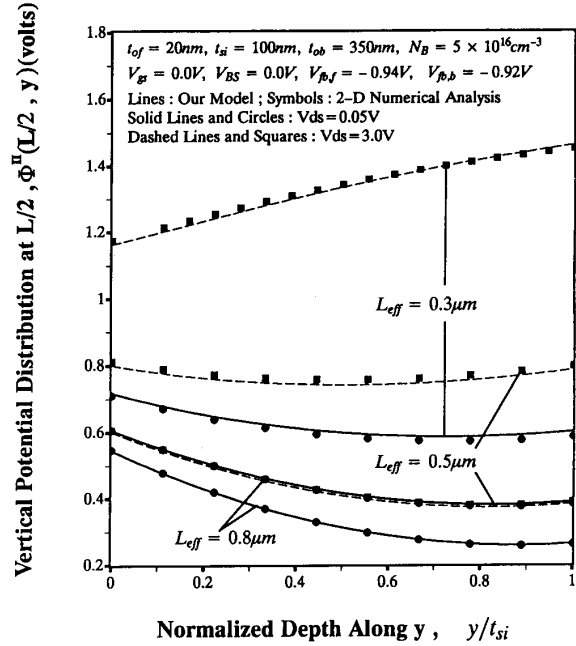


Fig. 4. Vertical potential distribution in the Si film for different channel lengths and drain biases.

shown in Fig. 4 and excellent agreements can be seen. These indicate that the errors caused by the artificial boundary potentials in Region I and Region III are negligibly small. In our analytic modeling, the effects of free carriers are neglected in the subthreshold region and this assumption is proven to be valid for short-channel thin-film SOI MOSFET's even when the drain bias is large.

III. THE THRESHOLD-VOLTAGE MODEL

From Section II, the surface potential distribution at the front interface can be further rewritten in terms of terminal voltages as

$$\begin{aligned} \Phi_f^{\text{II}}(x) = & -\frac{qN_B}{2\epsilon_{\text{si}}} x(L-x) + V_{bi} + \frac{x}{L} V_{ds} \\ & + \sum_{m=1}^{\infty} (V_{gs}' \cdot G_f^m + V_{BS}' \cdot G_b^m + P^m) \sin k_m x \end{aligned} \quad (7)$$

where

$$\begin{aligned} G_f^m = & [1 - (-1)^m] \frac{R_1^m}{d_0^m} \left[\frac{2}{m\pi \cosh k_m t_{of}} \right. \\ & \left. + \sum_{n=1}^{\infty} \frac{t_{mn}^1}{(n-0.5)\pi} \left[(-1)^{n+1} - \frac{1}{(n-0.5)\pi} \right] \right] \\ G_b^m = & [1 - (-1)^m] \frac{R_2^m}{d_0^m} \left[\frac{2}{m\pi \cosh k_m t_{ob}} \right. \\ & \left. + \sum_{n=1}^{\infty} \frac{t_{mn}^{\text{III}}}{(n-0.5)\pi} \left[(-1)^{n+1} - \frac{1}{(n-0.5)\pi} \right] \right] \end{aligned}$$

$$\begin{aligned}
 P^m &= \frac{1}{d_0^m} \left[[1 - (-1)^m] \frac{qN_B L^2}{2\epsilon_{si}} \frac{4(R_1^m + R_2^m)}{(m\pi)^3} \right. \\
 &\quad \left. + T^m [V_{bi}(1 - (-1)^m) + V_{ds}(-1)^{m+1}] \right] \\
 T^m &= \sum_{n=1}^{\infty} [R_1^m t_{mn}^I + R_2^m t_{mn}^{III}] [(n - 0.5)\pi]^{-2} \\
 &\quad - 2(R_1^m + R_2^m)/m\pi \\
 R_1^m &= -1 - \frac{\epsilon_{si} \tanh k_m t_{ob}}{\epsilon_{ox} \tanh k_m t_{si}} \\
 R_2^m &= -\frac{\epsilon_{si} \tanh k_m t_{of}}{\epsilon_{ox} \sinh k_m t_{si}}.
 \end{aligned}$$

The Fourier coefficients in (7) are further checked if the first coefficient $m = 1$ is the dominant term. It is shown that the coefficient of the first term is about ten times larger than the next significant coefficient when the drain bias is small. However, a large error may be produced for a large-drain bias if the first-term approximation is used. It is seen that the first coefficient is not even the dominant one in some cases. Another problem of using the first-term approximation is that it will lead to an erroneous result for the position of the minimum surface potential. Therefore, the series solution is kept for the following derivations without losing accuracy.

Differentiating (7) with respect to x , the position of the minimum surface potential can be obtained. Note that the location of the minimum surface potential (x_{\min}) can only be solved by iteration since no explicit solution for x_{\min} can be obtained. The calculated x_{\min} is then substituted into (7) and the minimum surface potential is obtained as

$$\begin{aligned}
 \Phi_{f,\min}^{II} &= -\frac{qN_B}{2\epsilon_{si}} x_{\min}(L - x_{\min}) + V_{bi} + \frac{x_{\min}}{L} V_{ds} \\
 &\quad + V'_{gs} \sum_{m=1}^{\infty} G_f^m \sin k_m x_{\min} \\
 &\quad + V'_{BS} \sum_{m=1}^{\infty} G_b^m \sin k_m x_{\min} \\
 &\quad + \sum_{m=1}^{\infty} P^m \sin k_m x_{\min}. \quad (8)
 \end{aligned}$$

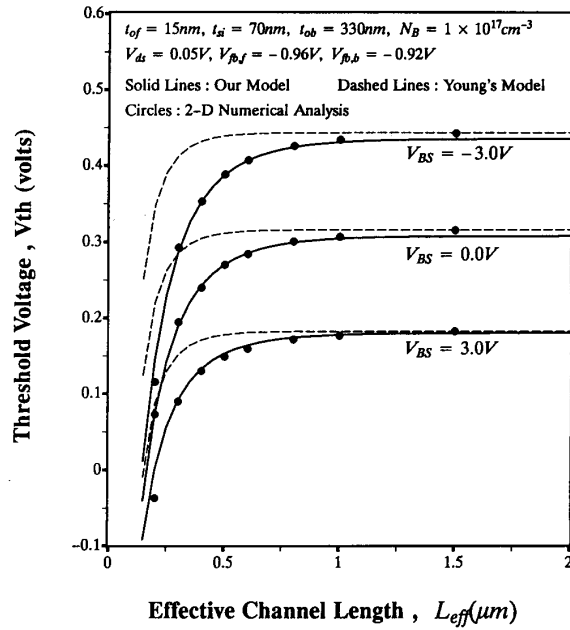
It is clearly seen that the relation between the minimum surface potential and the external bias can be deduced from (8) when the minimum surface potential reaches the inversion condition $\phi_{f,\text{inv}}$ as the applied gate bias is equal to the threshold voltage. Setting $\Phi_{f,\min}^{II} = \phi_{f,\text{inv}}$, the threshold voltage can be analytically expressed as

$$\begin{aligned}
 V_T^f &= V_{fb,f} + \left\{ \phi_{f,\text{inv}} + \frac{qN_B}{2\epsilon_{si}} x_{\min}(L - x_{\min}) \right. \\
 &\quad \left. - V_{bi} - \frac{x_{\min}}{L} V_{ds} \right\} \left[\sum_{m=1}^{\infty} G_f^m \sin k_m x_{\min} \right]^{-1} \\
 &\quad - \sum_{m=1}^{\infty} (V'_{BS} \cdot G_b^m + P^m) \sin k_m x_{\min} \\
 &\quad \cdot \left[\sum_{m=1}^{\infty} G_f^m \sin k_m x_{\min} \right]^{-1}. \quad (9)
 \end{aligned}$$

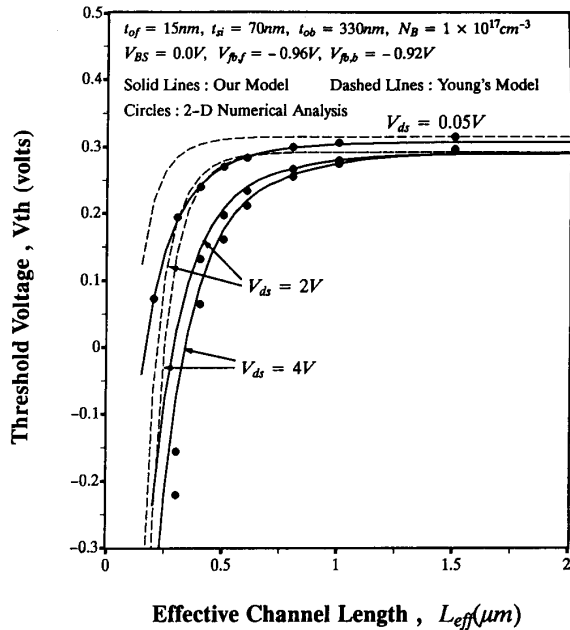
Note that an initial guess for the threshold voltage is used to calculate the location of the minimum surface potential, then x_{\min} is substituted into (9) to solve the threshold voltage, and the iteration procedure is continued until convergence is reached. It is found that the number of iterations is small and the computation time used is short. In order to compare the results of the developed analytic threshold-voltage model with those of 2D numerical analysis, the threshold voltage derived by the 2D numerical analysis is defined by the drain current as follows: the threshold voltage of a long-channel (10- μm) SOI MOSFET is extracted by checking the $\Phi_{f,\min}^{II} - V_{gs}$ relation corresponding to the $I_{ds} - V_{gs}$ characteristics from 2D numerical analysis. The normalized drain current corresponding to $\Phi_{f,\min}^{II} = 2\phi_{fp}$ is taken as the reference current, then the threshold voltage of a shorter-channel device is extracted by equating the normalized drain current to the specified reference current.

The calculated threshold voltages using (9) for a SOI MOSFET with 70-nm Si film, 15-nm front gate oxide, and 320-nm bottom oxide are compared with the 2D numerical analysis in Fig. 5(a) with the back-gate bias as a parameter. It is clearly seen that excellent agreements between our model and the 2D numerical analysis are obtained and poor agreements for Young's model are demonstrated. Comparisons of the calculated threshold voltages as a function of the effective channel length with the drain as a parameter are shown in Fig. 5(b), in which the thickness of the front gate oxide, the Si film, and the bottom oxide remain the same. Fig. 5(b) shows that good agreements are also obtained for devices with channel length as short as 0.4 μm . However, the discrepancy for shorter channel length devices (for example, $L_{\text{eff}} = 0.3 \mu\text{m}$) operated with larger drain bias is mainly due the serious drain-induced barrier-lowering effect at the bottom Si/SiO₂ interface, as checked by 2D numerical analysis. It is clearly seen that the underestimation of the drain-induced barrier-lowering effect in [5] results in a smaller threshold-voltage roll-off for shorter effective channel lengths.

The calculated threshold voltages using our model as a function of the effective channel length with the thickness of the front gate oxide as a parameter are compared with those using 2D numerical analysis in Fig. 6. It is easily seen that the developed threshold-voltage model agrees well with the 2D numerical analysis. Similar good agreement can also be observed from Fig. 7, where the threshold voltage is plotted as a function of the effective channel length with the thickness of the Si film as a parameter. The slight discrepancy for shorter channel devices is attributed to the easy penetration of the source/drain built-in electric fields at the back Si surface. From Figs. 6 and 7, it is clearly seen that our analytic model agrees with 2D numerical analysis for different doping concentrations in the Si film. It is concluded that the penetration of the source/drain electric fields is more likely to occur at the back Si surface when the doping concentration in the Si film is lower or when the channel length is shorter. This



(a)



(b)

Fig. 5. Calculated threshold voltages as a function of the effective channel length for different models or method (a) with the back-gate bias as a parameter and (b) with the drain bias as a parameter.

effect can be improved by increasing the doping concentration at the back Si surface using ion implantation. It should be noted that the condition of the fully depleted device can be estimated by the one-dimensional analysis of the Poisson's equation, as derived in [4]. From the above comparisons, it is clearly demonstrated that the developed threshold-voltage model can accurately predict

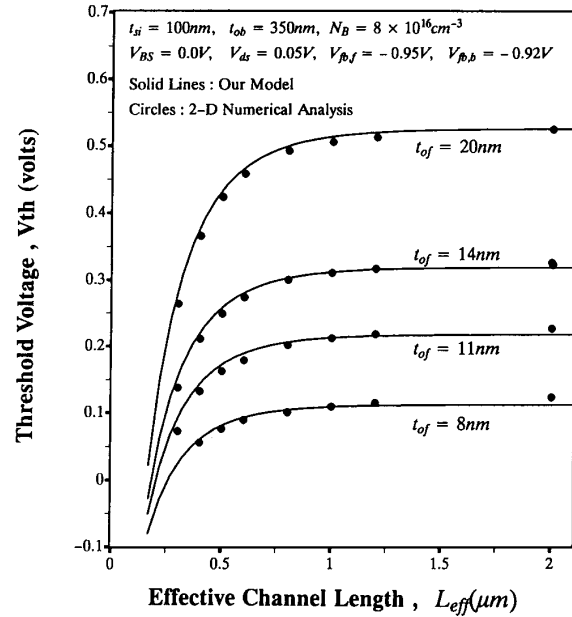


Fig. 6. Calculated threshold voltages as a function of the effective channel length with the thickness of the front-gate oxide as a parameter.

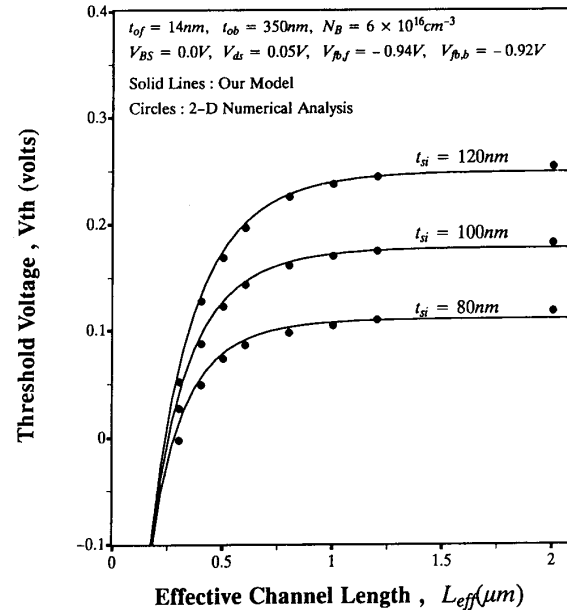


Fig. 7. Calculated threshold voltages as a function of the effective channel length with the thickness of the Si film as a parameter.

the threshold behavior of fully depleted short-channel SOI MOSFET's with different structure parameters under different applied biases.

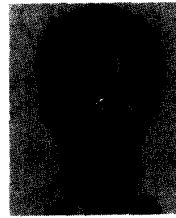
IV. CONCLUSION

The exact solution of the 2D Poisson's equation has been analytically derived by a three-zone Green's function solution technique with the suitable boundary con-

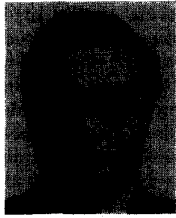
ditions in different zones. The accuracy of the derived 2D potential distribution in the Si film has been verified by 2D numerical analysis. This indicates that the uses of the artificial linear potentials at the oxide edges are sufficient for solving the 2D Poisson's equation. Based on the accurate 2D potential distribution, the analytic potential distribution at both front and back surfaces in the Si film are derived. It is shown that the location of the minimum surface potential can only be solved iteratively and the computation effort taken by the iteration procedure is small. Moreover, an analytic threshold-voltage model is derived and compared with the 2D numerical analysis. It is clearly shown that good agreements are obtained between the developed threshold-voltage model and the 2D numerical analysis for wide ranges of device structure parameters and applied biases. The accurate analytic threshold-voltage model can provide a fast physical analysis of the short-channel effect and further give the scaling rule for deep-submicrometer thin-film SOI MOSFET's in ULSI.

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