

# High-Density MIM Capacitors Using $\text{Al}_2\text{O}_3$ and $\text{AlTiO}_x$ Dielectrics

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**Abstract**—We have investigated the electrical characteristics of  $\text{Al}_2\text{O}_3$  and  $\text{AlTiO}_x$  MIM capacitors from IF (100 KHz) to RF (20 GHz) frequency range. Record high capacitance density of 0.5 and 1.0  $\mu\text{F}/\text{cm}^2$  are obtained for  $\text{Al}_2\text{O}_3$  and  $\text{AlTiO}_x$  MIM capacitors, respectively, and the fabrication process is compatible to existing VLSI backend integration. However, the  $\text{AlTiO}_x$  MIM capacitor has very large capacitance reduction as increasing frequencies. In contrast, good device integrity has been obtained for  $\text{Al}_2\text{O}_3$  MIM capacitor as evidenced from the small frequency dependence, low leakage current, good reliability, small temperature coefficient, and low loss tangent.

**Index Terms**—Capacitor, dielectric constant, frequency dependence, high  $k$ , MIM, RF.

## I. INTRODUCTION

THE MIM capacitor is one of the essential devices for analog and RF ICs, but the area of total MIM capacitors usually occupies a large portion of the whole ICs. To increase the circuit density and reduce the cost, large capacitance density is highly desirable. However, the attempt to increase the capacitance density  $\epsilon_0 k/t_d$  by reducing the dielectric thickness ( $t_d$ ) usually generates undesired high leakage current and poor RF loss tangent. The only choice left is to use high- $k$  dielectrics [1]–[5], and good IF to RF performance is obtained in high- $k$   $\text{AlTiO}_x$  and  $\text{Al}_2\text{O}_3$  MOS capacitors [5]. In this paper, we have further investigated the device integrity of high- $k$   $\text{AlTiO}_x$  and  $\text{Al}_2\text{O}_3$  MIM capacitors. In comparing with the MOS capacitors, the MIM capacitors faces the different challenge of low process temperature ( $\leq 400$  °C) for VLSI back-end process integration [6]. Under this process constraint, we have achieved record high capacitance density of 0.5 and 1.0  $\mu\text{F}/\text{cm}^2$  using respective  $\text{Al}_2\text{O}_3$  and  $\text{AlTiO}_x$  MIM capacitors, but the  $\text{AlTiO}_x$  MIM capacitor has fast capacitance reduction as frequency increases. In contrast, the  $\text{Al}_2\text{O}_3$  MIM capacitor has good device integrity of weak frequency and temperature dependence, small leakage current, and low-loss tangent that is good for circuit application.

## II. EXPERIMENTAL

The MIM capacitors were fabricated on 5000 Å  $\text{SiO}_2$  deposited 4-in Si substrate. In consideration of RF characterization, high- $k$   $\text{Al}_2\text{O}_3$  and  $\text{AlTiO}_x$  dielectrics with coplanar

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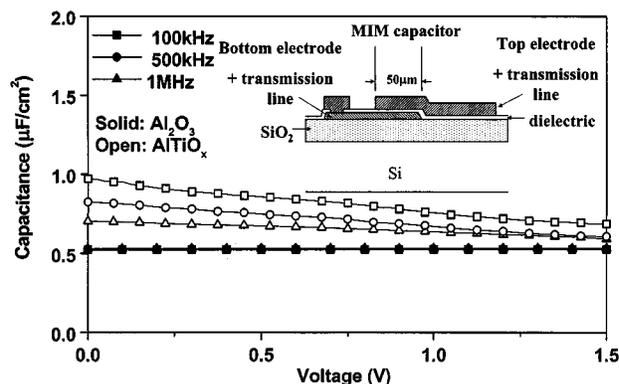


Fig. 1. Capacitance–voltage ( $C$ – $V$ ) characteristics of (a)  $\text{Al}_2\text{O}_3$  and (b)  $\text{AlTiO}_x$  MIM capacitors at the frequencies of 100 kHz, 500 kHz, and 1 MHz. The measured area is  $50 \mu\text{m} \times 50 \mu\text{m}$ .

transmission lines [7]–[10] were fabricated on  $\text{SiO}_2$ , where the transmission line also serves as top and bottom electrodes of MIM capacitor. Then thin Al or Ti/Al bi-layer was deposited under high vacuum condition on patterned bottom Pt/Ti transmission line, followed by subsequent oxidation and annealing at 400 °C to form respective high- $k$   $\text{Al}_2\text{O}_3$  and  $\text{AlTiO}_x$  [3]–[5]. The motivation of utilizing  $\text{AlTiO}_x$  is to increase  $k$  and capacitance density. In addition, it also preserves the merit of slow oxygen diffusion through Al–O matrix. Finally, Al is used for both top capacitor electrode and transmission line, and the device area is  $50 \mu\text{m} \times 50 \mu\text{m}$ . The  $\text{Al}_2\text{O}_3$  and  $\text{AlTiO}_x$  capacitors were measured using an HP4284A precision LCR meter at 100 KHz and 1 MHz, while the  $S$ -parameters were measured by HP8510C network analyzer ranging from 200 MHz to 20 GHz. The measured  $S$ -parameters are de-embedded from a dummy device and the high-frequency capacitance plus resistance values are extracted using an equivalent circuit model.

## III. RESULTS AND DISCUSSION

We have first measured the low-frequency capacitance. Fig. 1 depicts the  $C$ – $V$  characteristics for  $\text{Al}_2\text{O}_3$  and  $\text{AlTiO}_x$  at frequencies ranging from 100 KHz to 1 MHz, respectively, while the inserted figure is the cross-sectional view of device including the MIM capacitor and transmission line. Record high-capacitance density of 0.5 and 1.0  $\mu\text{F}/\text{cm}^2$  are measured for respective  $\text{Al}_2\text{O}_3$  and  $\text{AlTiO}_x$  at 100 KHz, but  $\text{AlTiO}_x$  capacitor shows fast capacitance reduction and large voltage dependence at this IF region. Therefore, the merit of high-capacitance density for  $\text{AlTiO}_x$  capacitor is vanished even increasing frequency to 1 MHz. In contrast, the  $\text{Al}_2\text{O}_3$  MIM capacitor shows much less capacitance change at this

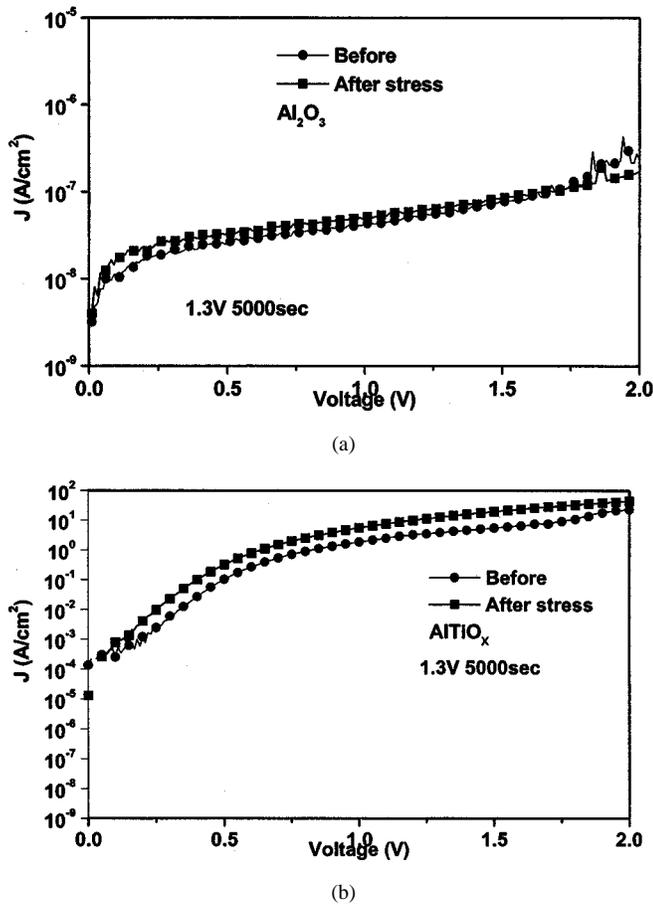


Fig. 2.  $J$ - $V$  characteristics of (a)  $\text{Al}_2\text{O}_3$  and (b)  $\text{AlTiO}_x$  MIM capacitors before and after stress. The small change of leakage current in  $\text{Al}_2\text{O}_3$  MIM capacitor after stress suggests the excellent reliability.

frequency range. The average voltage dependence of  $\text{Al}_2\text{O}_3$  MIM capacitor from 100 KHz to 1 MHz follows the voltage dependence of  $C_o(\alpha \times V^2 + \beta \times V + 1)$ , where  $C_o$ ,  $\beta$ , and  $\alpha$  are  $0.5263 \mu\text{F}/\text{cm}^2$ ,  $1888 \text{ ppm}/\text{V}$ , and  $2051 \text{ ppm}/\text{V}^2$ , respectively. Although the first and second-order voltage coefficients are larger than  $\text{SiO}_2$  and  $\text{Si}_3\text{N}_4$  [11] and may be due to the thinner thickness used here, the capacitance change is only  $\sim 2\%$  for a 3.3 V applied voltage that is acceptable for most circuit applications.

Fig. 2(a) and (b) shows the  $J$ - $V$  characteristics of  $\text{Al}_2\text{O}_3$  and  $\text{AlTiO}_x$  MIM capacitors before and after constant voltage stress, respectively. The leakage current in  $\text{Al}_2\text{O}_3$  capacitor is  $4.3 \times 10^{-8} \text{ A}/\text{cm}^2$  at 1 V, which is several orders of magnitude smaller than that in  $\text{AlTiO}_x$  capacitor at the same physical thickness of 120 Å. The large leakage current in  $\text{AlTiO}_x$  capacitor may be due to both smaller bandgap and weaker Ti-O bond related higher defects, which is further confirmed by the large stress-induced leakage current (SILC) in  $\text{AlTiO}_x$  MIM capacitor. In contrast, only small amount of SILC is observed for  $\text{Al}_2\text{O}_3$  capacitor that demonstrates the good reliability for circuit application.

Fig. 3 illustrates the temperature dependence for  $\text{Al}_2\text{O}_3$  and  $\text{AlTiO}_x$  MIM capacitors. Low temperature coefficient of capacitance (TCC) of 109, 138 and 208 PPM/ $^\circ\text{C}$  are measured in  $\text{Al}_2\text{O}_3$  MIM capacitor at 150  $^\circ\text{C}$  and respective 1 MHz,

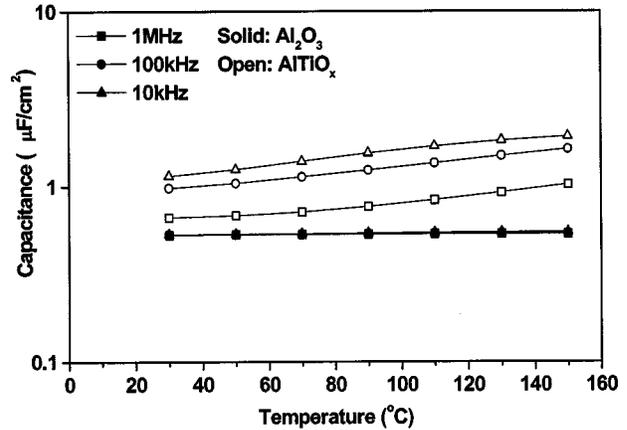


Fig. 3. Frequency and temperature dependence of capacitance for  $\text{Al}_2\text{O}_3$  and  $\text{AlTiO}_x$  MIM capacitors.

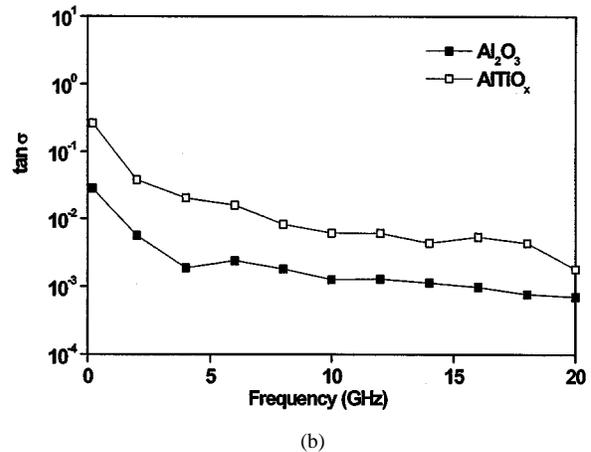
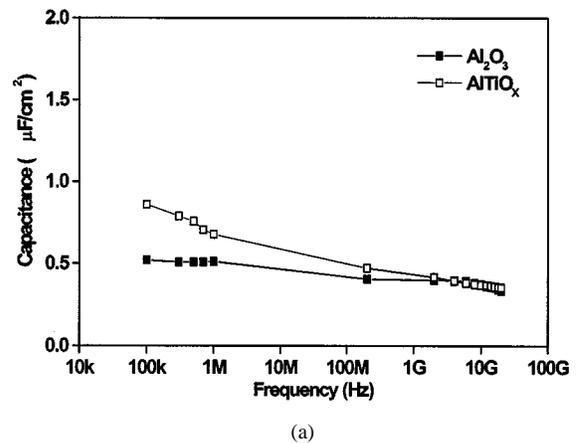


Fig. 4. (a) Frequency-dependent capacitance and (b) loss tangent for  $\text{Al}_2\text{O}_3$  and  $\text{AlTiO}_x$  MIM capacitors.

100 KHz, and 10 KHz, which is comparable with the temperature coefficient of  $\text{Si}_3\text{N}_4$  capacitor reported in the literature [11]. In contrast, near one order of magnitude larger TCC is found in  $\text{AlTiO}_x$  capacitors that make this capacitor have only limited application.

Fig. 4(a) and (b) demonstrate the frequency-dependent capacitance reduction and the loss tangent for various gate dielectrics, respectively. Only a small amount of capacitance reduction for  $\text{Al}_2\text{O}_3$  MIM capacitor is found over the entire frequency range

from 100 KHz to 20 GHz. In sharp contrast, large capacitance reduction is observed for AlTiO<sub>x</sub> MIM capacitor, and the capacitance gradually reduces to the same value as Al<sub>2</sub>O<sub>3</sub> MIM capacitor. The reason why AlTiO<sub>x</sub> MIM capacitor exhibits severe capacitance reduction may be due to the high defect density and slow time constant of traps from Ti–O<sub>x</sub>. The high RF performance of Al<sub>2</sub>O<sub>3</sub> MIM capacitor can also be evidenced from the very low loss tangent even at a large capacitor area of 2500 μm<sup>2</sup>, which is one order of magnitude smaller than that of AlTiO<sub>x</sub> capacitor. However, those values of loss tangent are still low enough to be used for RF application [12].

#### IV. CONCLUSION

Higher *k* dielectric is not necessary to give higher capacitance at high frequencies and other device integrity should be considered. The Al<sub>2</sub>O<sub>3</sub> MIM capacitor exhibits low leakage current, small frequency-dependent capacitance reduction, low TCC, low loss tangent, and good reliability that is suitable for circuit application at high frequencies.

#### REFERENCES

- [1] S. J. Lee, H. F. Luan, C. H. Lee, T. S. Jeon, W. P. Bai, Y. Senzaki, D. Roberts, and D. L. Kwong, "Performance and reliability of ultra thin CVD HfO<sub>2</sub> gate dielectrics with dual poly-Si gate electrodes," in *Proc. Symp. VLSI Technology*, 2001, pp. 133–134.
- [2] L. Kang, Y. Jeon, K. Onishi, B. H. Lee, W.-J. Qi, R. Nieh, S. Gopalan, and J. C. Lee, "Single-layer thin HfO<sub>2</sub> gate dielectric with *n*<sup>+</sup>-polysilicon gate," in *Proc. Symp. VLSI Technology*, 2000, pp. 44–45.
- [3] A. Chin, C. C. Liao, C. H. Lu, W. J. Chen, and C. Tsai, "Device and reliability of high-*k* Al<sub>2</sub>O<sub>3</sub> gate dielectric with good mobility and low *D<sub>it</sub>*," in *Proc. Symp. VLSI Technology*, 1999, pp. 133–134.
- [4] M. Y. Yang, S. B. Chen, A. Chin, C. L. Sun, B. C. Lan, and S. Y. Chen, "One-transistor PZT/Al<sub>2</sub>O<sub>3</sub>, SBT/Al<sub>2</sub>O<sub>3</sub> and BLT/Al<sub>2</sub>O<sub>3</sub> stacked gate memory," in *IEDM Tech. Dig.*, Washington, DC, Dec. 2001.
- [5] A. Chin, S. B. Chen, K. T. Chan, J. C. Hsieh, M. H. Chang, C. C. Lin, and J. Liu, "RF performance limitation of high-*k* AlTiO<sub>x</sub> and Al<sub>2</sub>O<sub>3</sub> gate dielectrics," in *Proc. Int. Workshop on Gate Insulator*, Tokyo, Japan, Nov. 2001, pp. 62–63.
- [6] Y. H. Wu, A. Chin, K. H. Shih, C. C. Wu, C. P. Liao, S. C. Pai, and C. C. Chi, "The fabrication of very high resistivity Si with low loss and cross talk," *IEEE Electron Device Lett.*, vol. 21, p. 394, Sept. 2000.
- [7] K. T. Chan, A. Chin, C. M. Kwei, D. T. Shien, and W. J. Lin, "Transmission line noise from standard and proton-implanted Si," in *MTT-S Dig.*, 2001.
- [8] K. T. Chan, A. Chin, Y. B. Chen, Y.-D. Lin, D. T. S. Duh, and W. J. Lin, "Integrated antennas on Si, proton-implanted Si and Si-on-quartz," in *IEDM Tech. Dig.*, Washington, DC, Dec. 2001.
- [9] Y. H. Wu, A. Chin, K. H. Shih, C. C. Wu, C. P. Liao, S. C. Pai, and C. C. Chi, "RF loss and crosstalk on extremely high resistivity (10k–1MΩ-cm) Si fabricated by ion implantation," in *Proc. IEEE Microwave Theory and Techniques Soc. Int. Microwave Symp.*, 2000, pp. 221–224.
- [10] Y. H. Wu, A. Chin, C. S. Liang, and C. C. Wu, "The performance limiting factors as RF MOSFETs scale down," in *Radio Frequency Integrated Circuits Symp.*, 2000, pp. 151–155.
- [11] J. A. Babcock, S. G. Balster, A. Pinto, C. Dirnecker, P. Steinmann, R. Jumpertz, and B. El-Kareh, "Analog characteristics of metal-insulator-metal capacitors using PECVD nitride dielectrics," *IEEE Electron Device Lett.*, vol. 22, pp. 230–232, May 2001.
- [12] C.-M. Hung, Y.-C. Ho, I.-C. Wu, and K. O, "High-Q capacitors implemented in a CMOS process for low-power wireless applications," in *MTT-S Dig.*, 1998, pp. 505–511.