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Study of carbon nanoemitters using CO₂–CH₄ gas mixtures in triode-type field emission arrays

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Abstract

In this study, we used methane/carbon dioxide gas mixtures to deposit nanoemitters on the gate-structured metal-insulatorsemiconductor (MIS) diodes. Due to the nanoemitters growing in a high carbon concentration gas source of CH_4/CO_2 , the growth rate is better than conventional mixtures by using hydrocarbons diluted in hydrogen. Moreover, the bias also promotes the growth rate in the CH_4/CO_2 gas mixtures. In addition, selected area deposition (SAD) of emitters was successfully achieved by using the Pt layer as inhibitor in the bias assisted microwave plasma chemical vapor deposition (BAMPCVD). The field emission current and the current density of nanoemitters on field emission arrays (FEAs) are 154 μ A and 490 mA cm⁻², respectively. This may be due to the following reasons: (I) short gate-emitter spacing; (II) small gate aperture; and (III) emitter with a sharp feature. © 2002 Elsevier Science B.V. All rights reserved.

Keywords: Field emission; CVD; SEM; Raman spectroscopy

1. Introduction

The emission of electrons from the surface of diamond and diamond-like carbon (DLC) films has attracted much attention due to its potential application in cold cathode devices. Diamond possesses negative electron affinity (NEA), in addition to remarkable characteristics such as large thermal conductivity and being chemically inert. Many studies have focused on fabricating diamond films [1] or diamond-clad silicon field emission arrays (FEAs) [2].

The most popular gas mixtures for the chemical vapor deposition (CVD) of diamond are hydrocarbon diluted in hydrogen. Therefore, hydrogen is a very high percentage in the gas mixtures and the diluted hydrocarbon gas is the only source of carbon. In this study, we used methane/carbon dioxide to deposit carbon nanoemitters in triode-type FEAs without hydrogen dilution. Furthermore, the selective area deposition (SAD) of nanoem-

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itters by using a Pt layer as a nucleation inhibitor is also successfully completed in the CVD process.

2. Experiment

The fabrication process of the metal-insulator-semiconductor (MIS) diode and diamond-like deposition procedure have been previously presented [3]. First, we made the MIS diode structure by semiconductor process technology. Starting substrates were mirror-polish n-type, (100) oriented wafers with a resistivity of $4.5 \sim 5.5 \Omega$ cm⁻¹. After fabricating the MIS diode, specimens were put in the bias assisted microwave plasma chemical vapor deposition (BAMPCVD) system to deposit diamond with various deposition parameters. Fig. 1 shows the cross-section of the triode-type structure. The reactive gases used in deposition were the mixtures of CH₄- CO_2 . The flow rates of CH_4/CO_2 and deposition time remained constant at 30/30 sccm and 15 min, respectively. While processing the deposition, the specimens were subjected to a negative bias voltage. Table 1 lists the experimental conditions.

Scanning electron microscopy (SEM) was used to observe the morphology of nanoemitters on FEAs,

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Fig. 1. The schematic cross-section of a triode device structure.

micro-Raman spectroscopy was used to identify the quality of emitters, and an I-V measuring system to obtain their field emission characterization. The field emission properties of the FEAs were measured by using a triode technique. An anode plate, an ITO glass,







Fig. 3. SEM photograph of sample B growing under 130 V.



Table 1Deposition conditions of nanoemitter

Sample	Flow rate CH_4/CO_2 (sccm)	Negative bias (V)	Deposition time (min)
A	30/30	120	15
В	30/30	130	15
С	30/30	150	15
D	30/30	180	15

Total pressure = 15 torr; microwave power = 300 W; Substrate temp. \approx 750 °C.



Fig. 2. SEM photograph of sample A growing under 120 V.





Fig. 4. SEM photograph of sample C growing under 150 V.

3. Results and discussion

Figs. 2-5 show SEM photographs of nanoemitters growing under different bias. Each picture is only one of the 50×50 circles on the FEAs. Every picture in the bottom side is an enlarged image of the top one. The width and the depth of the hole are 4 μ m and 7000 Å, respectively. Under the bias below 130 V, there is only low density of tiny tips inside the hole. Fig. 2 shows a SEM photograph of the nanoemitters growing under 120 V. Once we increased the deposition time, which is over 15 min, not only inside the hole but also on the Pt-gate were carbon materials. Indeed, the carbon materials may replace the Pt to form the carbon-gated FEAs. In the worst case scenario, this phenomenon will taint the I-V measurement because the field emission current maybe inexact because of the influence from both the nanoemitters and carbon-gated surface. Figs. 4 and 5 display that higher bias drastically enhances the growth



RKV X1

Fig. 5. SEM photograph of sample D growing under 180 V.



Fig. 6. Raman spectra of nanoemitters growing under different applied bias.

(a)



Fig. 7. (a) The emission current (I_a) vs. the gate voltage (V_{gc}) and (b) the emission current density (J_e) of nanoemitters growing under 130 V on FEAs.

rate of nanoemitters. However, this leads the triode-type device to a diode structure because of the emitters exceeding the height of the hole.

Examining Figs. 2–5, we get the following results. (I) The applied bias can enhance the growth rate of nanoemitters with CH_4/CO_2 gas mixtures in CVD process. Moreover, the higher bias is applied, the higher growth rate is obtained. The bias effect is also valid for the hydrocarbon diluted in hydrogen [4]. (II) The growth rate of CH_4/CO_2 gas mixtures is better than the conventional gas CH_4/H_2 [5]. This is due to the nanoemitters growing in a high carbon concentration gas source of CH_4/CO_2 . (III) In addition, the deposition under high bias will cause higher density, larger and sharper nanoemitters. Thus, emitters with sharp features have small radius curvature and high aspect ratio. (IV) All the emitters directly grow upward under bias condition.



Fig. 8. The F-N plot of nanoemitters growing under 130 V on FEAs.

The region for diamond growth is indicated in the C–H–O diagram presented by Bachmann et al. [6]. The parameter range for diamond deposition from CH₄/CO₂ is narrower than the CH₄/H₂. The molar ratio of CH₄/CO₂=1.08~1.63 is needed for diamond growth. In terms of the ratio of C/O, the range is from 1.04 to ~1.31. When the ratio is out of this range, the graphite is found on the deposit. In other words, the growth conditions are very restrictive. Nevertheless, due to the difference of various deposition conditions such as microwave power, pressure and substrates, etc., the results maybe change. In this study, the volume ratio of CH₄/CO₂=1 is used.

Fig. 6 shows Raman spectra of emitters growing under applied various bias. We find there is no obvious difference among these spectra. All of them have two sharp peaks located at approximately 1355 cm^{-1} and 1582 cm^{-1} , respectively. These imply that the nanoemitters are characteristic of microcrystalline graphite. The Raman spectra of amorphous carbon can be decomposed into two features located approximately at 1550 cm⁻¹ (corresponding to the G-line associated with the optically allowed E_{2g} zone center mode of crystalline graph-ite) and 1350 cm⁻¹ (roughly corresponding to the D-line associated with disorder-allowed zone-edge modes of graphite). The positions, widths, and relative intensities of these two peaks are found to vary systematically with deposition conditions and emitters' properties [7,8]. The intensity of these two peaks is the same. This indicates that there is much amorphous carbon in the samples.

Fig. 7 displays the electron-emitting characteristic of the nanoemitters growing under 130 V on FEAs. If the applied bias is above 130 V, the emitters will grow over the hole. This indicates that the emitters exceed the height of Pt-gate. Under this condition, the device becomes diode structure instead of triode-type one. The field emission current (I_a) of nanoemitters from all of the holes on FEAs is approximately 154 μ A (at gateto-cathode voltage, $V_{\rm gc} = 50$ V). A display requires at least 1 mA cm⁻² to excite the phosphorus pixel. Thus the emission current density ($J_{\rm e}$) obtained from nanoemitters on all holes is approximately 490 mA cm⁻². Therefore, the higher emission current density of the nanoemitters on FEAs is due to the following reasons: (I) short gate-emitter spacing; (II) small gate aperture; and (III) emitters possessing sharp feature with small radius [9,10]. It has been shown that the tip radius of curvature and the gate aperture influence the field enhancement factor β most significantly; i.e. β increases rapidly as tip radius and gate aperture decrease [11– 13].

The threshold voltage $(V_{\rm th})$ is defined as the intersection of the F-N plots shown in Fig. 8 with the abscissa. The threshold voltage $(V_{\rm th})$ is approximately 13 V for the nanoemitters. Due to the smaller gate aperture; diameter and shallower gate structure, a field emission can be triggered at a lower threshold voltage. However, there is a saturated current at $V_{\rm gc} = 50$ V for nanoemitters on FEAs. This phenomenon is caused by the fact that the dielectric layer (SiO₂) in the developed pattern may be broken under high applied voltage, which will create a leakage current.

4. Conclusions

In this study, we successfully fabricated the triodetype FEAs for decreasing the turn-on voltage and increasing the emission current. Results are summarized as follows.

- First, we successfully completed the selective area deposition (SAD) of nanoemitters by using a Pt-layer as a nucleation inhibitor in the BAMPCVD process.
- 2. Due to the nanoemitters growing in a high carbon concentration gas source of CH_4/CO_2 , the growth

rate is better than with conventional mixtures by using hydrocarbons diluted in hydrogen (CH_4/H_2) .

- 3. The higher bias is applied, the higher the growth rate obtained in the CH_4/CO_2 gas mixtures.
- 4. The emission current and current density for the nanoemitters on FEAs are 154 μ A and 490 mA cm⁻², respectively. The turn-on voltage is approximately 13 V. This is due to the effect of: (I) small gate aperture; (II) the short gate-emitter spacing; and (III) emitters possessing sharp features with small radius.

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