

# A Physical Model for Hole Direct Tunneling Current in P<sup>+</sup> Poly-Gate PMOSFETs with Ultrathin Gate Oxides

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**Abstract**—A model of the hole direct tunneling gate current accounting for heavy and light hole's subbands in the quantized inversion layer is built explicitly. This model comprises four key physical parameters: inversion layer charge density, hole impact frequency on SiO<sub>2</sub>/Si interface, WKB transmission probability, and reflection correction factor. With the effective hole mass  $m_{oxh} = 0.51m_o$  for parabolic dispersion relationship in the oxide, experimental reproduction without any parameter adjustment is consistently achieved in p<sup>+</sup> poly-gate pMOSFETs with 1.23, 1.85, and 2.16 nm gate oxide thicknesses. The proposed model can thereby serve as a promising characterization means of direct tunnel oxides. In particular, it is calculated that the secondary subbands and beyond, although occupying few holes, indeed contribute substantially to the direct tunneling conduction due to effective lower barrier heights, and are prevailing over the first subbands for reducing oxide field down below 1 MV/cm.

## I. INTRODUCTION

THE MOSFET gate oxide thickness is rapidly approaching the direct tunneling limit that ultimately leads to intolerably increased standby power [1] and/or impractical applications [2]. Thus, accurate characterization and modeling of ultrathin oxides in the direct tunneling regime is essential and crucial. A series of models have recently been published concerning the electron direct tunneling in n<sup>+</sup> poly-gate nMOSFETs: a self-consistent numerical method of solving Schrödinger's and Poisson's equations [1] and computationally efficient models [3], [4]. The latter can provide more transparent understandings since it is made up of four key physical parameters: accumulation or inversion layer charge density, electron impact frequency on interface, WKB transmission probability, and specially, the reflection correction factor [3], [4]. As to another complementary devices, namely, p<sup>+</sup> poly-gate pMOSFETs, the hole direct tunneling under channel inversion condition was found to dominate over valence electron direct tunneling [5], followed by

more evidences [6], [7]. In this paper, we present a model of the hole direct tunneling current featuring the above four similar physical parameters. This model can serve as a promising means of sensitively characterizing direct tunnel oxides and can enable in-depth understandings of the roles of the subbands in the quantized inversion layer.

## II. CHARACTERIZATION AND PARAMETER EXTRACTION

The p<sup>+</sup> poly-gate pMOSFETs were fabricated by a 0.18- $\mu$ m process technology [8] with gate oxides grown in diluted wet oxygen ambient to three different thicknesses. The gate dimension was drawn to 100  $\times$  100  $\mu$ m<sup>2</sup>. Accurate determination of ultrathin oxide thickness  $T_{ox}$  is strongly demanded. Three techniques in terms of high resolution TEM(HRTEM), polysilicon depletion and quantum mechanics corrected capacitance-voltage ( $C-V$ ) [9]–[11], and direct tunneling (DT)  $I-V$  [3] were adopted as shown in Fig. 1, through which consistent results were achieved as compared in Fig. 2. Fig. 1(a) just shows highly-localized HRTEM cross section while the variation across the wafer is depicted in Fig. 2 in terms of a bar. Our  $C-V$  data in Fig. 1(b) was measured in parallel mode with 1-MHz AC frequency. QM corrected  $C-V$  fitting based on van Dort's model for surface quantization [9], [10] was carried out to extract physical  $T_{ox}$ . In particular, the singular point problem encountered around the flat-band voltage  $V_{FB}$  was eliminated by adopting a modified version [11]. In Fig. 1(b),  $C-V$  fitting for  $T_{ox} = 1.3$  nm is limited to nondistorted range,  $-0.6$  V  $< V_G < 0.6$  V, where the tunneling current effect or others can be neglected.  $C-V$  fitting in Fig. 1(b) also produced the p<sup>+</sup> polysilicon dopant concentration  $N_{poly} = 4.2 \times 10^{19}$  cm<sup>-3</sup> and the effective channel dopant concentration  $N_{well} = 4 \times 10^{17}$  cm<sup>-3</sup>, all being found to be consistent with the SIMS doping profile. In Fig. 1(c), the devices were biased in channel accumulation (positive gate voltage) with source, drain, and n-well tied to ground, and the oxide field strength  $E_{ox}$  was get in advance by means of the well-known  $C-V$  integration technique. With the effective electron mass  $m_{oxe} = 0.61m_o$  for Franz-type dispersion relationship in the oxide, the conduction electron DT  $I-V$  fitting in Fig. 1(c) extracted  $T_{ox} = 1.23, 1.85,$  and  $2.16$  nm from three samples. Note that as all data go closer to the straight line with the unity slope in Fig. 2, more confidence for DT  $I-V$  extracted  $T_{ox}$ , as well as its subsequent applications in consistently calculating the DT hole current, can all be ensured.

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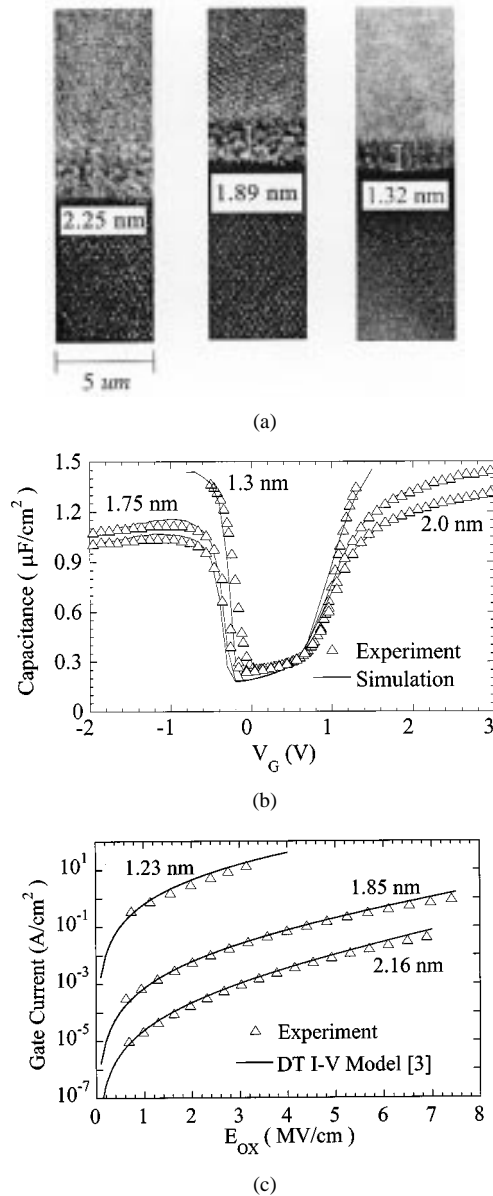


Fig. 1. (a) HRTEM images of three pMOSFET gate stacks.  $T_{ox}$  values extracted from the canvases correspond to 2.25, 1.89, and 1.32 nm, respectively. (b) The oxide thickness extraction using  $C-V$  method was based on van Dorst's model [9] and successive researchers [10], [11]. Best fitting produces  $T_{ox}$  of 2.0, 1.75, and 1.3 nm, respectively. (c)  $I-V$  fitting to find  $T_{ox}$ .  $T_{ox}$  values extracted by electron direct tunneling model [3], including quantization effect in accumulation layer under  $V_G > V_{FB}$ , are 2.16, 1.85, and 1.23 nm, respectively.

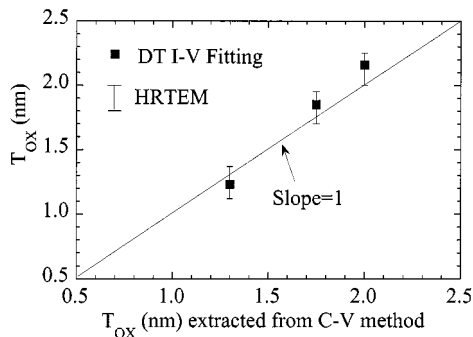


Fig. 2. Comparing  $T_{ox}$  extracted from several techniques.

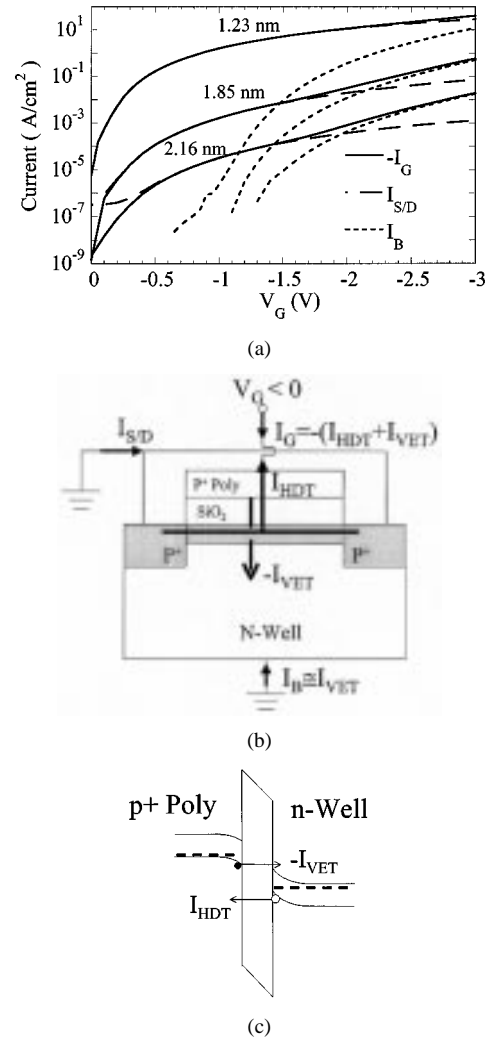


Fig. 3. (a) Measured hole direct tunneling current  $I_{HDT} (\approx I_{S/D})$  and valence electron tunneling current  $I_{VET} (\approx I_B)$  of three different  $T_{ox}$  under  $V_G < 0V$ . (b) The carrier separation configuration and (c) band diagram representation are schematically drawn.

With source, drain, and n-well tied to ground, the source/drain current ( $I_{S/D}$ ), bulk current ( $I_B$ ), and gate current ( $I_G$ ) measured in inversion are plotted in Fig. 3(a) versus gate voltage ( $V_G$ ). The corresponding carrier separation configuration and the band diagram representation are schematically drawn in Fig. 3(b) and (c), respectively. It can be identified that the hole direct tunneling is responsible for source/drain current while the valence electron direct tunneling constitutes bulk current. It is seen from Fig. 3 that the hole direct tunneling dominates over the valence-band electron direct tunneling in gate voltage of 0 V to certain crossover point, which is consistent with [5]–[7]; however, the magnitude of this crossover gate voltage increases with decreasing oxide thickness, a phenomenon not mentioned before. A specific criterion of 1 A/cm<sup>2</sup> at gate voltage of  $-1$  V sets an ultimate limit of slightly thicker than 1.23 nm, again consistent with [2]. The impact of hole direct tunneling on normal device operation can be found in Fig. 4, revealing that the hole direct tunneling from inverted channel can reverse the polarity of the linear drain current. The similar drain current degradations for the n-channel counterpart have been reported

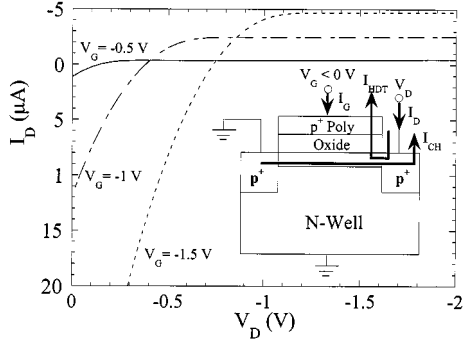


Fig. 4. Measured output  $I$ - $V$  characteristics with  $W/L = 10 \mu\text{m}/50 \mu\text{m}$  and  $T_{ox} = 1.23 \text{ nm}$ .  $I_D$  reverses its polarity at small  $|V_D|$  due to direct hole tunneling.

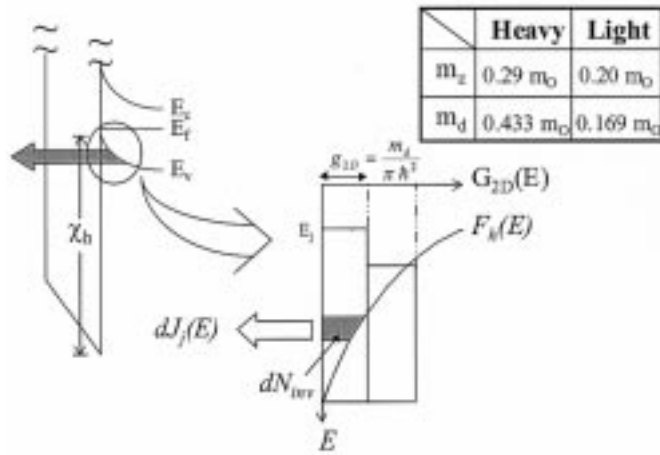


Fig. 5. Physical picture of valence hole quantized phenomenon along  $\langle 100 \rangle$  direction.  $G_{2D}(E)$  indicates density of states relative to hole's energy.  $F_h(E)$  represents Fermi-Dirac distribution function of hole and  $E_j$  means the quantized extremity energy of the  $j$ th subband.

previously [12], [13]. The impact of hole DT as outlined in Fig. 4 is one of the primary motivations of the underlying study. To clarify the essential mechanisms, however, the work focuses on the case of tying source and drain to a common potential.

### III. PHYSICAL MODEL

The two valence bands (heavy and light band), which are degenerate at  $k = 0$  ( $\Gamma$  point), have the form of warped spheres; therefore, the  $k \cdot p$  method concerning degenerate perturbation is necessary for the valence band [14]:

$$E_{V_{1,2}}(k) = \frac{\hbar^2}{2m_o} \{ Ak^2 \pm (B^2 k^4 + C^2 [k_x^2 k_y^2 + k_y^2 k_z^2 + k_z^2 k_x^2])^{1/2} \}.$$

Fig. 5 schematically shows the physical picture of the valence-band hole tunneling from the silicon inversion layer. In Fig. 5, a table lists the effective mass ( $m_z$ ) along  $\langle 100 \rangle$  direction and density of states mass ( $m_d$ ) of each type of hole as cited in [15]. The direct tunneling hole current from each subbands can be formulated in analogy with the mathematical treatment of

electron direct tunneling in [3]. Hole current density contributed by  $j$ th subband with energy  $E$  to  $E + dE$  can be written as

$$dJ_j(E) = qf_j g(E) P_t(E) dE \quad (1)$$

where

- $q$  elemental charge;
- $f_j$  impact frequency of hole's wave packet on  $\text{SiO}_2/\text{Si}$  interface;
- $g(E)$  inversion layer charge density per unit area and magnitude of  $E$  associated with  $j$ th subband;
- $P_t(E)$  transmission probability through  $\text{SiO}_2$  layer.

Denoting  $z_j$  as the classical turning point at the  $j$ th subband edge and  $v_{\text{Si},\perp}(z) = \sqrt{2(E_j - qV(z))/m_z}$  as the velocity of wave packet

$$f_j = \left[ 2 \int_0^{z_j} \frac{dz}{v_{\text{Si},\perp}(z)} \right]^{-1} = \frac{q\epsilon_{ox}|E_{ox}|}{4\epsilon_{\text{Si}}} \left( \frac{m_z E_j}{3} \right)^{-1/2}. \quad (2)$$

Triangle-like electrostatic potential is a good approximation for  $V(z)$ . Using Sommerfeld-Wilson's quantization rule, we are able to get quantized energy along  $z$ -direction or  $\langle 100 \rangle$  direction in reciprocal space

$$E_j = \left( \frac{\hbar^2}{2m_z} \right)^{1/3} \left[ \frac{3\pi q\epsilon_{ox}|E_{ox}|(j-1/4)}{2\epsilon_{\text{Si}}} \right]^{2/3}.$$

For inversion charge increment

$$dN_{inv} = \int_E^{E+dE} g_{2D} F_h(E) dE \simeq g_{2D} F_h(E) dE$$

$$g(E) = \frac{dN_{inv}}{dE} = g_{2D} F_h(E). \quad (3)$$

In (3), density of states per unit area for two-dimensional hole gas (2DHG),  $g_{2D}$ , is equal to  $m_d/\pi\hbar^2$  and Fermi-Dirac distribution function  $F_h(E)$  associated with valence holes is  $1/(1 + \exp(E_f - E/k_B T))$ .  $E_f$  is the quasi-Fermi level and  $k_B$  is Boltzmann's constant. Under the inversion condition, it is easy to build that  $|V_G - V_{FB}| = \phi_p + T_{ox}|E_{ox}| + \phi_s \cdot \phi_p$  means the potential drop on the p<sup>+</sup>-poly gate, and  $\phi_s$  signifies that on the n-well. Considering the poly-depletion effect, we know that  $\phi_p$  can be expressed as  $\epsilon_{ox}^2 E_{ox}^2 / (2q\epsilon_{\text{Si}} N_{poly})$ . Note that the values of oxide field strength  $E_{ox}$  are obtained by the  $C$ - $V$  integration technique so that the surface potential,  $\phi_s$ , can be computed directly. Thus,  $E_f$  is equal to  $\phi_{Bn} - \phi_s$ . Here  $\phi_{Bn} (= [E_g - K_B T \ln(N_C/N_D)]/q)$  is the potential difference between the quasi-Fermi level ( $E_f/q$ ) and valence band ( $E_V/q$ ) in the charge neutrality region of n-well.

Following [3],  $P_t(E)$  can be modeled by

$$P_t(E) = T_{WKB}(E) T_R(E) \quad (4)$$

where

$$T_{WKB}(E) = \exp \left[ -\frac{2}{\hbar} \int_0^{T_{ox}} \sqrt{2m_{ox} \hbar (E - qV(z))} dz \right]$$

$$= \exp \left( \frac{4\sqrt{2m_{ox} \hbar} (\varphi_{cath}^{3/2} - \varphi_{an}^{3/2})}{3q\hbar|E_{ox}|} \right).$$

$\varphi_{cath}$  is the barrier height of tunneling hole with total energy  $E$  at cathode side or p<sup>+</sup>-poly gate/ $\text{SiO}_2$  interface, and  $\varphi_{an}$  is

that at anode side or SiO<sub>2</sub>/n-well interface.  $\varphi_{cath} = q\chi_h - q|E_{ox}|T_{ox} - E$  and  $\varphi_{an} = q\chi_h - E$ . In our work, the SiO<sub>2</sub>/Si barrier height  $q\chi_h$  is 4.7 eV for both heavy and light hole. The total energy,  $E$ , consists of the transversal and longitudinal energies

$$E = \frac{\hbar^2(k_x^2 + k_y^2)}{2m_t} + E_j$$

where  $m_t$  is the transversal mass. On the other hand,  $T_R$  is a justified factor concerning wavefunction's reflection phenomenon occurring at SiO<sub>2</sub>/Si interfaces as discussed in [3] and [4], as follows:

$$T_R(E) = \frac{4v_{Si,\perp}(E)v_{ox}(\varphi_{an})}{v_{Si,\perp}^2(E) + v_{ox}^2(\varphi_{an})} \times \frac{4v_{Si,\perp}(E + q|E_{ox}|T_{ox})v_{ox}(\varphi_{cath})}{v_{Si,\perp}^2(E + q|E_{ox}|T_{ox}) + v_{ox}^2(\varphi_{cath})}$$

where  $v_{Si,\perp}(E)$  and  $v_{Si,\perp}(E + q|E_{ox}|T_{ox})$  are the group velocities of the holes incident and leaving the oxide, respectively. The group velocity of hole with energy  $E$  along  $\langle 100 \rangle$  direction within the  $j$ th subband at SiO<sub>2</sub>/Si interfaces is independent of  $E$  with the following expression:

$$v_{Si,\perp}(E) = v_{Si,\perp}(z=0) = \sqrt{\frac{2E_j}{m_z}}$$

Besides,  $v_{ox}(\varphi_{an})$  and  $v_{ox}(\varphi_{cath})$  are the magnitudes of the purely imaginary group velocities of holes at the cathode and anode side within the oxide, respectively. Parabolic dispersion relation,  $E_{SiO_2} = q\chi_h - E - qV_{ox}(x) = \hbar^2 k_{ox}^2 / 2m_{oxh}$ , is adopted to gauge the tunneling hole's behavior within the oxide film. Consequently, the imaginary group velocity which is dependent on  $E$  within oxide can be described by

$$v_{ox} = \frac{1}{\hbar} \frac{dE_{SiO_2}}{dk_{ox}} = \sqrt{\frac{2E_{SiO_2}}{m_{oxh}}}$$

This factor could not be neglected when electrostatic potential  $V(x)$  changes acutely with respect to position or the intensity of incident wave cannot be treated to being equal to that of reflection wave.

Recalling (1)–(4), the tunneling current density contributed by the  $j$ th subband with energy  $E$  ranging from  $E_j$ , the subband extremity, to infinity can be shown below:

$$\begin{aligned} J_j &= \int_{E_j}^{\infty} dJ_j(E) \\ &= \int_{E_j}^{\infty} qf_j g(E) P_t(E) dE \\ &= qf_j g_{2D} \int_{E_j}^{\infty} F_h(E) P_t(E) dE. \end{aligned}$$

An explicit model of hole direct tunneling current from all heavy and light holes, subbands is readily built as follows:

$$J_t = \sum_j \left[ \int_{E_{j,H}}^{\infty} qf_{j,H} g_H(E) P_{t,H}(E) dE + \int_{E_{j,L}}^{\infty} qf_{j,L} g_L(E) P_{t,L}(E) dE \right]$$

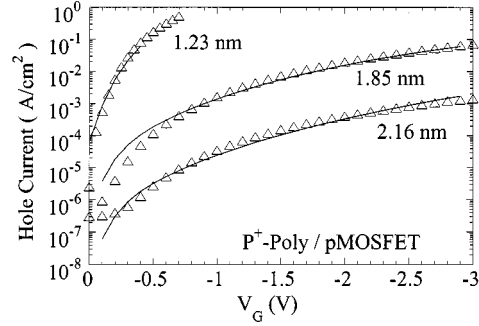


Fig. 6. Simulated results (lines) and experimental data (symbols) for pMOSFET direct tunneling hole current of three different  $T_{ox}$  under  $V_G < 0$  V.  $V_G$  can be related to  $E_{ox}$  by means of the  $C$ - $V$  integration technique.

$$= q \sum_j \left[ f_{j,H} g_{2D,H} \int_{E_{j,H}}^{\infty} F_h(E) P_{t,H}(E) dE + f_{j,L} g_{2D,L} \int_{E_{j,L}}^{\infty} F_h(E) P_{t,L}(E) dE \right]. \quad (5)$$

Index  $H$  and  $L$  mean heavy and light holes, respectively.

#### IV. CALCULATION AND DISCUSSION

Essentially, there are three input parameters to the model: the oxide field  $E_{ox}$ , the effective hole mass  $m_{oxh}$  for parabolic relationship in the oxide, and the oxide thickness  $T_{ox}$ . The other physical parameter values are unique and cannot be arbitrarily adjusted when fitting data. Fortunately, the well-recognized integration technique carried out directly on the  $C$ - $V$  curves in Fig. 1(b) can easily quantify  $E_{ox}$ . An effective hole mass  $m_{oxh} = 0.51m_o$  for heavy and light hole was found to be capable of modeling reasonably well the hole direct tunneling  $I$ - $V$  characteristics as plotted in Fig. 6 for three different oxide thicknesses. The corresponding oxide thicknesses were consistently identical to those involved with conduction-band electron tunneling in Fig. 1(c) due to the same samples used. Excellent reproduction in Fig. 6 can lead to one argument that the proposed model can find its potential applications in terms of a sensitive means of characterizing direct tunnel oxides.

Ten subbands for heavy hole and six subbands for light hole were used in above modeling. Based on the simplified method to calculate the subband energies [16], in-depth understandings concerning the roles of different subbands in the quantized inversion layer can be created fully. First of all, the occupation ratio, defined as  $N_{inv,j} / \sum N_{inv,j}$ , of the subbands was calculated versus oxide field as shown in Fig. 7. It can be seen that the heavy hole lowest (first) subband occupies most (90%) of all inversion holes and this ratio drops with lowering  $-E_{ox}$ . However, the light hole first subband, which occupies considerably 10%, exhibits opposite trend. Fig. 7 also reveals that the remaining subbands get few due to their higher quantized states whereas as we go toward lower  $-E_{ox}$ , more and more holes are filling the secondary subbands and beyond. Although higher energy states share far less carriers, the other factor such as the transmission probability can be much larger than the ground state due to effective lower barrier heights for tunneling. The resulting tunneling current thus contains a substantial component from the secondary subbands

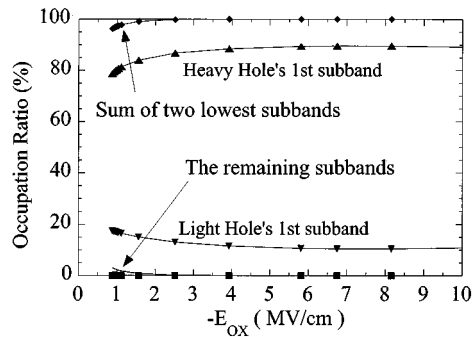


Fig. 7. Occupation ratio  $N_{inv,j} / \sum N_{inv,j}$  of all the subbands versus oxide field.  $N_{well} = 4 \times 10^{17} \text{ cm}^{-3}$ .

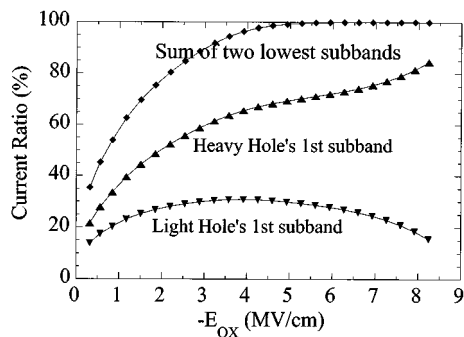


Fig. 8. Partial fraction  $J_j / \sum J_j$  of direct tunneling hole current contributed by two lowest subbands versus oxide field.  $T_{ox} = 1.85 \text{ nm}$ ;  $N_{well} = 4 \times 10^{17} \text{ cm}^{-3}$ ;  $N_{poly} = 4.2 \times 10^{19} \text{ cm}^{-3}$ .

and beyond. This is valid, in particular, under the condition of small  $-E_{ox}$  as clearly depicted in Fig. 8 in terms of the partial fraction defined as  $J_j / \sum J_j$ . For the first time, this figure points out that the hole direct tunneling from the secondary subbands and beyond prevails over the first subbands for reducing oxide field down below around 1 MV/cm.

V. CONCLUSION

A physical model of the hole direct tunneling through ultra-thin oxides has been built and experimental reproduction has been consistently achieved in p<sup>+</sup> poly-gate pMOSFETs for different oxide thicknesses. This model has evidenced its potential applications in sensitively characterizing direct tunnel oxides as well as enabling in-depth understandings of the role of the different subbands in affecting hole direct tunneling conduction.

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From 1994 to 1995, he worked on intermetal dielectric development for 0.35- $\mu\text{m}$  CMOS at Taiwan Semiconductor Manufacturing Company (TSMC), Hsinchu. Since September 1999, he is responsible for gate quality silicon and high- $k$  gate dielectric for 0.1- $\mu\text{m}$  CMOS at TSMC. He is now with Sematech for process development and reliability of gate quality silicon.

**M.S. Liang**, photograph and biography not available at the time of publication.