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Citation: Applied Physics Letters **77**, 555 (2000); doi: 10.1063/1.127042 View online: http://dx.doi.org/10.1063/1.127042 View Table of Contents: http://scitation.aip.org/content/aip/journal/apl/77/4?ver=pdfcov Published by the AIP Publishing

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## Oxide thinning percolation statistical model for soft breakdown in ultrathin gate oxides

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(Received 3 February 2000; accepted for publication 31 May 2000)

An existing cell-based percolation model with parameter correlation can find its potential applications in assessing soft-breakdown (BD) statistics as long as the oxide thinning due to the localized physical damage near the  $SiO_2/Si$  interface is accounted for. The resulting model is expressed explicitly with the critical trap number per cell  $n_{\rm BD}$  and the remaining oxide thickness  $t'_{\rm ox}$ both as parameters. Reproduction of time-to-bimodal (soft- and hard-) breakdown statistical data from 3.3-nm-thick gate-oxide samples yields  $n_{\rm BD}$  of 3 and 4 for soft and hard breakdown, respectively. The extracted  $t'_{ox}$  of 1.0 nm for soft breakdown, plus the transition layer thickness of 0.5 nm in the model, is fairly comparable with literature values from current-voltage fitting. The dimension and area of the localized physically damaged region or percolation path (cell) are quantified as well. Based on the work, the origins of soft and hard breakdown are clarified in the following: (i) soft breakdown behaves intrinsically as hard breakdown, that is, they share the same defect (neutral trap) generation process and follow Poisson random statistics; (ii) both are independent events corresponding to different  $t'_{0x}$  requirements; and (iii) hard breakdown takes place in a certain path located differently from that for the first soft breakdown. © 2000 American Institute of Physics. [S0003-6951(00)02130-6]

The time-dependent dielectric breakdown is one of the of concern in scaled metal-oxidemost issues semiconductor field-effect transistors (MOSFETs). An anomalous phenomenon of soft breakdown (SBD) (also called *B*-mode stress-induced leakage current<sup>1</sup> and quasibreakdown),<sup>2</sup> which behaves fundamentally differently from hard breakdown (HBD), has recently been observed in ultrathin (<5 nm) gate oxides.<sup>1–7</sup> The oxide thinning theory<sup>2</sup> has originally been reported in explaining this phenomenon: the electrons injected from the polysilicon gate can travel through the ultrathin oxide ballistically and release most of their energy to physically damage the anode SiO<sub>2</sub>/Si interface, thus effectively reducing the oxide thickness to the direct tunneling regime. This theory has been cited in many applications such as post-soft-breakdown current-voltage (I-V) models,<sup>2,8,9</sup> quantitative determination of the remaining oxide thickness by I-V fitting,<sup>2,9</sup> and physics basis for electron and hole direct tunneling conduction.<sup>10</sup> On the other hand, there have been published a series of percolation statistical models each successfully dealing with dielectric breakdown.<sup>11–15</sup> In this letter, we demonstrate that a cellbased percolation model with parameter correlation<sup>15</sup> can find potential applications in assessing soft-breakdown statistics as long as the local oxide thinning<sup>2</sup> is accounted for. In such a way, current understandings regarding the origins of soft and hard breakdown, the dimension and area of the localized physically damaged region, and the remaining oxide thickness, can all be substantially improved.

The *n*-channel MOSFETs with two different gate-oxide areas of 50×50 and 80×100  $\mu$ m<sup>2</sup> were fabricated in a 0.18  $\mu$ m process. In this process, the gate oxide was thermally grown in a dilute oxygen ambient, and the polysilicon gate was arsenic implanted at 50 keV and  $2 \times 10^{15} \text{ cm}^{-2}$ , followed by N<sub>2</sub>O annealing. The gate-oxide thickness  $t_{ox}$  was determined to be 3.3 nm by polysilicon depletion and quantum-mechanical-corrected C-V method. Stressing gate oxide was carried out under gate electron injection with constant current  $J_G$  of -125 mA/cm<sup>2</sup> and with source, drain, and substrate tied to ground. The variation of gate voltage with stress time was monitored, from which the two key time criteria were extracted, namely, the time to soft breakdown  $T_{\text{SBD}}$  and the time to hard breakdown  $T_{\text{HBD}}$ . Figure 1 depicts the measured results by HP4145B for two typical samples, respectively, showing the presence of bimodal (both soft and hard) breakdown and the absence of soft breakdown. At  $T_{\text{SBD}}$  a first, small sudden drop of around 20 mV in gate voltage occurred while a more drastic change down to around -1 V was observed at  $T_{HBD}$ . It was found that about 95% of the total samples exhibit bimodal characteristics, whereas the remaining show only hard breakdown, that is, no noticeable signal for soft breakdown can be detected, as explained later. The inset of Fig. 2 shows the experimental  $T_{\text{SBD}}$  and  $T_{\text{HBD}}$  distributions from all samples in a Weibull plot, revealing that as hard breakdown turns into soft breakdown, the modal of the distributions moves to a lower time scale and the Weibull slope decreases. Also plotted in Fig. 2

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FIG. 1. Measured gate voltage vs stress time under constant current stressing for two samples. The oxide area was  $80 \times 100 \ \mu m^2$  and the stressing was done in gate injection of  $-125 \ mA/cm^2$ .

is the Weibull plot translated to the oxide area  $A_{\rm ox}$  of 80  $\times 100 \,\mu {\rm m}^2$  using Poisson area scaling,<sup>16</sup> showing that both soft and hard breakdown indeed follow Poisson random statistics.<sup>17</sup>

The above time-to-breakdown parameters  $T_{\text{SBD}}$  and  $T_{\text{HBD}}$  were defined in terms of different sudden drops in gate voltage. This definition is adequate since it was found that after  $T_{\text{SBD}}$  the poststress gate I-V characteristic curve resembled the direct tunneling one of the fresh oxide with thinner thickness as reported earlier,<sup>2,3,9,10</sup> while it became Ohmic after  $T_{\text{HBD}}$ . In addition, three relevant observations were drawn from emission spectroscopy EMMI pictures (not shown here): (i) the number of photoemission sites for soft breakdown increases with stress time; (ii) these sites were randomly distributed; and (iii) the first soft-breakdown site was different from the hard-breakdown events prior to the



FIG. 2. Translated (with respect to  $80 \times 100 \ \mu m^2$ ) statistical distributions of time to soft breakdown  $T_{SBD}$  and time to hard breakdown  $T_{HBD}$ . Also plotted are the lines from the model. The inset shows  $T_{SBD}$  and  $T_{HBD}$  distributions for two oxide areas.



FIG. 3. Cross-sectional schematic showing a localized oxide thinning percolation path and a hard-breakdown percolation path. The spheres represent the traps generated during stress. The figure is not to scale.

final hard breakdown. Further evidence was found by plotting a scatter diagram (not shown here) between the measured  $T_{\rm SBD}$  and  $T_{\rm HBD}$  from the above 95% of the samples, exhibiting a very weak correlation between soft breakdown and hard breakdown. This suggests both events are statistically independent of each other.

Dielectric breakdown can be regarded as a process of the neutral electron trap generation during stress, critically producing one percolation path or more. The gate-oxide area can be divided into many identical square cells with cell area  $A_0$ .<sup>11,12</sup> Figure 3 schematically shows two different percolation paths within the cell, respectively, representing soft and hard breakdown. In the hard-breakdown percolation path, the traps span the whole oxide to connect one plate to the other one, while for the soft-breakdown path such plate-to-plate connection is incomplete as characterized by the remaining oxide thickness  $t'_{ox}$  over the localized physically damaged region according to the theory.<sup>2</sup> Obviously, all the traps are randomly generated within the effective thickness of  $t_{\rm ox} - 2t - t'_{\rm ox}$  for soft breakdown. Here, t = 0.5 nm) (Refs. 14 and 15) denotes the transition-layer thickness to account for the conductive nature in the vicinity of the interface between the polysilicon gate and oxide and that between the oxide and underlying silicon. The critical trap density  $N_t$  distribution for the critical trap number per cell  $n_{\rm BD}$  satisfied in a certain soft-breakdown percolation path can be calculated by the existing cell-based percolation model<sup>11,12</sup> with the oxide thinning included:

$$\ln[-\ln(1-F)] = \ln A_{\text{ox}} + \ln \left\{ N_t (t_{\text{ox}} - 2t - t'_{\text{ox}}) - \frac{1}{A_0} \ln \left[ \sum_{n=0}^{n=n_{\text{BD}}-1} \frac{[A_0 (t_{\text{ox}} - 2t - t'_{\text{ox}})N_t]^n}{n!} \right] \right\}, \quad (1)$$

where *F* denotes the cumulative percentage of time to breakdown. Prior to performing Eq. (1), parameter correlation<sup>15</sup> between  $n_{\rm BD}$  and  $A_0$  must be taken into account:

$$A_0 = 0.3(t_{\rm ox} - 2t - t'_{\rm ox})^2 / \sqrt{n_{\rm BD}}.$$
 (2)

Equation (2) can readily quantify the area  $A_0$  of the localized physically damaged region or percolation path (cell). Equations (1) and (2) can also deal with hard breakdown for  $t'_{ox}$ 

=0. A unique relationship<sup>13</sup> between the trap density  $N_t$  and the electron stress fluence  $Q_e$  was employed:  $N_t = B Q_e^{0.6}$ , where *B* is a constant to be fitted. Thus, the above model with  $Q_e = J_G T_{\text{SBD}}$  or  $Q_e = J_G T_{\text{HBD}}$  can practically handle time-to-breakdown data with fitting parameters: *B*,  $t'_{\text{ox}}$ , and  $n_{\text{BD}}$ .

Best-fitting the model to hard-breakdown data points in Fig. 2 yields  $n_{BD} = 4$  and  $B = 7 \times 10^{17} \text{ cm}^{-1.8} \text{C}^{-0.6}$  for two different areas. With known B, next fitting soft-breakdown data leads to  $n_{\rm BD}$ =3 and  $t'_{\rm ox}$ =1.0 nm. Adequate reproduction, as shown in Fig. 2, evidences that the statistical distributions of soft breakdown behave intrinsically as hard breakdown.<sup>13,16</sup> The extracted  $t'_{ox}$  is reasonable since its quantity plus the transition-layer thickness t of 0.5 nm is fairly comparable with literature values obtained by I-V fitting: 2.2 nm for the 4 nm gate oxide<sup>2</sup> and a constant of about 1.7 nm over 3.5-5.0 nm gate-oxide thicknesses.<sup>9</sup> On the other hand, the area of the localized physically damaged region was estimated based on Eq. (2) to be 0.3 nm<sup>2</sup>, far away from the reported values: i.e.,  $1.55 \times 1.55$  nm<sup>2</sup> in Ref. 8 and  $1 \times 10^3 - 6 \times 10^3$  nm<sup>2</sup> in Ref. 9. One of the principal reasons for such order-of-the-magnitude differences is that the direct tunneling depends on the oxide thickness exponentially but on the area only linearly, that is, an adjustment of  $t'_{ox}$  gives rise to an exponential change in tunneling area such as to maintain the same direct tunneling current.

According to the above analysis, the origins of soft and hard breakdown can be clarified in the following: (i) soft breakdown behaves intrinsically as hard breakdown, that is, they share the same defect (neutral trap) generation process and follow Poisson random statistics; (ii) both are independent events corresponding to different  $t'_{ox}$  requirements; and (iii) hard breakdown takes place in a certain path located differently from that for the first soft breakdown. Therefore, hard breakdown can statistically compete with soft breakdown, that is, hard breakdown could dominate making soft breakdown not easily detected. This effect, which can reasonably explain the above-mentioned 5% population for the absence of soft breakdown during stress measurement, has been well documented elsewhere.<sup>18</sup> However, we disagree with the argument of the same citation<sup>18</sup> that soft-breakdown and hard-breakdown phenomena do not obey to the same defect generation kinetics. Instead, more and more evidences<sup>19,20</sup> tends to favor our opinion. Finally, the distinct  $t'_{\rm ox}$  for soft and hard breakdown indicates that the conductance in the former percolation path is lower than the latter, consistent with the recent theory prediction<sup>21</sup> that the upper and lower limits of the conductance stochastic values in the percolation path correspond to the occurrences of hard breakdown and soft breakdown, respectively.

In summary, an oxide thinning cell-based percolation model with parameter correlation has been established to assess soft-breakdown statistics in ultrathin gate oxides. It has exhibited the ability of reproducing the statistical distributions of time to soft breakdown from 3.3-nm-thick oxide samples. Current understandings regarding the origins of soft and hard breakdown, the dimension and area of the localized physically damaged region, and the remaining oxide thickness, have all been substantially improved based on the model.

The authors would like to thank the Advanced Reliability Engineering Department/UMC for the EMMI analysis. Special thanks are devoted to M. J. Chang for performing C-V fitting, and Dr. H. T. Huang and K. N. Yang, Reliability Physics Laboratory, for their stimulating discussions. This work was supported by the National Science Council under Contract No. 89-2215-009-049.

- <sup>1</sup>K. Okada, S. Kawasaki, and Y. Hirofuji, Extended Abstracts of the 1994 International Conference on Solid State Devices and Materials (SSDM) (1994), p. 565 (unpublished).
- <sup>2</sup>S. H. Lee, B. J. Cho, J. C. Kim, and S. H. Choi, Tech. Dig. Int. Electron Devices Meet. 605 (1994).
- <sup>3</sup>T. Yoshida, S. Miyazaki, and M. Hirose, Extended Abstracts of the 1996 International Conference on Solid State Devices and Materials (SSDM) (1996), p. 539 (unpublished).
- <sup>4</sup>M. Depas, T. Nigam, and M. M. Heyns, IEEE Trans. Electron Devices **43**, 1499 (1996).
- <sup>5</sup>B. E. Weir, P. J. Silverman, D. Monroe, K. S. Krisch, M. A. Alam, G. B. Alers, T. W. Sorsch, G. L. Timp, F. Baumann, C. T. Liu, Y. Ma, and D. Hwang, Tech. Dig. Int. Electron Devices Meet. 73 (1997).
- <sup>6</sup>E. Miranda, J. Suñé, R. Rodriguez, M. Nafria, and X. Aymerich, Appl. Phys. Lett. **73**, 490 (1998).
- <sup>7</sup>T. Sakura, H. Utsunomiya, Y. Kamakura, and K. Taniguchi, Tech. Dig. Int. Electron Devices Meet. 183 (1998).
- <sup>8</sup>D. Z. Y. Ting, Appl. Phys. Lett. 74, 585 (1999).
- <sup>9</sup>W. Mizubayashi, H. Itokawa, S. Miyazaki, and M. Hirose, Extended Abstracts of the 1999 International Conference on Solid State Devices and Materials (SSDM) (1999), p. 318 (unpublished).
- <sup>10</sup> Y. D. He, H. Guan, M. F. Li, B. J. Cho, and Z. Dong, Appl. Phys. Lett. 75, 2432 (1999).
- <sup>11</sup>J. Suñé, I. Placencia, N. Barniol, E. Farres, F. Martin, and X. Aymerich, Thin Solid Films 185, 347 (1990).
- <sup>12</sup>D. J. Dumin, J. R. Maddux, R. S. Scott, and R. Subramoniam, IEEE Trans. Electron Devices **41**, 1570 (1994).
- <sup>13</sup> R. Degraeve, G. Groeseneken, R. Bellens, M. Depas, and H. E. Maes, Tech. Dig. Int. Electron Devices Meet. 863 (1995).
- <sup>14</sup> H. T. Huang, M. J. Chen, J. H. Chen, C. W. Su, C. S. Hou, and M. S. Liang, IEEE International Symposium on VLSI Technology, Systems and Applications, Proceedings of Technical Papers (1999), p. 70 (unpublished).
- <sup>15</sup> M. J. Chen, H. T. Huang, J. H. Chen, C. W. Su, C. S. Hou, and M. S. Liang, IEEE Electron Device Lett. **20**, 523 (1999).
- <sup>16</sup>E. Y. Wu, E. Nowak, L. K. Han, D. Dufresne, and W. W. Abadeer, Tech. Dig. Int. Electron Devices Meet. 441 (1999).
- <sup>17</sup>D. R. Wolters and J. F. Verwey, *Instabilities in Silicon Devices* (Elsevier, Amsterdam, The Netherlands, 1986), p. 332.
- <sup>18</sup>S. Bruyere, D. Roy, E. Vincent, and G. Ghibaudo, Microelectron. Reliab. 39, 815 (1999).
- <sup>19</sup>E. Wu, E. Nowak, J. Aitken, W. Abadeer, L. K. Han, and S. Lo, Tech. Dig. Int. Electron Devices Meet. 187 (1998).
- <sup>20</sup>J. Suñé, G. Mura, and E. Miranda, IEEE Electron Device Lett. **21**, 167 (2000).
- <sup>21</sup> M. A. Alam, B. Weir, J. Bude, P. Silverman, and Don Monroe, Tech. Dig. Int. Electron Devices Meet. 449 (1999).