

# 國立交通大學

電子工程學系 電子研究所碩士班

## 碩士論文

鎳/氧化鈣/氮化鈦結構的電阻式記憶體  
及其熱處理效應

The logo of National Tsing Hua University is a circular emblem with a gear-like border. Inside the circle, there is a stylized figure holding a torch, and the year '1896' is inscribed at the bottom. The text 'National Tsing Hua University' is written around the inner edge of the circle.

Effect of Thermal Treatment on Resistive Random Access  
Memory (RRAM) of Ni / HfO<sub>x</sub> / TiN Structure

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中華民國九十八年八月

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在本論文中，我們使用鎳、非化學當量的氧化鈣和氮化鈦的金屬-氧化物-金屬結構來展示電阻式記憶體的特性。可以觀察到重覆性的電阻值轉換而且平均的電阻比例大於 100。經過氫氣退火的氧化鈣薄膜展現出大電流操作的特性，藉由 X 光繞射分析觀測到氧化鈣薄膜已經結晶，這可能是造成大電流操作的原因。為了降低功率消耗，我們對氧化鈣進行氧退火並且使用細絲理論來解釋量測到的數據。量測結果顯示：比起沒有氧退火的試片而言，氧退火過的試片其電流和功率消耗都大約小 10 倍。經過氧退火製程後，操作電壓並沒有很顯著的增加，而且耐用度也稍微改善。除此之外，資料保留特性和非破壞性讀取也在本論文中被測試。我們的研究顯示：退火過的鎳/氧化鈣/氮化鈦的電阻式記憶體是很有希望被應用在低功率消耗的非揮發性記憶體。

# Effect of Thermal Treatment on Resistive Random Access Memory (RRAM) of Ni / HfO<sub>x</sub> / TiN Structure

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## **ABSTRACT**

In this thesis, we used metal-oxide-metal (MOM) structure of nickel (Ni), non-stoichiometric hafnium oxide (HfO<sub>x</sub>), and titanium nitride (TiN) to demonstrate RRAM characteristics. Voltage-induced resistance switching is repeatedly observed in the Ni/HfO<sub>x</sub>/TiN device with average resistance ratio greater than 100. The HfO<sub>x</sub> film which is annealed in Ar ambient exhibits large current operation. It is probably due to crystallization of HfO<sub>x</sub> film by X-Ray Diffraction (XRD) result. In order to reduce power dissipation, HfO<sub>x</sub> is oxygen annealed and we use filamentary model to explain the measured data. Measurement result shows that all operation current and power are smaller about 10 times than the sample which is not annealed in O<sub>2</sub> ambient. Operation voltage is not elevated noticeably and endurance is slightly improved after oxygen annealing process. Besides, data retention and non-destructive readout are tested in this thesis. Our study shows that the annealed Ni/HfO<sub>x</sub>/TiN RRAM is a promising candidate for low power nonvolatile memory applications.

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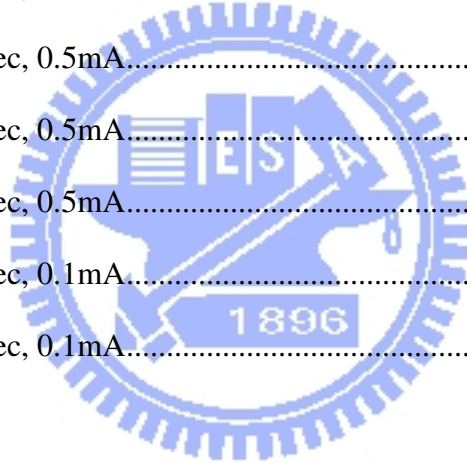
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陳信宇于新竹交大 2009年8月

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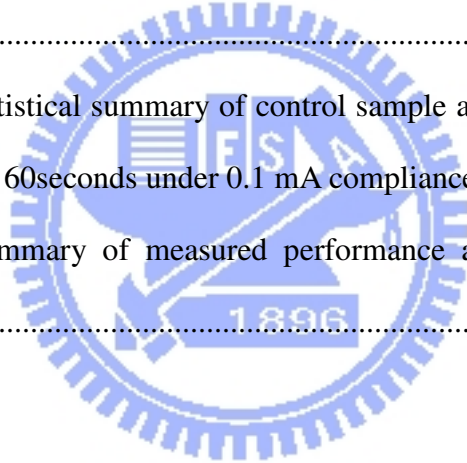
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# CHAPTER 1

## INTRODUCTION

### 1-1 Overview of Nonvolatile Memory

During the last several years, there has been a rapid growth of nonvolatile memory (NVM) due to the demand for applications to personal computer (PC) and portable device. NVM is computer memory that can retain the stored information even when powered off. NVM of common usage includes not only mechanical type, such as hard disk (HD) and digital video disk (DVD), but also electrical type, such as read-only memory (ROM) and flash memory. The advantages of small size, anti-seismic, and low power consumption make flash memory more superior in applying mobile equipment than mechanical type. Moreover, it is believed that flash memory will have higher operation speed than mechanical type in the long run.

However, flash memory is facing the downscaling limitation problem. The main difficulty is : tunnel oxide is such thin that charges stored in the floating gate will leak into the substrate through the thinner tunnel oxide. Inter-poly dielectric thickness, isolation spacing, cell-cell parasitic coupling, and number of stored charge are also issues of downscaling [1]. Therefore, several types of nonvolatile memories like ferroelectric random access memory (FeRAM), magnetic random access memory (MRAM), phase change memory (PCM), and resistive random access memory (RRAM) are under investigation. Among these nonvolatile memories, RRAM is a good candidate for a few factors: compared to FeRAM, it has non-destructive readout;

its operation voltage is lower than flash memory; its program current is lower than PCM and MRAM [2]; its operation speed is higher than PCM; compared to MRAM, its structure (Metal-Oxide-Metal) is so simple that it can be applied in 3D integration easily. Comparison of NVMs is listed in Table 1-1 [1].

Table 1-1 Comparison of Non-volatile Memory [1]

	Flash	PCM	FeRAM	MRAM	RRAM
Integration	Good	Good	Poor	Poor	Good
Multi Level Cell	Yes	Yes	No	No	Yes
Cell Size	$4F^2$	$6F^2$	$20F^2$	$30F^2$	$4F^2$
Current	$\sim \mu\text{A}$	$\sim \text{mA}$	$\sim \mu\text{A}$	$\sim \text{mA}$	$< 0.1\text{mA}$
Write/Erase Voltage (V)	12	3	0.9 ~ 3.3	1.5	$< 3$
Write/Erase Time	$\sim \text{ms}$	$\sim 100\text{ns}$	$\sim 40\text{ns}$	$< 10\text{ns}$	$< 10\text{ns}$
Read Voltage (V)	4 ~ 5	3	0.9 ~ 3.3	1.5	0.7
Read Time	70~90ns	60ns	45ns	20ns	$< 50\text{ns}$
Write/Erase Cycles	$> 10^5$	$10^8$	$10^{14}$	$> 3 \times 10^{16}$	$> 10^5$
Retention Time	$> 10 \text{ years}$	$> 10 \text{ years}$	$> 10 \text{ years}$	$> 10 \text{ years}$	$> 10 \text{ years}$

## 1-2 Nano CMOS and High- $\kappa$ Material

Recently, the technology, which is often applied to very-large-scale integrated circuit (VLSI), is complementary metal-oxide-semiconductor (CMOS) process. In order to achieve specification of Moore's law, the trend of CMOS process was

downscaling of channel length and thickness of gate oxide in the past. But the downscaling led to short channel effect and increased of leakage current through gate oxide [3]. Hence, strained silicon technique is brought up to enhance carrier mobility. Metal gate is proposed to improve depletion of poly-silicon gate and enhance capability of gate control over the channel. High- $\kappa$  material is used to relax the requirement of gate leakage current while maintaining the same capacitance. Among many proposed oxide, hafnium oxide film has great potential as high- $\kappa$  gate oxide due to its high dielectric constant, large energy bandgap, and compatibility with conventional CMOS process [3-6]. Furthermore, it is reported that thermal treatment, such as oxygen annealing [7, 8], enable hafnium oxide to have better performance of leakage current.



## 1-3 Resistive Random Access Memory

Silicon oxide has been shown to exhibit resistive switching as early as 1967 [9], and has recently been revisited [10, 11]. In addition, perovskite oxides and transition metal oxides (TMOs) were also found to have properties of resistive switching. But TMOs have more endurance cycles than the other one [12] and they are more easily applicable to normal semiconductor manufacturing processes than perovskite oxide, which consists of more than 3 components [13]. Nickel oxide [14], copper oxide [15], tantalum oxide [16], and hafnium oxide [17] have been revealed their excellent properties of resistive switching. For the most part, hafnium oxide is the best candidate of RRAM because hafnium oxide is a well-known oxide, has more reliability studies, and is the best choice of high  $\kappa$  gate dielectric in CMOS technology as mentioned above.

# 1-4 Typical Switching Operation and

## Model of RRAM

The structure of RRAM cell is metal-oxide-metal (MOM) as shown in the inset of Fig. 1-1. The relationship between applied voltage and current detection is also depicted in the inset of Fig. 1-1. Basic characterization of RRAM consists of the following methods: direct current (DC) voltage bias [18], DC current bias [19], and alternating current (AC) impedance measurement [20, 21]. Most popular is the voltage bias method that is current detection under several given bias voltage. It can set (turn on, or write) the RRAM cell from off state (i.e. low current level) to on state (i.e. high current level) and reset (turn off, or erase) from on state to off state under appropriate voltage bias. In accordance with voltage polarity, this method can be subdivided into four types schematized in Fig. 1-1. If the RRAM cell has four type operations, we call it “non-polarity” [18, 22]. If the RRAM cell only has single polarity operation, we call it “uni-polarity” [23]. If the RRAM cell belong to neither non-polarity nor uni-polarity, we call it “bi-polarity” [24]. In general, uni-polar operation is seen to be relevant to filamentary conductive path and anode interface [23]. Bi-polar operation is associated with the redox reaction and trap/detrapping at the anode interface [25] or space charge limited conduction (SCLC) model [15, 17]. For commercial application, uni-polar operation is superior to bi-polar operation due to the demand for single polar supply voltage merely or no need for voltage converters. The number of write and erase is called endurance.

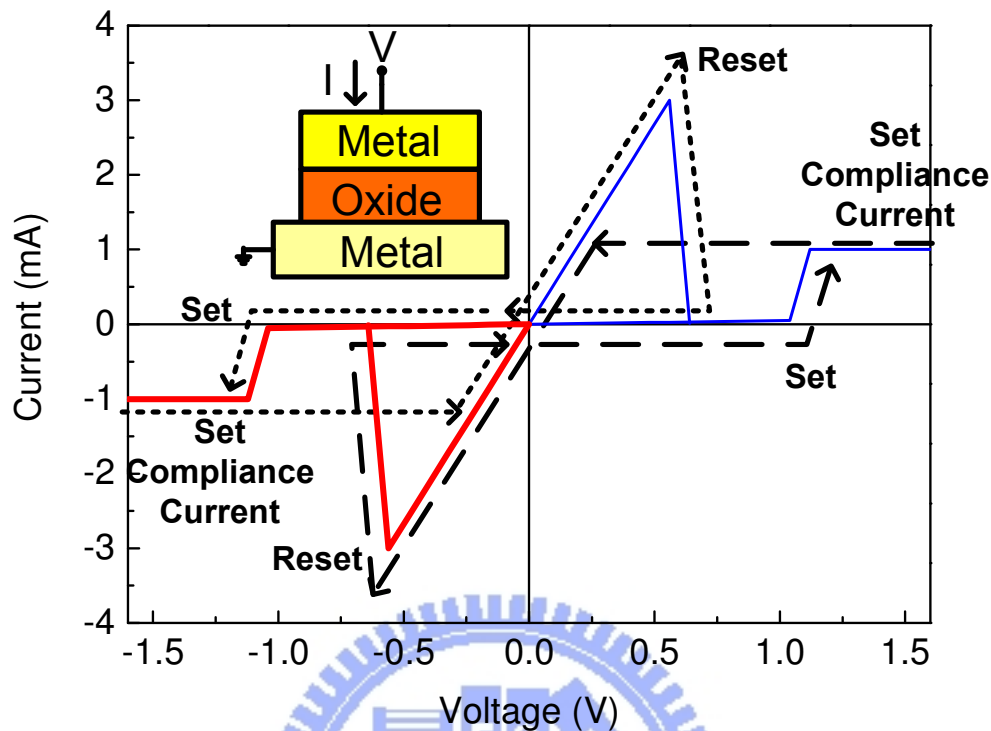


Fig. 1-1 Typical switching operation of RRAM

The inset depicts not only MOM structure of RRAM but the relationship between applied voltage and current detection. The thin blue line shows set under positive bias and reset under positive bias. The thick red line shows set under negative bias and reset under negative bias. The dash line shows set under positive bias and reset under negative bias. The dot line shows set under negative bias and reset under positive bias. Note y-axis is linear scale.

## 1-5 Filamentary Model and Anode

### Interface

Although exact resistive switching mechanism is a controversial issue, filamentary model is often used and accepted extensively. Hence, filamentary model by using DC voltage sweeping is introduced in this section. During voltage sweeping,

the voltage is applied to the top electrode and the bottom electrode is grounded. Here, uni-polar switching which is set under positive voltage and reset under positive voltage is used.

A transition metal oxide (TMO) is normally insulating without any voltage sweeping. When positive voltage is applied to top electrode, the oxygen ion ( $O^{2-}$ ) moves toward anode through oxygen vacancies as shown in Fig. 1-2 [26]. Oxygen leaving remains many oxygen vacancies which aligned together like “tree” or “filament” as depicted in Fig. 1-2 [26]. When voltage is raised to  $V_{\text{forming}}$  as shown in Fig. 1-3, current will suddenly increase (soft breakdown) to a certain limiting value (prevent completely breakdown [23]). That top electrode and bottom electrode are connected together by conductive filament forms on state as show in Fig. 1-4. This is called “forming process” which is initialized RRAM cell. Electron in this film can transport through these filament (or oxygen vacancy). Generally speaking,  $V_{\text{forming}}$  is usual positively related to oxide thickness [14, 17] and filament near anode is weaker than cathode [27]. Conductive Atomic Force Microscope (CAFM) results suggested that probably many current paths [27, 28], rather than a single filament, are involved as shown in Fig. 1-5.

Once the filament is formed, excute voltage sweep without any compliance current again. As voltage is raised to  $V_{\text{reset}}$  as shown in Fig. 1-3, current will reach maximal current and accumulate Joule heat. This heat results in rupture of weaker filament near anode interface [29] as showed in Fig. 1-6 and current suddenly decreases (reset) to low current level (off state). This is called “reset process” and explained why characteristic of RRAM is highly related to top electrode [30, 31]. One study indicated that the rupture thickness is about 3-10 nm [32].



The off state cell can return to on state by “set process”. Set process is similar to forming process as shown in Fig. 1-3.  $V_{set}$  is usually smaller than  $V_{forming}$  because set process only needs to construct shorter filament than forming process. Set and reset process can make the cell of state reversible.

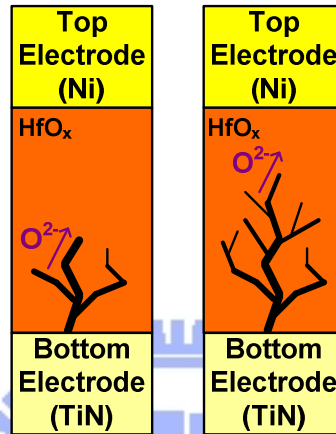


Fig. 1-2 Process of filament formation and oxygen ion movement

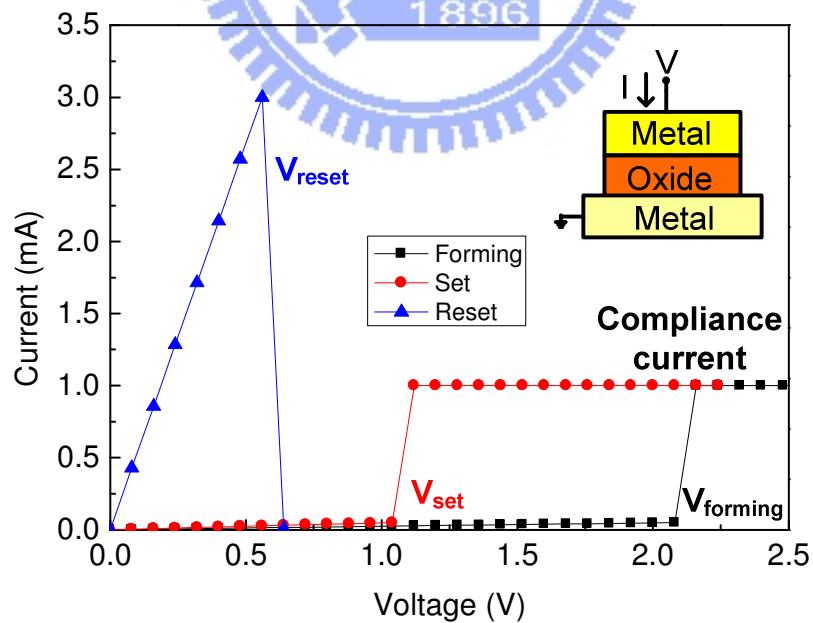


Fig. 1-3 I-V curve of uni-polar operation



Fig. 1-4 Conductive filament of on state

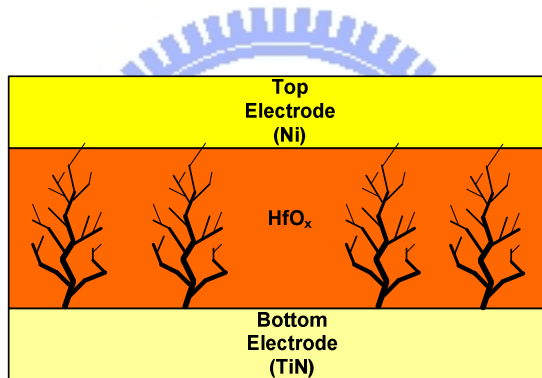


Fig. 1-5 Multi-filament of on state

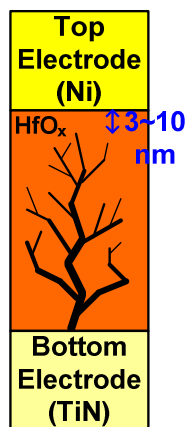


Fig. 1-6 Filament rupture near anode interface and conductive filament of off state

# CHAPTER 2

## EXPERIMENTAL PROCEDURE & MEASUREMENT SETUP

### 2-1 Motivation

Hafnium oxide based RRAM in previous studies seldom used in uni-polar operation. One study [23] uses platinum (Pt) as top electrode,  $\text{HfO}_x$  as oxide, TiN as bottom electrode to form MOM structure of RRAM. Pt hardly causes reaction with oxygen, but this inert metal is more expensive than others. We used nickel (Ni) as top electrode because of its high free energy [31] and low cost compared to inert metal.

Thermal treatment is used to improve leakage current of high  $\kappa$  material as mentioned in section 1-2. Several studies applied this technique on RRAM to enhance ratio [33] (i.e. on state current divided by off state current), decrease forming voltage [34] (i.e. first set voltage from fresh sample to on state), improve endurance [35]. Also, most of those studies focused on bi-polar operation. Hence, we used non-stoichiometric  $\text{HfO}_x$  films to demonstrate effect of thermal treatment and focus on uni-polar operation, behavior of on state and off state.

### 2-2 Experimental Procedures

The fabrication of RRAM cell is described as follows. A TiN film as the bottom electrode was deposited on Ti/SiO<sub>2</sub>/Si by sputtering. Then, non-stoichiometric  $\text{HfO}_x$

films with a thickness of 20 nm were deposited by Industrial Technology Research Institute (ITRI) using hafnium tetrachloride ( $\text{HfCl}_4$ ) and water ( $\text{H}_2\text{O}$ ) as reactants at temperature of  $300^\circ\text{C}$  in standard atomic layer deposition (ALD) system. After  $\text{HfO}_x$  deposition, some samples were thermally annealed in a rapid thermal annealing (RTA) system at  $400^\circ\text{C}$  or  $500^\circ\text{C}$  for 30, 60, 90, and 120 seconds. Argon (Ar) and Oxygen ( $\text{O}_2$ ) were used as the annealing gas for the annealing systems. The samples without thermal annealing treatment are called control samples. Then, nickel (Ni) as a top electrode was deposited by E-beam evaporation using a shadow mask at room temperature. The thickness of Ni and diameter of top electrode was 50 nm and  $50\ \mu\text{m}$ , respectively. The fabricated RRAM cell is shown in Fig. 2-1. The depth profile of the chemical composition of  $\text{HfO}_x$  films (control sample) measured by X-ray Photon–electron Spectroscopy (XPS) is shown in Fig. 2-2. The atomic ratio of oxygen to hafnium (O:Hf) inside the  $\text{HfO}_x$  film is found to be about 1.5 ( $x < 2$ ), which suggests that the  $\text{HfO}_x$  film is non-stoichiometric. The surface morphology of  $\text{HfO}_x$  samples was examined by atomic force microscopy (AFM) in tapping mode as shown in Fig. 2-3. The rms surface roughness over a scanning area of  $3 \times 3\ \mu\text{m}^2$  is around 4.9 nm.

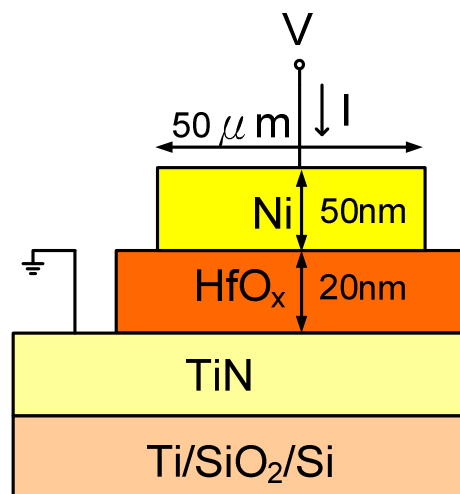


Fig. 2-1 The fabricated RRAM cell, voltage polarity and current direction of measurement

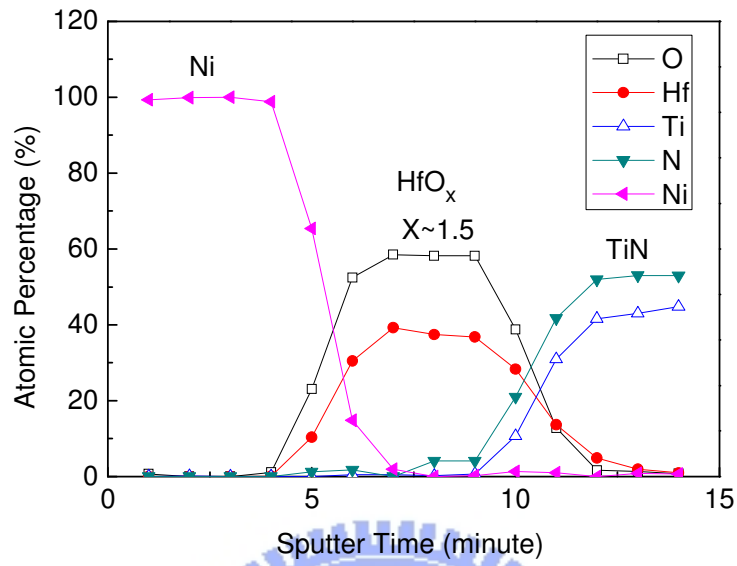


Fig. 2-2 XPS compositional depth profile of Ni/HfO<sub>x</sub>/TiN structure (control sample)

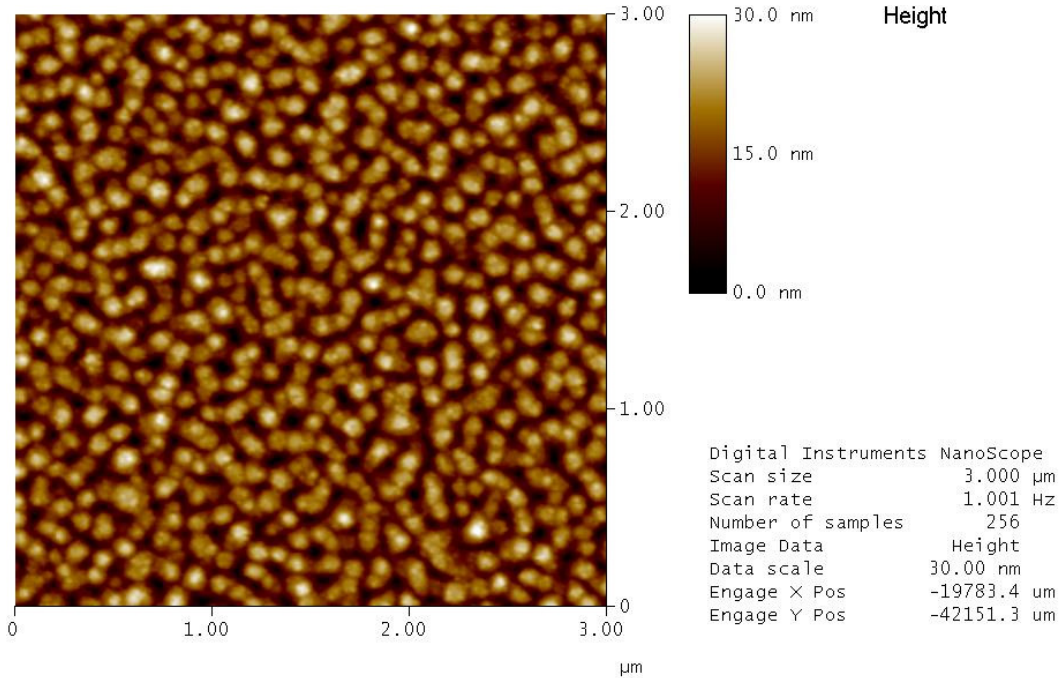


Fig. 2-3 AFM image of HfO<sub>x</sub>

## 2-3 Measurement Setup

The electrical properties of the RRAM devices were measured using a Agilent 4156C semiconductor parameter analyzer. During measurement, the voltage was applied to the Ni electrode and the TiN electrode was grounded. In this thesis, we adopted DC voltage bias method. Our voltage step was 0.08 V as voltage sweeping and compliance currents were 0.1, 0.5, and 1mA. In order to achieve the purpose of low power consumption, the lower compliance current was preferable. But not all samples of different process conditions had operation of low compliance current or sufficient endurance under small compliance current.

The uni-polar operation of RRAM is described as follows and shown in Fig. 2-4. Suppose that RRAM cell is at off state. Firstly, execute voltage sweeping (A→B) and detect current. As voltage is raised to  $V_{set}$ , current will suddenly increase (soft breakdown, or set) to a certain limiting value (0.1~10mA). Secondly, execute voltage sweeping (C→D) and detect current without any compliance current. In general, the second current is usually larger than the first at small voltage bias and maximal reset current is usually larger than compliance current. As voltage is raised to  $V_{reset}$ , current will suddenly decrease (reset) to low current level (off state) and stop sweeping immediately. These step (A→B and C→D) are called one cycle.  $V_{reset}$  is always smaller than  $V_{set}$ . This is how resistance switching is observed. If the suddenly increased current is too high (>30mA), this cell is hard breakdown (never return to off state or burn out) and becomes useless. If the compliance current is too small, this cell is not switched state or small on/off ratio. Therefore, setting a proper current limit (compliance current) is crucial.

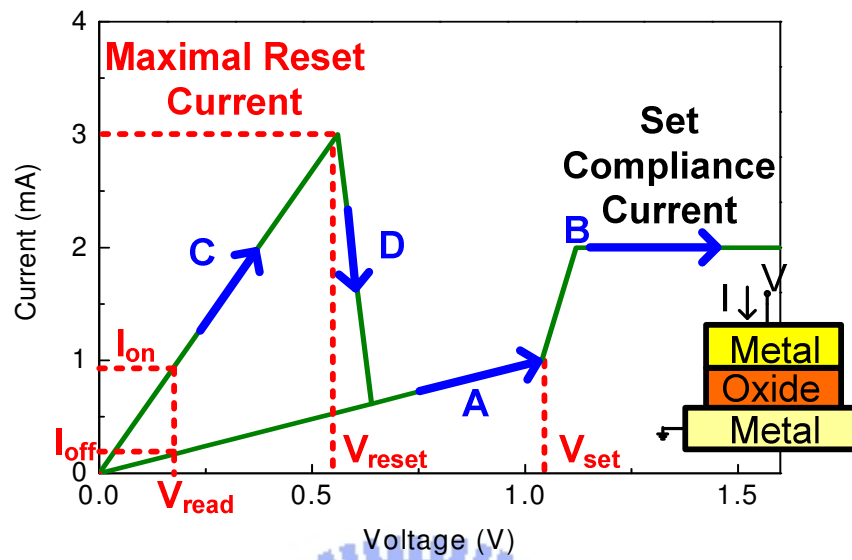
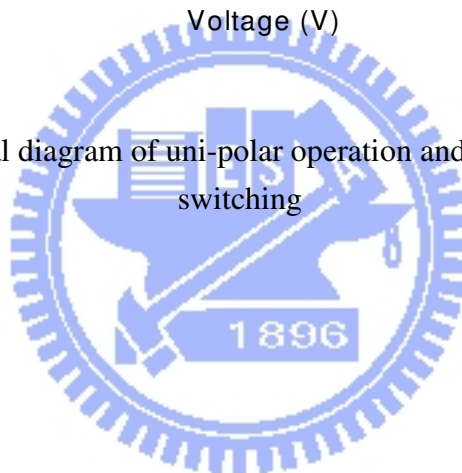


Fig. 2-4 Conceptual diagram of uni-polar operation and definition of resistive switching



# CHAPTER 3

## RESULTS & DISCUSSION

### 3-1 Definition of switching parameter

The switching parameter is defined in this section. As show in Fig. 2-4, the currents of on state and off state at small voltage  $V_{read}$  are defined as  $I_{on}$  and  $I_{off}$ , respectively. When  $V_{read}$  is limited to far less than  $V_{reset}$ , on state and off state are not disturbed. Then, conductance of these two states at  $V_{read}$  are defined in Eq. (3-1) and Eq. (3-2), respectively.

$$G_{on} = R_{on}^{-1} \equiv I_{on} / V_{read} \quad (3-1)$$

$$G_{off} = R_{off}^{-1} \equiv I_{off} / V_{read} \quad (3-2)$$

Conductance ratio of two states is defined as Eq. (3-3).

$$Ratio \equiv G_{on} / G_{off} \quad (3-3)$$

Assume two states are equally probable, and then expectation value of readout power  $P_{read}$  can be expressed as Eq. (3-4).

$$P_{read} = \frac{1}{2} V_{read} I_{on} + \frac{1}{2} V_{read} I_{off} = \frac{1}{2} V_{read}^2 G_{on} + \frac{1}{2} V_{read}^2 G_{off} \quad (3-4)$$

Assume  $G_{on} \gg G_{off}$ , then the term of off state can be neglected. Eq. (3-4) can be simplified as Eq. (3-5).

$$P_{read} \equiv \frac{1}{2} V_{read} I_{on} = \frac{1}{2} V_{read}^2 G_{on} \quad (3-5)$$



To make  $G_{on}$  overly large gain large Ratio ( $= G_{on}/G_{off}$ ) causes high readout power as indicated in Eq. (3-5). For commercial NVM, the number of times of readout is  $10^{12}$ . Therefore, small readout power is significant. In order to achieve the purpose of small readout power,  $G_{on}$  must be decreased as indicated in Eq. (3-5). Nevertheless, small  $G_{on}$  leads to small conductance ratio as indicated in Eq. (3-3). Hence,  $G_{off}$  must also be reduced for large enough ratio.  $G_{on}$  is only need to be large enough to distinguish  $G_{on}$  from  $G_{off}$ .

Besides, set power [36] and reset power (power of write and erase) are defined in Eq. (3-6) and Eq. (3-7), respectively. The set power and reset power are also sketched as Fig. 3-1.

$$P_{set} = \int_0^{V_{set} + \Delta V} I_{set} dV \quad (3-6)$$

$$P_{reset} = \int_0^{V_{reset} + \Delta V} I_{reset} dV \quad (3-7)$$

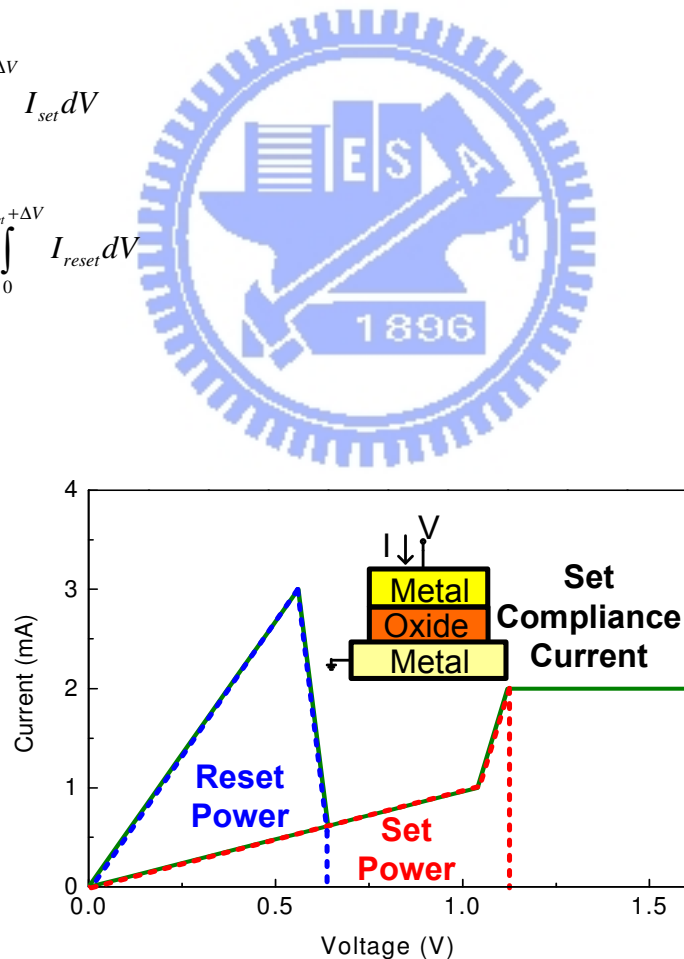


Fig. 3-1 Definition of set and reset power

## 3-2 Example of Switching Parameter

### Extraction

In this section, extraction and presentation of measurement result are stated clearly by using the measurement result of control sample. From 1<sup>st</sup> and 2<sup>nd</sup> I-V curve of Fig. 3-2, the cell in off state was not switching (only 1.74 ratio) under too small compliance current (0.1mA). In this case, we must boost compliance current to 0.5mA to have large on/off ratio (178) as shown in 3<sup>rd</sup> and 4<sup>th</sup> I-V curve of Fig. 3-2. This phenomenon was mentioned in section 2-3. So we concluded that 0.1mA compliance current was useless for control sample. A possible reason is that for many filaments in  $\text{HfO}_x$ , each filament acquires less energy under 0.1mA compliance current.

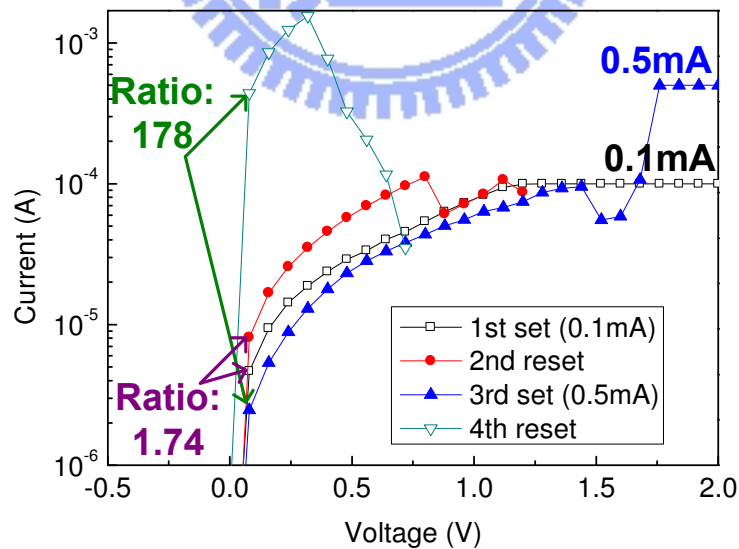


Fig. 3-2 I-V curves of control sample under different compliance currents

Fig. 3-3 shows fifty-two I-V switching curves. Operation voltage, selected I-V curve, and Maximal reset current (defined in section 3-1) from Fig. 3-3 are depicted in Fig. 3-4, Fig. 3-5, and Fig. 3-6, respectively.

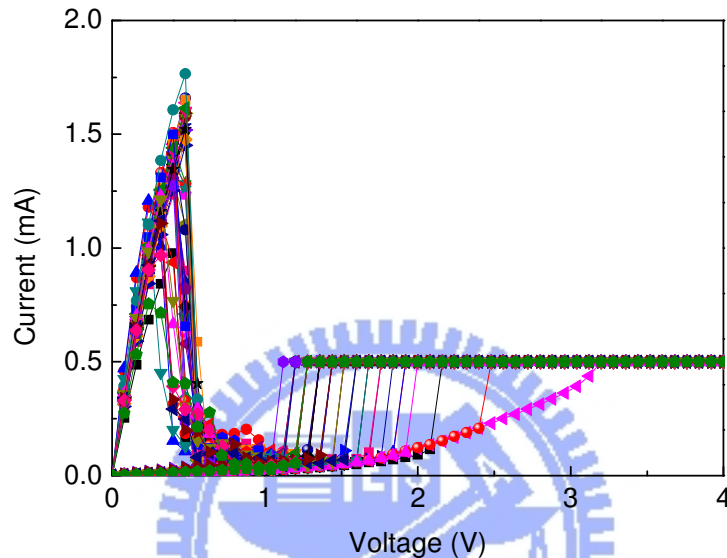


Fig. 3-3 Fifty-two I-V switching curves of control sample under 0.5 mA compliance current

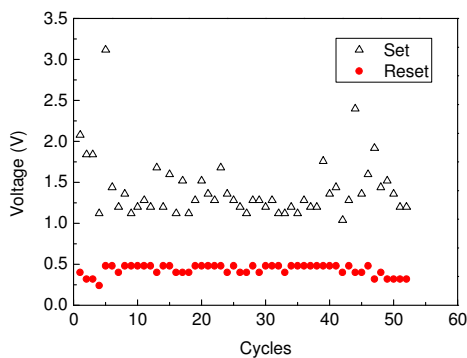


Fig. 3-4 (a)

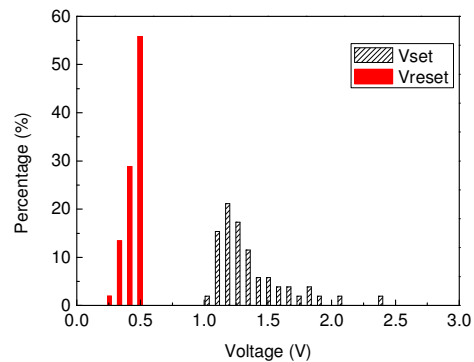


Fig. 3-4 (b)

Fig. 3-4 (a)  $V_{set}$  and  $V_{reset}$  from Fig. 3-3 (b) histogram of  $V_{set}$  and  $V_{reset}$  from Fig. 3-3

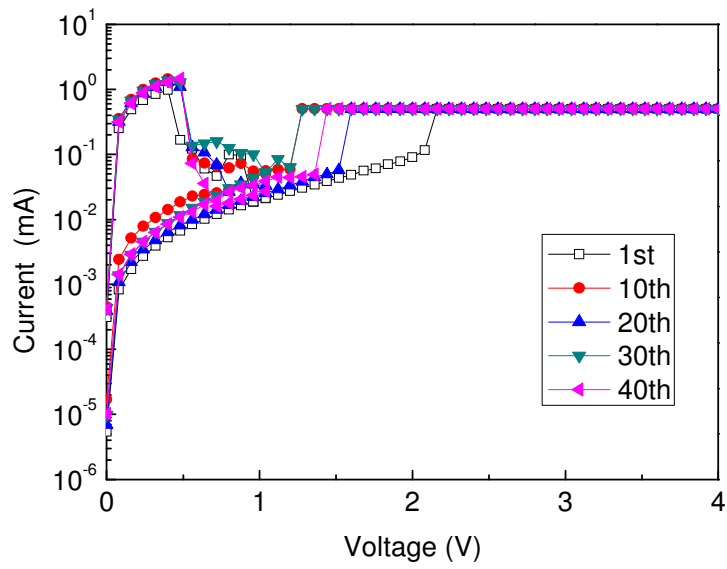


Fig. 3-5 Selected I-V curves from Fig. 3-3

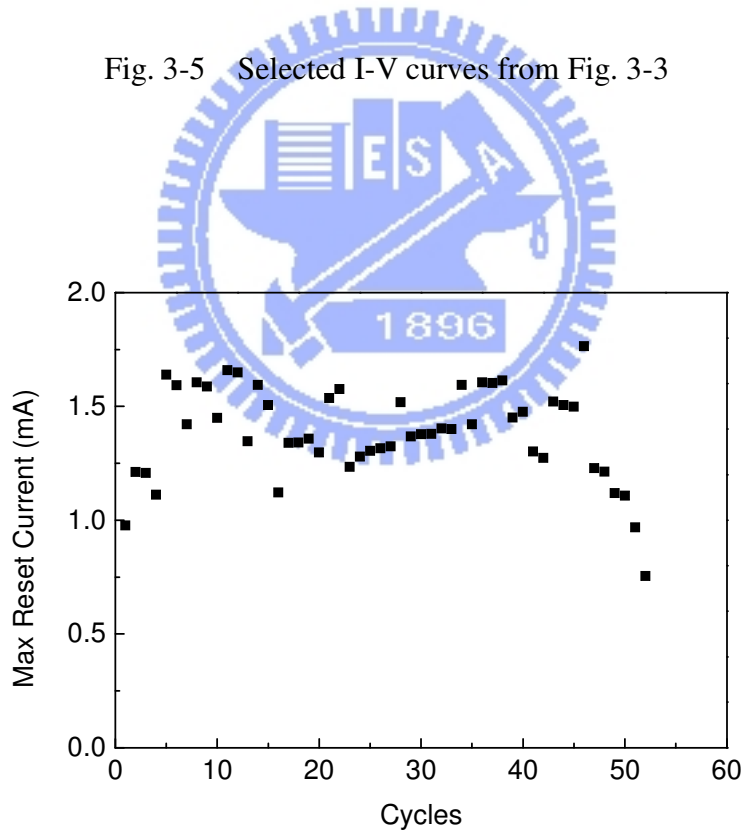


Fig. 3-6 Maximal reset current of Fig. 3-3

Fig. 3-7 left y-axis shows average current of turn on and turn off process. Fig. 3-7 right x-axis shows ratio of  $I_{\text{turn on}} / I_{\text{turn off}}$ . This is the advantage of RRAM due to large ratio at small readout voltage. So we chose 0.08 V as  $V_{\text{read}}$  (sketched in Fig. 3-7) to calculate conductance of on state and off state ( $G_{\text{on}}$  and  $G_{\text{off}}$ ).  $V_{\text{read}}$  is far less than  $V_{\text{reset}}$  to prevent disturbing on state and off state as mentioned in section 3-1. Fig. 3-8 shows calculated  $G_{\text{on}}$ ,  $G_{\text{off}}$ , and Ratio and large window region to prevent confusing on state and off state. This cell was measured only fifty-two cycles because 53th cycle under 0.5mA compliance was not switching, which is similar to Fig. 3-2. Detail measurement results of each process condition are provided in Appendix of this thesis. Because measurement data is numerous, we use statistical quantities to infer measurement result. Average ( $\mu$ ), standard deviation ( $\sigma$ ), and coefficient of variation (CV) are defined in Eq. (3-8), Eq. (3-9), and Eq. (3-10), respectively.

$$\mu = \frac{\sum_{i=1}^n x_i}{n} \quad (3-8)$$

$$\sigma = \sqrt{\frac{\sum_{i=1}^n (x_i - \mu)^2}{n-1}} \quad (3-9)$$

$$CV = \frac{\sigma}{\mu} \quad (3-10)$$

where  $x_i$  is measurement data and  $n$  is number of measurement data.

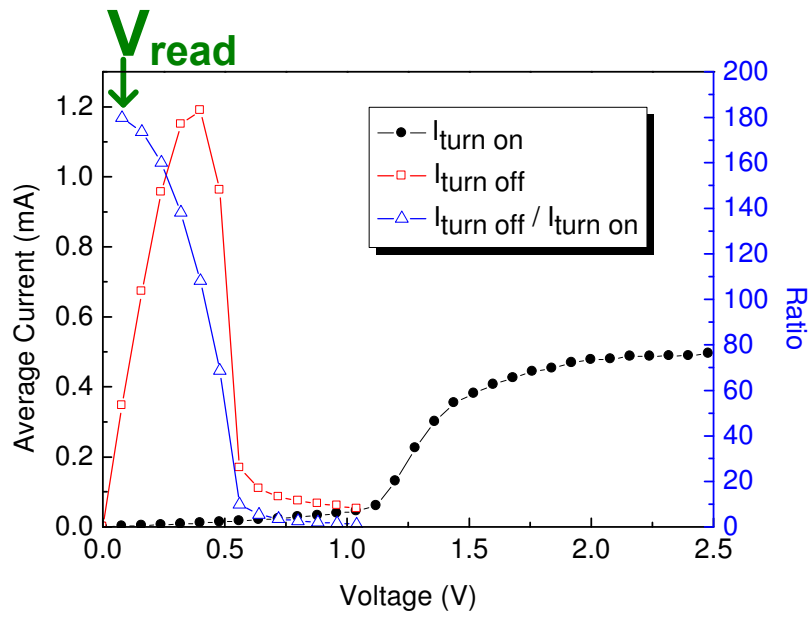


Fig. 3-7 Average currents of Fig. 3-3 and their ratio

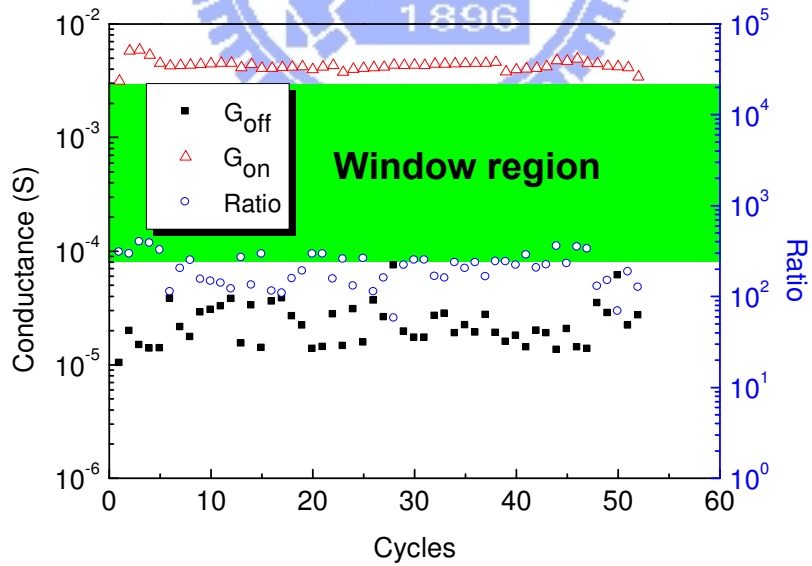


Fig. 3-8  $G_{\text{on}}$ ,  $G_{\text{off}}$ , Ratio of Fig. 3-3

## 3-3 Comparison between Control Sample and RTA in Ar Ambient

Forming process I-V curves of control sample and RTA at 500°C in Ar ambient 30, 60, and 90 seconds are shown in Fig. 3-9. The time of RTA in Ar ambient at 500°C is positively related to leakage current in forming process. Fig. 3-10 shows X-Ray Diffraction (XRD) result of control sample, RTA in Ar ambient at 500°C for 30 seconds and 3 minutes. We can not identify crystal orientation of the peak in XRD result because the XRD data base have no non-stoichiometric hafnium oxides information. But it can be clearly observed that hafnium oxide crystallize if the time of Ar RTA is too long. This phenomenon may cause large leakage current in forming process.

Comparison of control sample and RTA at 400°C and 500°C in Ar ambient for 90 seconds under 0.5mA compliance current is depicted in Fig. 3-11. Table 3-1 also lists statistical quantities of switching parameters which is shown in Fig. 3-11. Fig. 3-11 (a) shows large variation of  $G_{\text{off}}$  after RTA compared to control sample. The temperature of Ar RTA is moderately positively related to  $G_{\text{on}}$  and maximal reset current as shown in Fig. 3-11 (b)(e) and Table 3-1. Large  $G_{\text{on}}$  implies more readout power consumption after RTA in Ar ambient. In addition, Fig. 3-11 (e) shows a tendency of large current operation after several tens switching in RTA 500°C for 90 seconds sample.

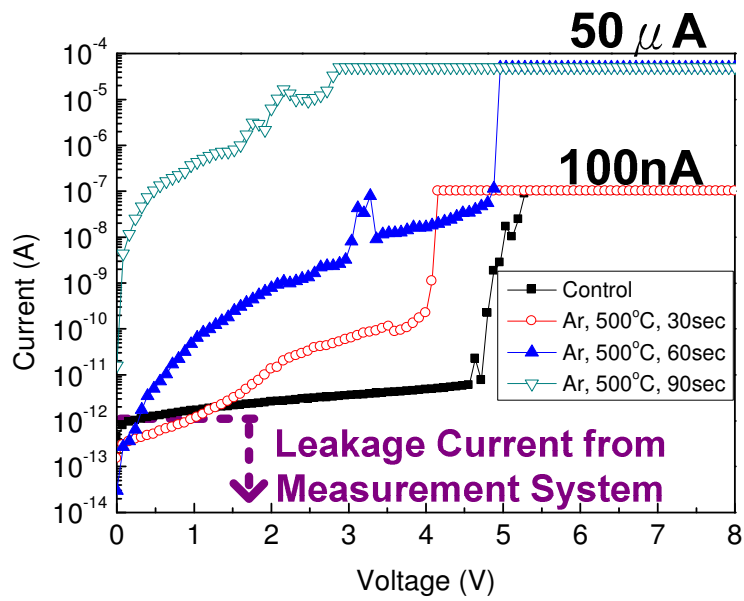


Fig. 3-9 Forming process I-V curves of control sample and RTA at 500°C in Ar ambient for 30, 60, and 90 seconds

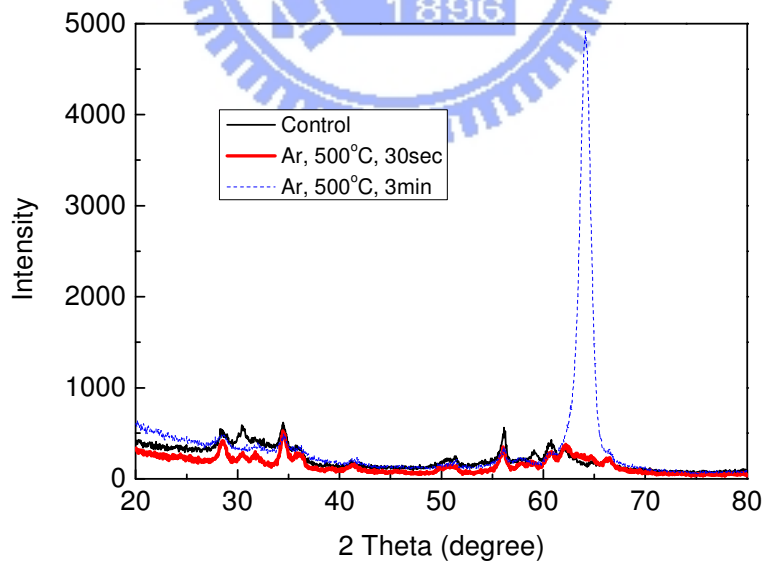


Fig. 3-10 X-Ray Diffraction result of control sample, RTA in Ar ambient at 500°C for 30 seconds and 3 minutes



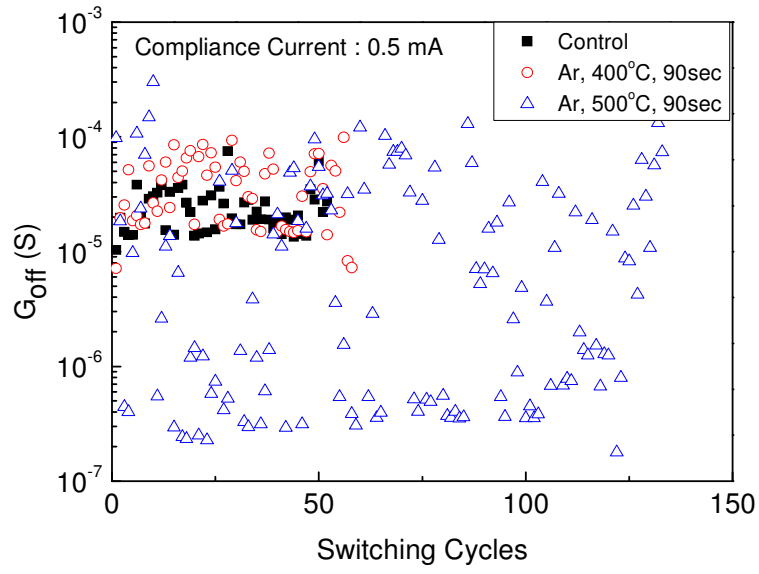


Fig. 3-11 (a)

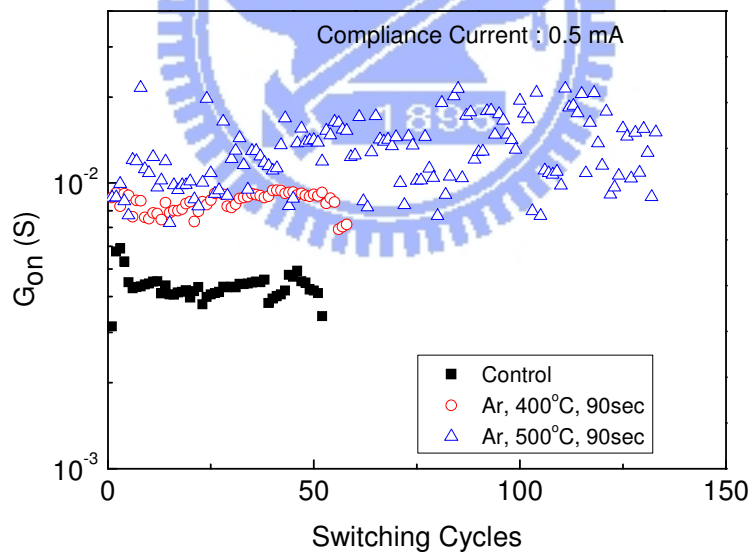


Fig. 3-11 (b)

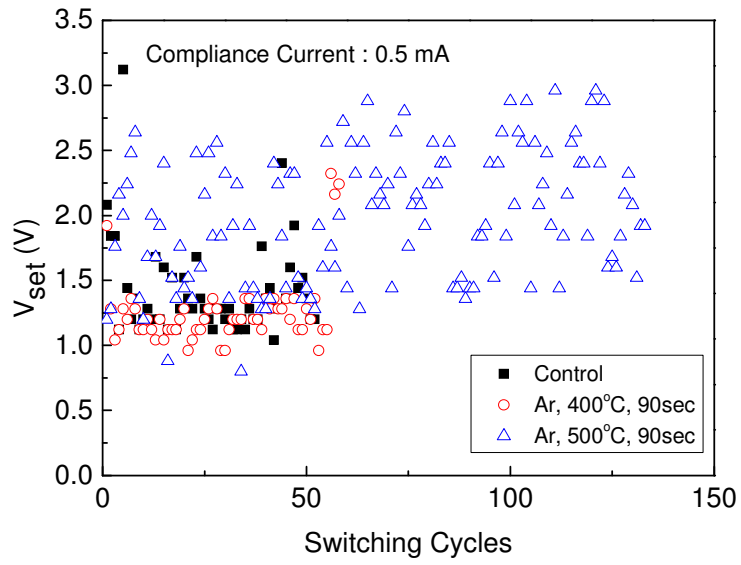


Fig. 3-11 (c)

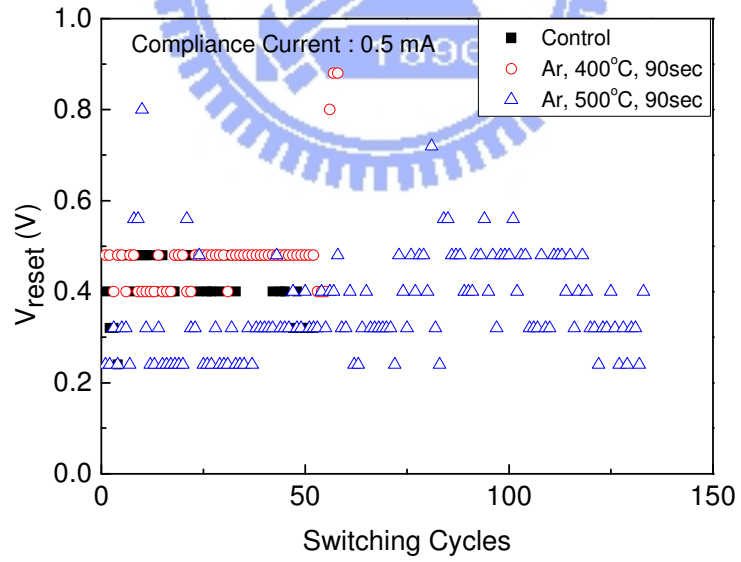


Fig. 3-11 (d)

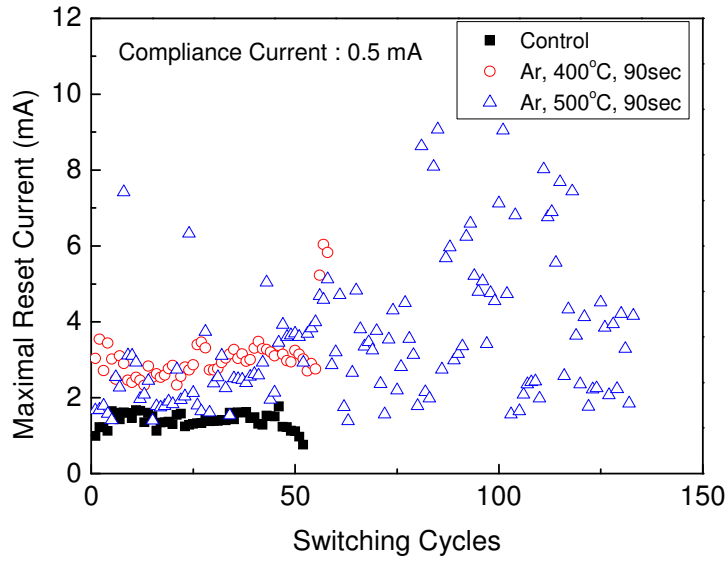


Fig. 3-11 (e)

Fig. 3-11 Comparison of control sample, RTA at 400°C and 500°C in Ar ambient for 90 seconds under 0.5 mA compliance current : (a)  $G_{off}$  (b)  $G_{on}$  (c)  $V_{set}$  (d)  $V_{reset}$  (e) Maximal reset current

Table 3-1 Statistical summary of control sample and RTA at 400°C and 500°C in Ar ambient for 90 seconds under 0.5 mA compliance current

		Control	Ar 400°C 90sec	Ar 500°C 90sec
$G_{off}$ (S)	$\mu$	$2.41 \times 10^{-5}$	$3.87 \times 10^{-5}$	$2.28 \times 10^{-5}$
$G_{on}$ (S)	$\mu$	$4.33 \times 10^{-3}$	$8.58 \times 10^{-3}$	$1.3 \times 10^{-2}$
Max $I_{reset}$ (A)	$\mu$	$1.39 \times 10^{-3}$	$3.07 \times 10^{-3}$	$3.49 \times 10^{-3}$
$V_{set}$ (V)	$\mu$	1.409	1.257	1.981
$V_{reset}$ (V)	$\mu$	0.431	0.477	0.362
Set Power (W)	$\mu$	$6.68 \times 10^{-5}$	$8.54 \times 10^{-5}$	$6.9 \times 10^{-5}$
Reset Power (W)	$\mu$	$4.15 \times 10^{-4}$	$1.02 \times 10^{-3}$	$9.91 \times 10^{-4}$

## 3-4 Comparison between Control Sample and RTA in O<sub>2</sub> Ambient

### 3-4-1 Time of RTA in O<sub>2</sub> Ambient at 400°C

Comparison of control sample and RTA at 400°C in O<sub>2</sub> ambient for 30, 60, 90 seconds under 0.5 mA compliance current are shown in Fig. 3-12. Table 3-2 also lists statistical quantities of switching parameters which is shown in Fig. 3-12.

From Fig. 3-12, we can observe some tendency in control, 30, and 60 seconds condition.  $G_{\text{off}}$ ,  $G_{\text{on}}$ , and maximal reset current decreases with time of O<sub>2</sub> RTA.  $V_{\text{set}}$  and  $V_{\text{reset}}$  increases with time of O<sub>2</sub> RTA. CV which is defined in section 3-2 increases with the time of O<sub>2</sub> RTA at 400°C as shown in Table 3-2. This phenomenon suggests that oxygen molecules fill some oxygen vacancies and this effect results in reduction of number of filaments. Reduction in number of filament leads to decrease of  $G_{\text{on}}$ ,  $G_{\text{off}}$ , and maximal reset current. Assume the number of filament of resistive switching is about the same in each sample of a variety of process conditions but different in each resistive switching, and then the sample deficient in filament (i.e. the longer O<sub>2</sub> RTA sample) has large variation in  $G_{\text{on}}$ . Although  $V_{\text{set}}$  and  $V_{\text{reset}}$  of O<sub>2</sub> RTA at 400°C for 60 seconds sample are little larger than control sample, the set and reset power are still smaller than control sample.

Moreover,  $G_{\text{on}}$  of O<sub>2</sub> RTA at 400°C for 90 seconds sample is too small and so dispersive (about three orders) that  $G_{\text{on}}$  and  $G_{\text{off}}$  are mixed up as shown in Fig. 3-13. As shown in Fig. 3-14 (a), on state I-V curves of O<sub>2</sub> RTA at 400°C for 60 seconds sample are linear at small readout voltage but 90 seconds sample are non-linear. Slope

of linear fitting are shown in Fig. 3-14 (b). It can be clearly observed that slope of O<sub>2</sub> RTA at 400°C for 60 seconds sample is equal to 1 but 90 seconds sample is larger than 1. Small G<sub>on</sub> and large reset current operation due to set incompletely on the O<sub>2</sub> RTA at 400°C for 90 seconds sample is not suitable for RRAM application. Hence, too many oxygen vacancies leads to large power consumption and lack of oxygen vacancies destroys RRAM characteristics. This phenomenon suggests that proper oxygen vacancies (or proper O<sub>2</sub> RTA) are needed in resistive switching operation.

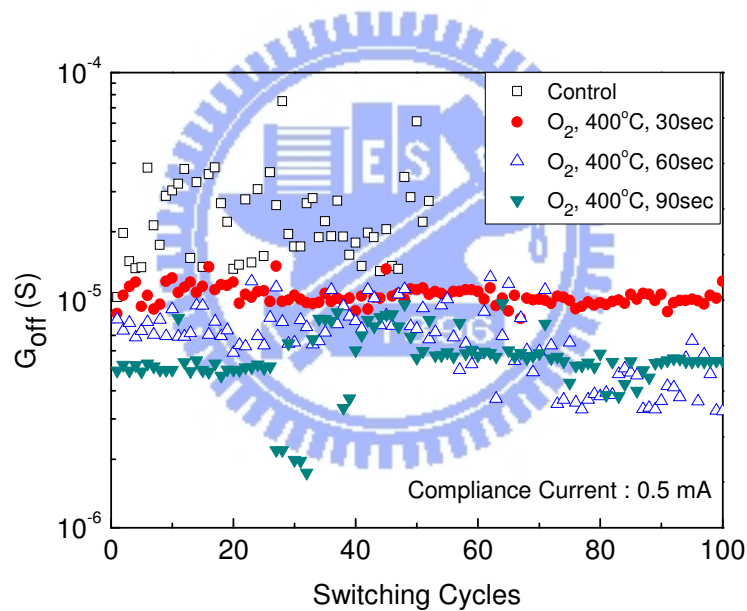


Fig. 3-12 (a)

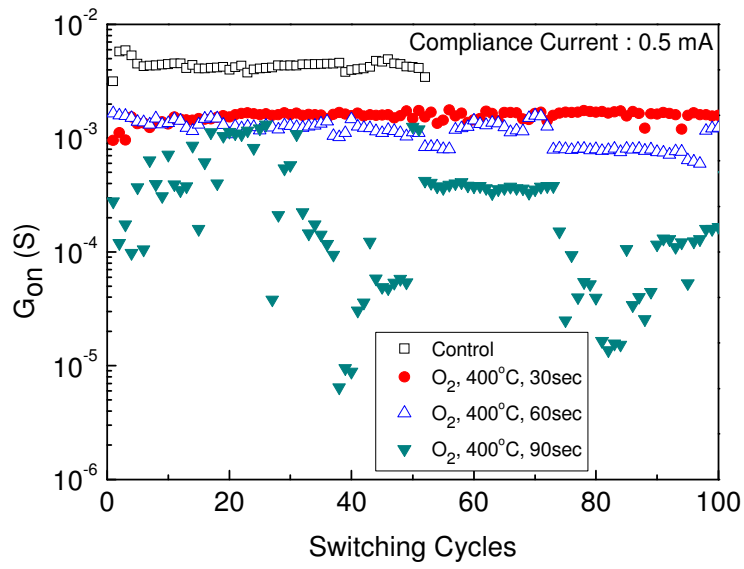


Fig. 3-12 (b)

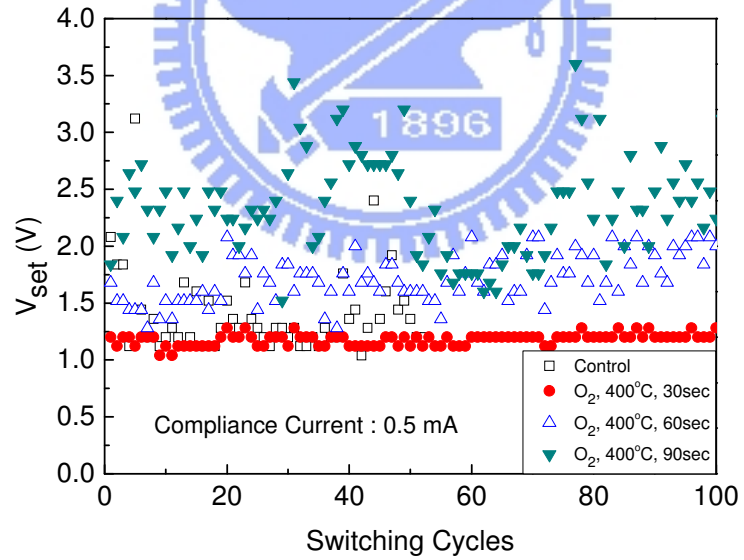


Fig. 3-12 (c)

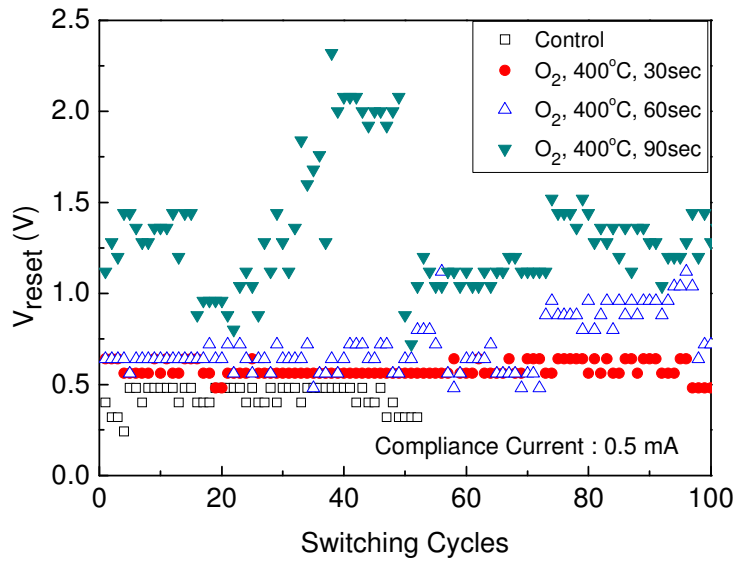


Fig. 3-12 (d)

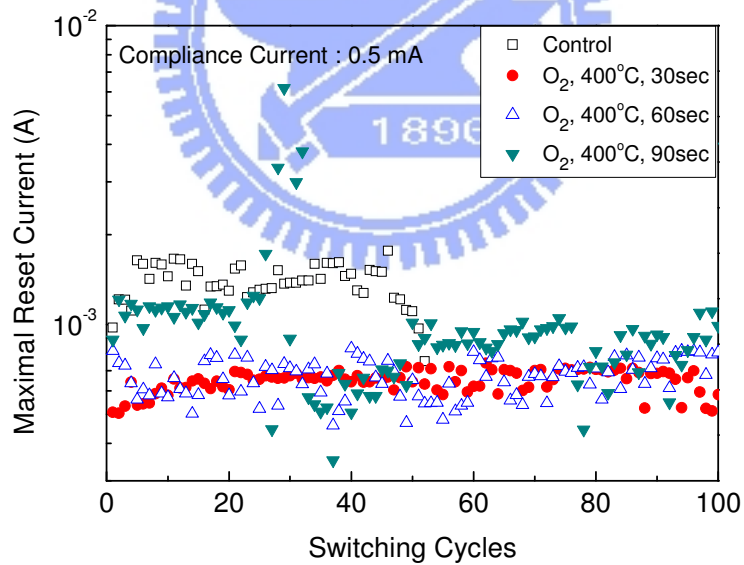


Fig. 3-12 (e)

Fig. 3-12 Comparison of control sample, RTA at 400°C in  $O_2$  ambient for 30, 60, and 90 seconds under 0.5 mA compliance current : (a)  $G_{\text{off}}$  (b)  $G_{\text{on}}$  (c)  $V_{\text{set}}$  (d)  $V_{\text{reset}}$  (e) Maximal reset current

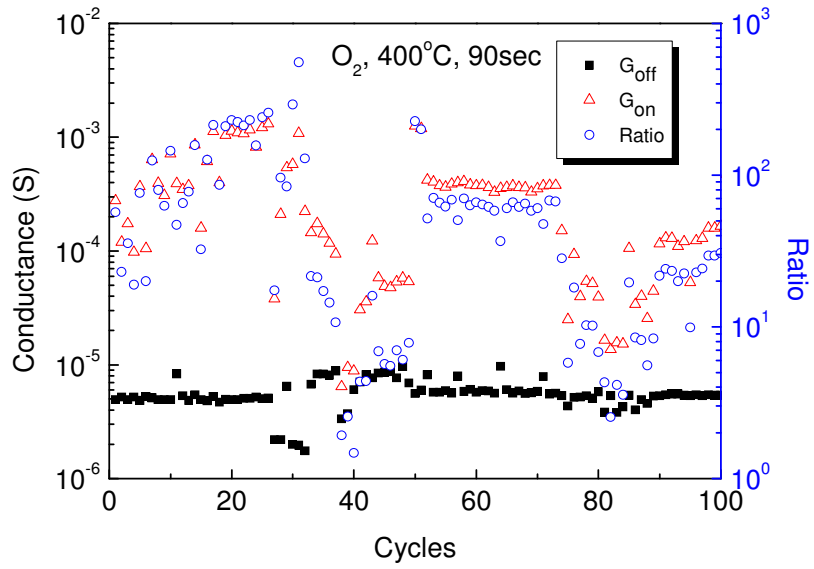


Fig. 3-13  $G_{on}$ ,  $G_{off}$ , and Ratio of the sample of RTA in  $O_2$  ambient at  $400^\circ C$  for 90 seconds under 0.5mA compliance current

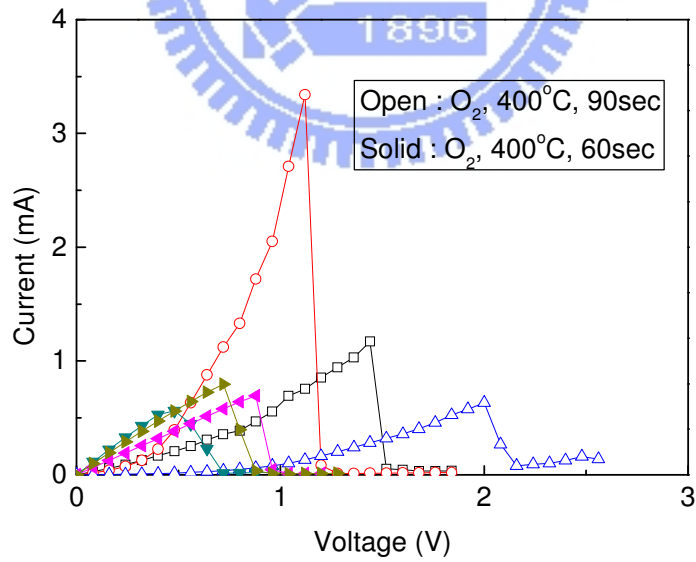


Fig. 3-14 (a)



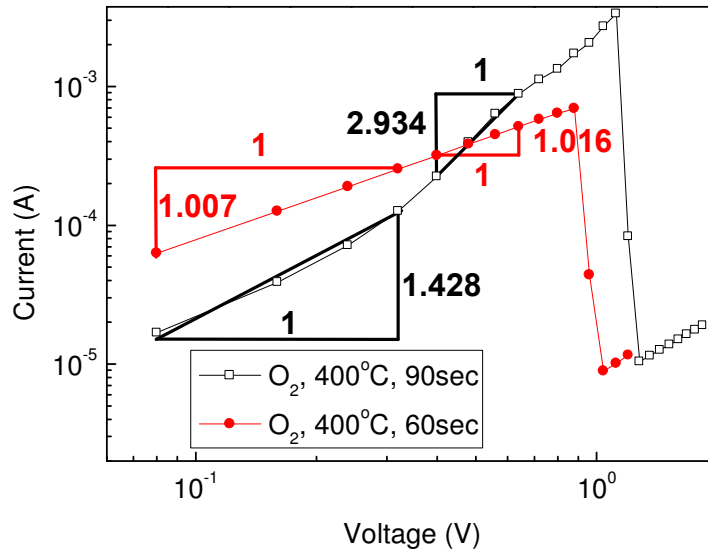


Fig. 3-14 (b)

Fig. 3-14 (a) Reset process I-V curves of O<sub>2</sub> RTA at 400°C for 60 and 90 seconds samples (b) Linear fitting of reset process log I – log V curves

Table 3-2 Statistical summary of control sample and RTA at 400°C in O<sub>2</sub> ambient for 30, 60, and 90 seconds under 0.5 mA compliance current

		Control	O <sub>2</sub> 400°C 30sec	O <sub>2</sub> 400°C 60sec	O <sub>2</sub> 400°C 90sec
G <sub>off</sub> (S)	μ	2.41×10 <sup>-5</sup>	1.04×10 <sup>-5</sup>	6.83×10 <sup>-6</sup>	5.67×10 <sup>-6</sup>
G <sub>on</sub> (S)	μ	4.33×10 <sup>-3</sup>	1.55×10 <sup>-3</sup>	1.14×10 <sup>-3</sup>	3.22×10 <sup>-4</sup>
	CV	0.10	0.10	0.24	1.03
Max I <sub>reset</sub> (A)	μ	1.39×10 <sup>-3</sup>	6.49×10 <sup>-4</sup>	6.69×10 <sup>-4</sup>	1.07×10 <sup>-3</sup>
	CV	0.15	0.09	0.15	0.70
V <sub>set</sub> (V)	μ	1.409	1.179	1.718	2.330
V <sub>reset</sub> (V)	μ	0.431	0.578	0.713	1.334
Set Power (W)	μ	6.68×10 <sup>-5</sup>	6.9×10 <sup>-5</sup>	5.39×10 <sup>-5</sup>	8.94×10 <sup>-5</sup>
Reset Power (W)	μ	4.15×10 <sup>-4</sup>	2.47×10 <sup>-4</sup>	2.89×10 <sup>-4</sup>	5.5×10 <sup>-4</sup>

### 3-4-2 Temperature of RTA in O<sub>2</sub> Ambient

Comparison of control sample, RTA in O<sub>2</sub> ambient for 60 and 90 seconds at 400°C or 30 seconds at 500°C under 0.5mA compliance current are shown in Fig. 3-15. Table 3-3 also lists statistical quantities of switching parameters which is shown in Fig. 3-15. From  $G_{on}$ ,  $G_{off}$ ,  $V_{set}$  and  $V_{reset}$  of Fig. 3-15 and Table 3-3, we can deduce that switching properties of O<sub>2</sub> RTA at 500°C for 30 seconds is between O<sub>2</sub> RTA at 400°C for 60 seconds and 90 seconds. In addition, O<sub>2</sub> RTA at 500°C for 30 seconds sample exhibits nonlinear I-V curves of on state at small readout voltage as O<sub>2</sub> RTA at 400°C for 90 seconds sample and reduces average conductance ratio smaller than two orders as depict in Table 3-3. Moreover, O<sub>2</sub> RTA at 500°C for 60 seconds sample does not exhibit resistive switching properties. As shown in Fig. 3-16, it can not be set even under 5mA compliance current and eventually burn out under larger compliance current. So far, the best RTA process condition of 0.5 mA compliance current operation is O<sub>2</sub> RTA at 400°C for 60 seconds.

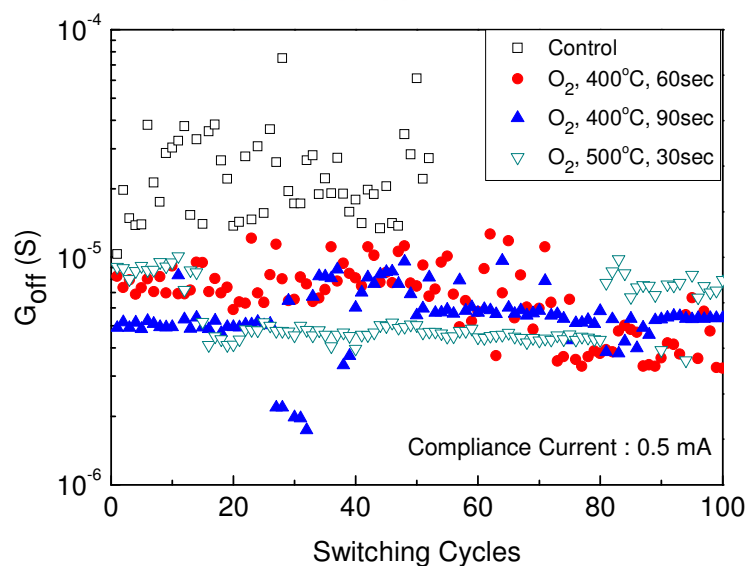


Fig. 3-15 (a)

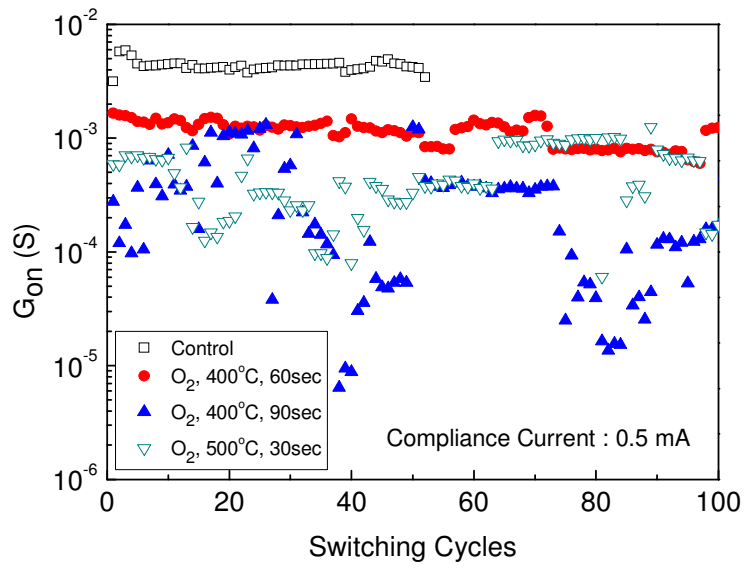


Fig. 3-15 (b)

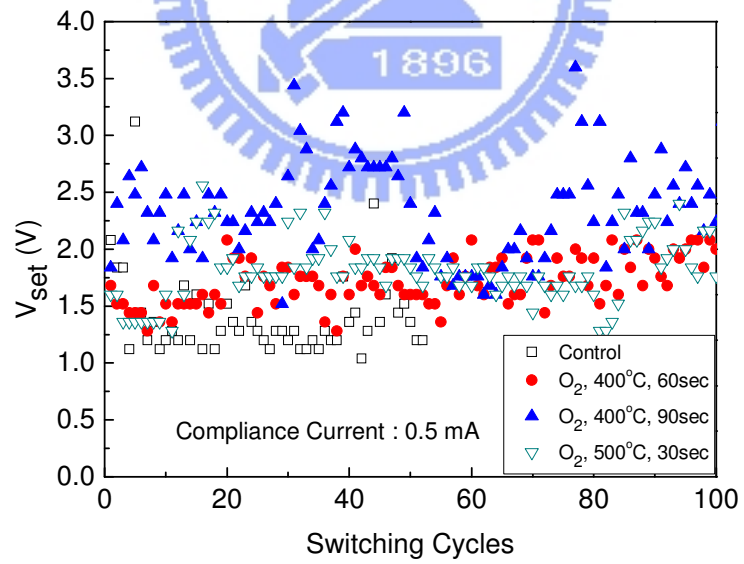


Fig. 3-15 (c)

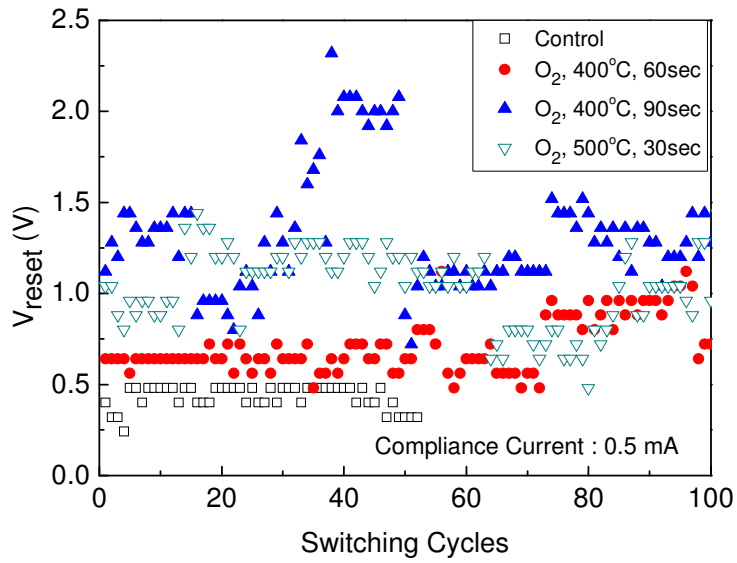


Fig. 3-15 (d)

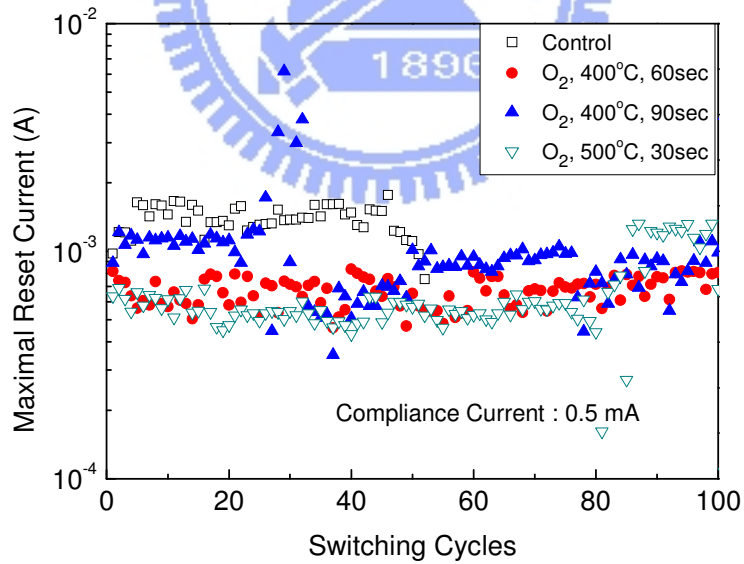


Fig. 3-15 (e)

Fig. 3-15 Comparison of control sample, RTA in  $O_2$  ambient for 60 and 90 seconds at  $400^\circ C$  or 30 seconds at  $500^\circ C$  under 0.5 mA compliance current : (a)  $G_{off}$  (b)  $G_{on}$  (c)  $V_{set}$  (d)  $V_{reset}$  (e) Maximal reset current

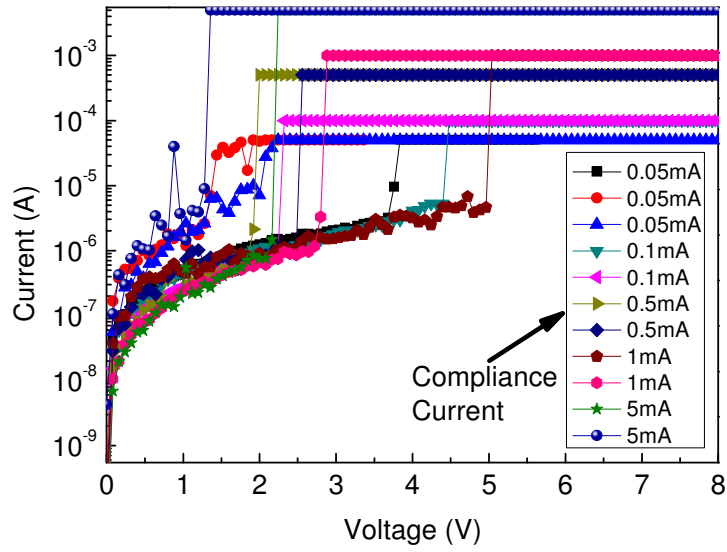


Fig. 3-16 Set process I-V curves of RTA in O<sub>2</sub> ambient at 500°C for 60 seconds

Table 3-3 Statistical summary of control sample and RTA in O<sub>2</sub> ambient at 400°C for 60, 90 seconds and 500°C for 30 seconds under 0.5 mA compliance current

		Control	O <sub>2</sub> 400°C 60sec	O <sub>2</sub> 500°C 30sec	O <sub>2</sub> 400°C 90sec
G <sub>off</sub> (S)	μ	2.41×10 <sup>-5</sup>	6.83×10 <sup>-6</sup>	5.76×10 <sup>-6</sup>	5.67×10 <sup>-6</sup>
	CV	0.10	0.24	0.61	1.03
G <sub>on</sub> (S)	μ	4.33×10 <sup>-3</sup>	1.14×10 <sup>-3</sup>	4.93×10 <sup>-4</sup>	3.22×10 <sup>-4</sup>
	CV	0.10	0.24	0.61	1.03
Max I <sub>reset</sub> (A)	μ	1.39×10 <sup>-3</sup>	6.69×10 <sup>-4</sup>	6.33×10 <sup>-4</sup>	1.07×10 <sup>-3</sup>
	CV	0.15	0.15	0.39	0.70
V <sub>set</sub> (V)	μ	1.409	1.718	1.828	2.330
V <sub>reset</sub> (V)	μ	0.431	0.713	1.042	1.334

### 3-4-3 Compliance Current

Comparison of control sample, RTA in O<sub>2</sub> ambient at 400°C for 30 and 60 seconds under 0.1 mA compliance current are shown in Fig. 3-17. Table 3-4 also lists statistical quantities of switching parameters which is shown in Fig. 3-17. It can be observed that O<sub>2</sub> RTA at 400°C for 30 seconds sample has small endurance (< 50) under 0.1mA compliance current as shown in Fig. 3-17. But it can be slightly elevated by increase of O<sub>2</sub> RTA time (RTA for 60 seconds sample). In addition, control sample and Ar RTA samples are not operated under 0.1mA compliance current. We think that control sample and Ar RTA samples leaves many filaments. So each filament acquires less energy under 0.1mA compliance current. This can also explain why O<sub>2</sub> RTA at 400°C for 30 seconds sample has small endurance. Moreover, maximal reset current is reduced by increasing the time of O<sub>2</sub> RTA. Maximal reset current of O<sub>2</sub> RTA at 400°C for 60 seconds sample is usually 0.1mA, but 0.5mA at some switching cycles. This phenomenon has been explained in Kinoshita et al.'s study [37]. In addition, the O<sub>2</sub> RTA at 400°C for 90 seconds sample can not be measured under 0.1 mA compliance current due to its small G<sub>on</sub>. The O<sub>2</sub> RTA at 400°C for 60 seconds sample can be measured under 0.05mA compliance current but it can switch only several tens times.

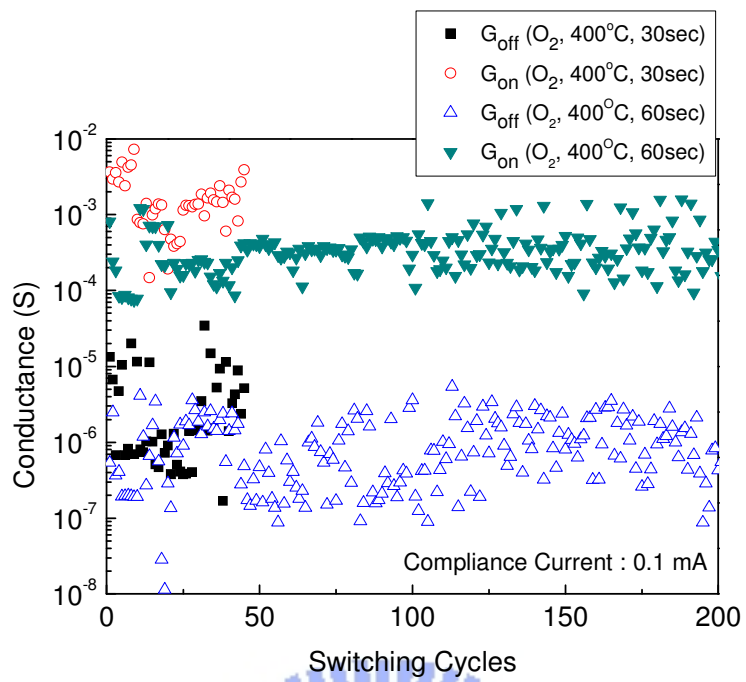


Fig. 3-17 (a)

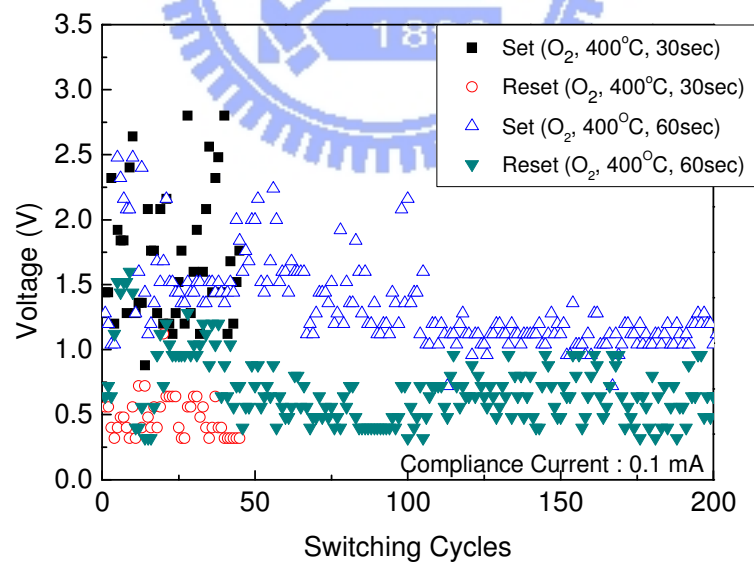


Fig. 3-17 (b)

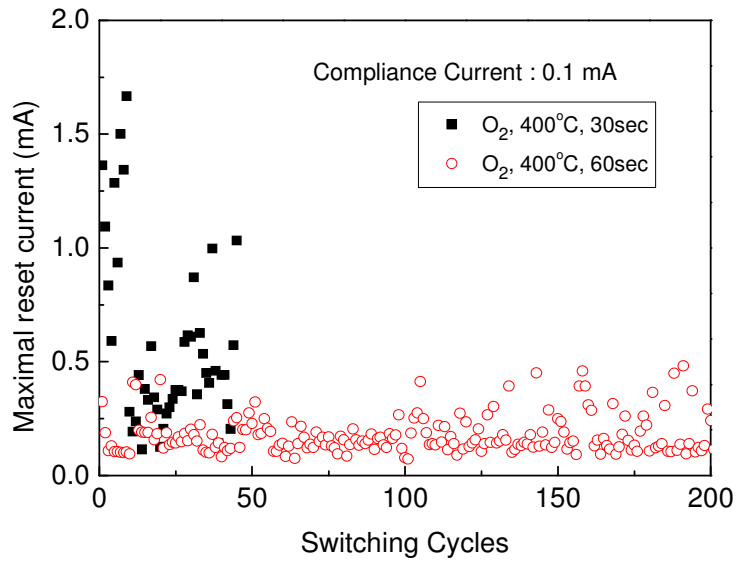


Fig. 3-17 (c)

Fig. 3-17 Comparison of control sample, RTA in O<sub>2</sub> ambient at 400°C for 30 and 60 seconds under 0.1 mA compliance current : (a) G<sub>off</sub> and G<sub>on</sub> (b) V<sub>set</sub> and V<sub>reset</sub> (c) Maximal reset current

Table 3-4 Statistical summary of control sample and RTA in O<sub>2</sub> ambient at 400°C for 30 and 60seconds under 0.1 mA compliance current

		O <sub>2</sub> 400°C 30sec	O <sub>2</sub> 400°C 60sec
G <sub>off</sub> (S)	μ	4.51×10 <sup>-6</sup>	1.09×10 <sup>-6</sup>
G <sub>on</sub> (S)	μ	1.82×10 <sup>-3</sup>	3.89×10 <sup>-4</sup>
Max I <sub>reset</sub> (A)	μ	5.73×10 <sup>-4</sup>	1.77×10 <sup>-4</sup>
V <sub>set</sub> (V)	μ	1.70	1.33
V <sub>reset</sub> (V)	μ	0.48	0.67
Set Power (W)	μ	1.29×10 <sup>-5</sup>	7.46×10 <sup>-6</sup>
Reset Power (W)	μ	1.81×10 <sup>-4</sup>	6.91×10 <sup>-5</sup>



### 3-4-4 Summary and Comparison

Retention test and switching cycles of RTA in O<sub>2</sub> ambient at 400°C for 60 seconds under 0.1mA compliance current are shown in Fig. 3-18. The retention test was directly given 0.08V (=V<sub>read</sub>) voltage bias and logarithmically detected off state current (I<sub>off</sub>) from 0 to 10<sup>4</sup> seconds. Off state conductance as shown in Fig. 3-18 was I<sub>off</sub> divided by V<sub>read</sub>. After retention test of off state, the cell was set to on state under 0.1mA compliance current. Similarly to off state retention test, on state retention was conducted again. On state conductance was also shown in Fig. 3-18. From Fig. 3-18, the retention test is agreeable to I-V sweep method. Also, this RRAM cell exhibits non-destructive readout and good data retention.

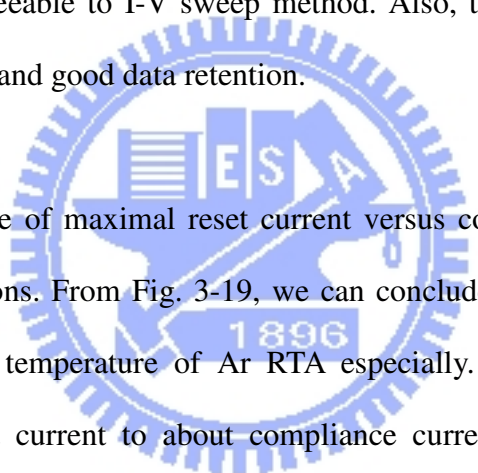


Fig. 3-19 shows average of maximal reset current versus compliance current under various process conditions. From Fig. 3-19, we can conclude that Ar RTA increases maximal reset current, temperature of Ar RTA especially. However, O<sub>2</sub> RTA can decrease maximal reset current to about compliance current even under different compliance currents. The effect that low compliance current led to low reset current has been mentioned in Gao et al.'s study [38].

Table 3-5 also lists the performance of the control sample and O<sub>2</sub> RTA at 400°C for 60 seconds under 0.1mA compliance current. We used low cost nickel (Ni) as top electrode and applied O<sub>2</sub> RTA to diminished compliance current to 0.1mA, maximal reset current to 0.18mA, set power to 7.46 μW, reset power to 69.1 μW, conductance of off state to 1.09 μS, conductance of on state to 0.39mS.

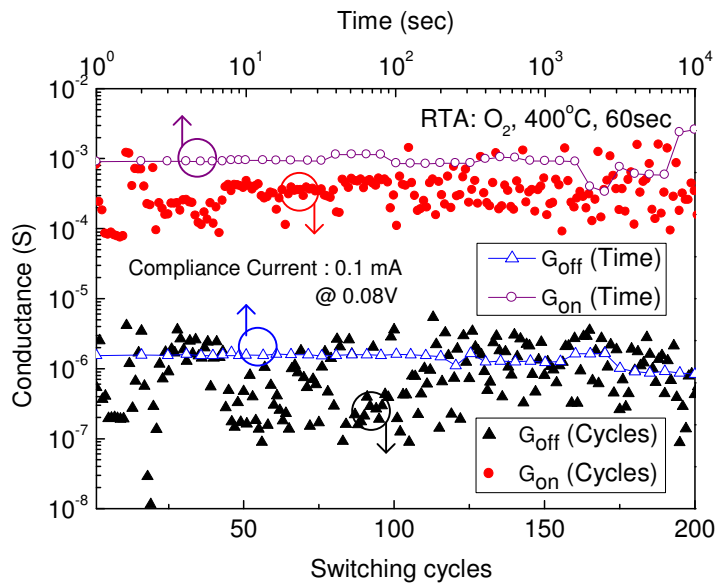


Fig. 3-18 Retention test and switching cycles of RTA in O<sub>2</sub> ambient at 400°C for 60 seconds under 0.1mA compliance current

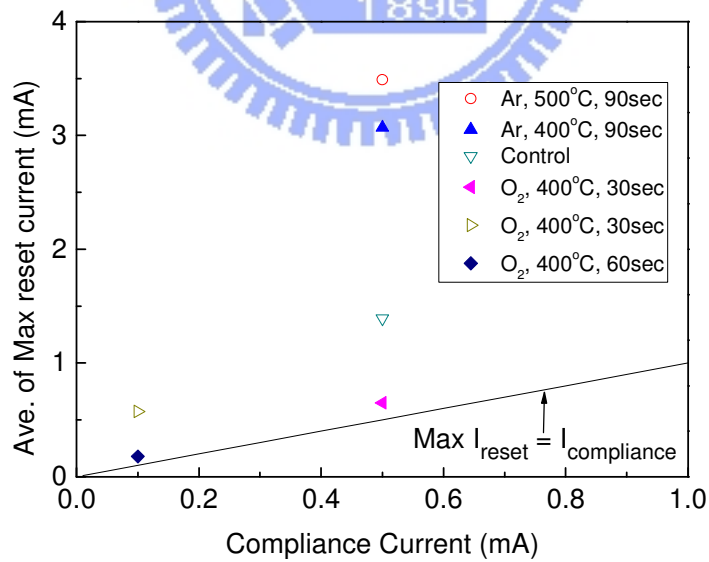


Fig. 3-19 Average of maximal reset current versus compliance current under various process conditions

Table 3-5 Summary of measured performance and comparison with the other works

	Control	O <sub>2</sub> 400°C 60sec	2007 JJAP [23]	2008 IEDM [16]
Structure	Ni/HfO <sub>x</sub> /TiN	Ni/HfO <sub>x</sub> /TiN	Pt/HfO <sub>x</sub> /TiN	Pt/TaO <sub>x</sub> /Pt
Compliance current (mA)	0.5	0.1	0.1	-
Operation mode	uni-polar	uni-polar	uni-polar	bi-polar
V <sub>read</sub> (V)	0.08	0.08	0.5	-
G <sub>off</sub> (S)	2.41×10 <sup>-5</sup>	1.09×10 <sup>-6</sup>	1×10 <sup>-6</sup>	4×10 <sup>-4</sup>
G <sub>on</sub> (S)	4.33×10 <sup>-3</sup>	3.89×10 <sup>-4</sup>	9.09×10 <sup>-5</sup>	6.67×10 <sup>-3</sup>
Max I <sub>reset</sub> (mA)	1.39	0.18	< 0.1	0.15
V <sub>set</sub> (V)	1.41	1.33	2.4	-1.5 **
V <sub>reset</sub> (V)	0.43	0.67	1.5	2 **
Set Power (W)	6.68×10 <sup>-5</sup>	7.46×10 <sup>-6</sup>	2.5×10 <sup>-4</sup> *	-
Reset Power (W)	4.15×10 <sup>-4</sup>	6.91×10 <sup>-5</sup>	1.5×10 <sup>-4</sup> *	-

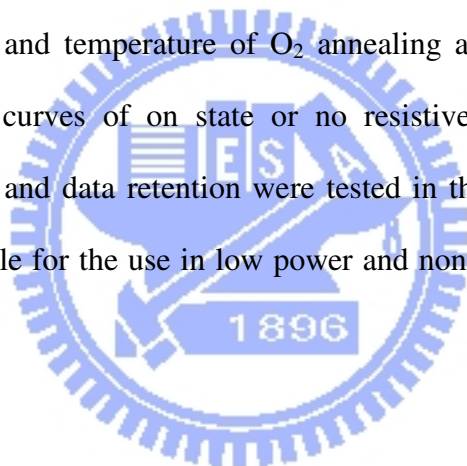
\* The definition of set power and reset power is not clear.

\*\* Measurement was conducted by pulse I-V measurement.

# CHAPTER 4

## CONCLUSION

The resistive switching characteristics of a Ni/HfO<sub>x</sub>/TiN structure have been demonstrated. Although the exact mechanism of RRAM is not yet clear, the filament model is plausible. Proper annealing can slightly elevate endurance. Ar RTA leads to large current of on state and reset process. Proper O<sub>2</sub> annealing can reduce compliance current, operation power, and conductance of on and off state while maintaining sufficient ratio. If time and temperature of O<sub>2</sub> annealing are too high, RRAM cell exhibits nonlinear I-V curves of on state or no resistive switching. In addition, non-destructive readout and data retention were tested in this thesis. Base on above reason, RRAM is suitable for the use in low power and nonvolatile memory portable device applications.



# CHAPTER 5

## FUTURE WORK

I am presenting preliminary results of a pilot experiment that will be further analyzed, expanded and replicated. The switching operation of set and reset under negative bias is the needful due to asymmetrical MOM structure. Chemical analysis is used to explore oxygen concentration of  $\text{HfO}_x$  films. Pulse I-V measurement can provide not only an extra degree of freedom of measurement, time but also more precise reliability tests, endurance and data retention. Future research is necessary to determine with certainty the relationship between optimal condition of  $\text{O}_2$  RTA and thickness or oxygen content of  $\text{HfO}_x$ . It may be of interest for future research that the effect of thermal treatment on RRAM which is constructed by the other metal top electrode. We are hopeful that future research will provide more detailed results which may support these views.

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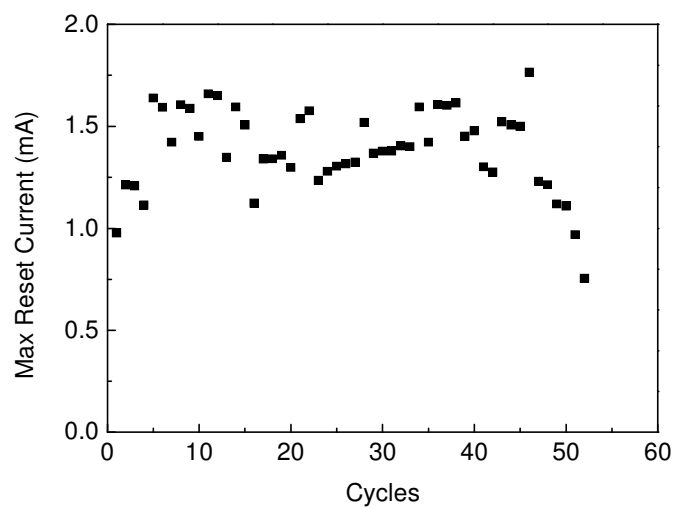
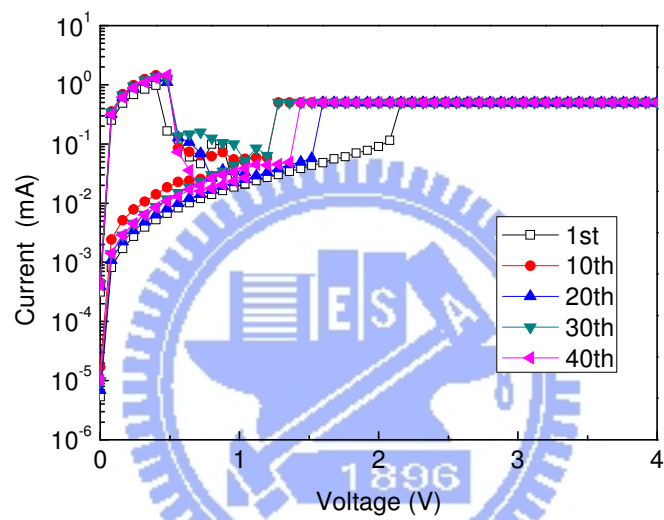
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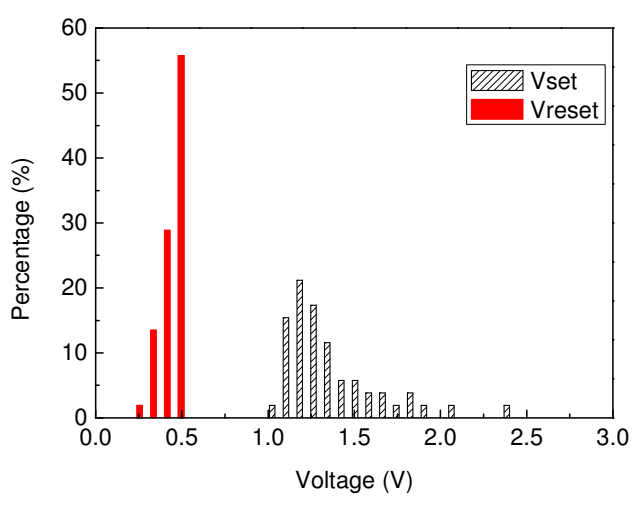
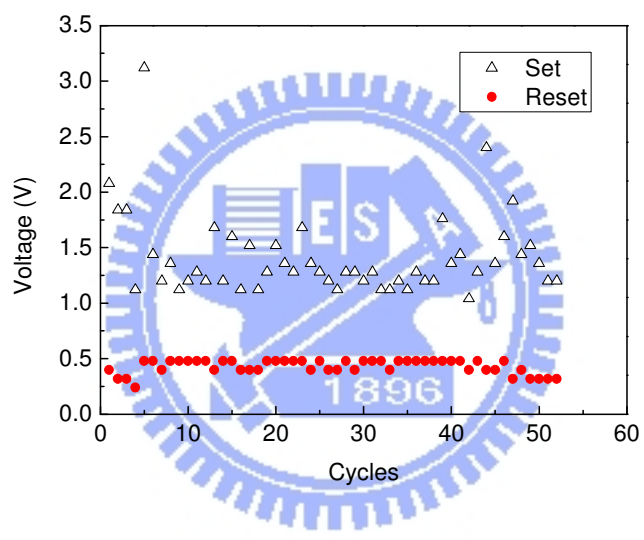
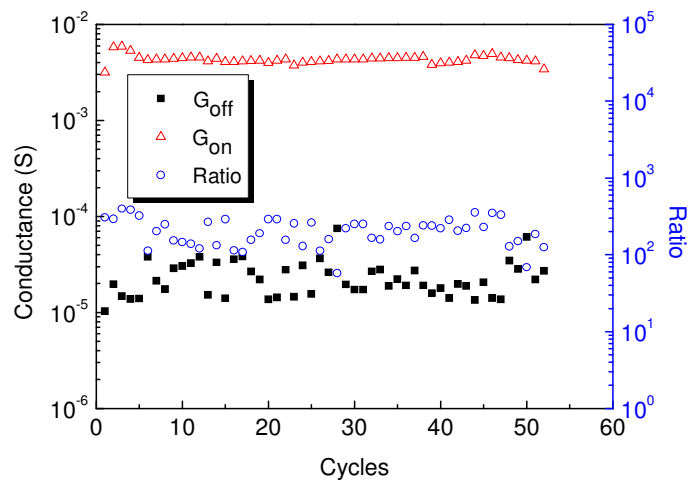


# APPENDIX A DETAIL

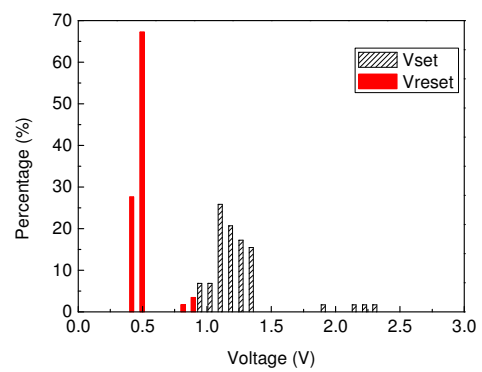
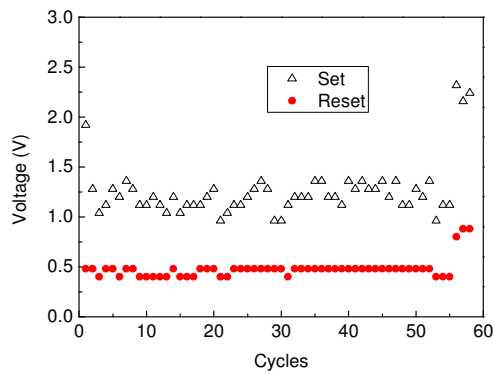
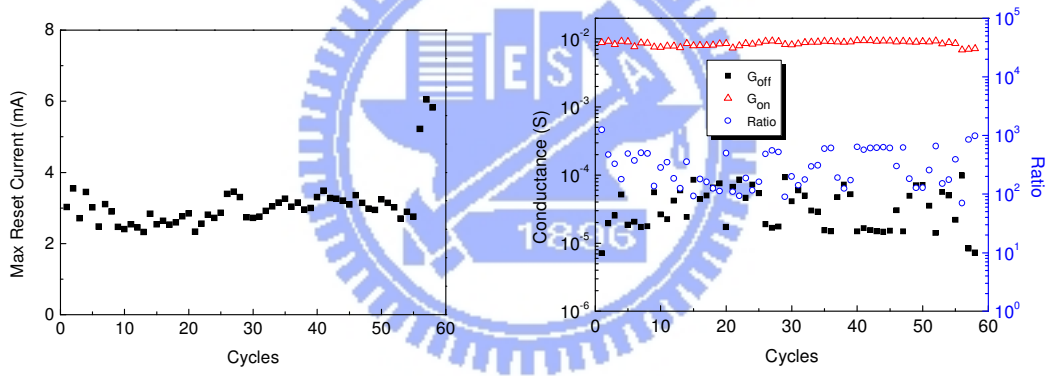
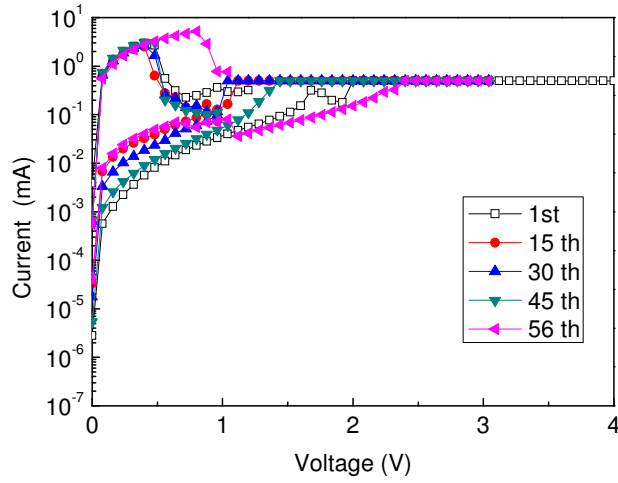
## MEASUREMENT RESULT

### A-1 Control Sample, 0.5mA

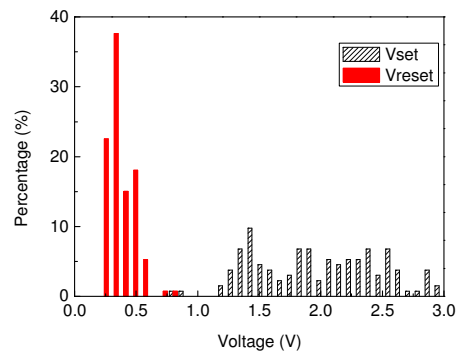
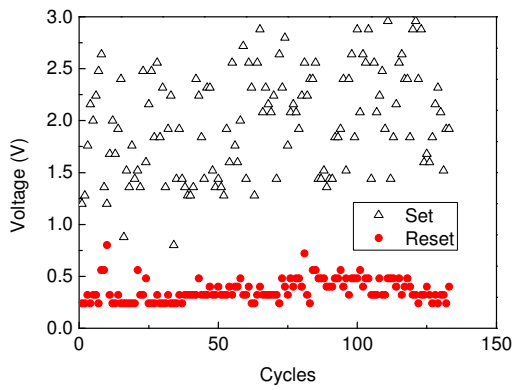
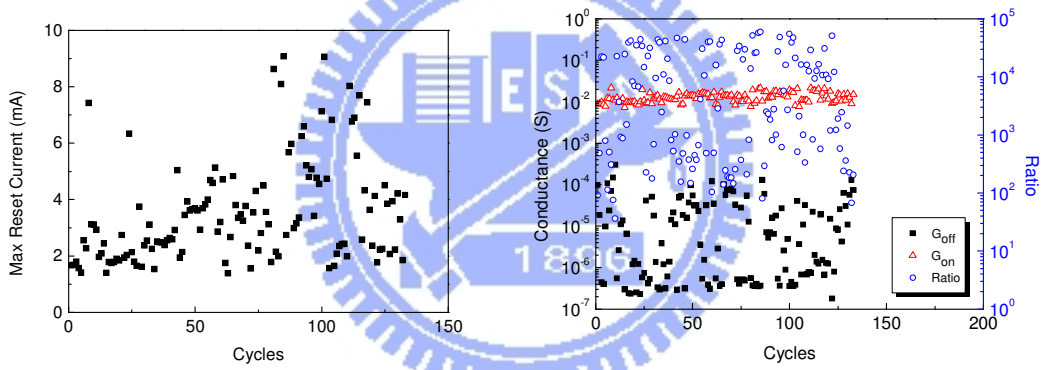
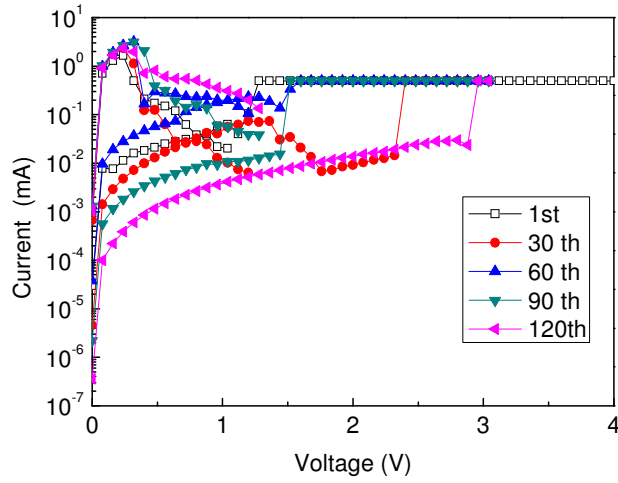




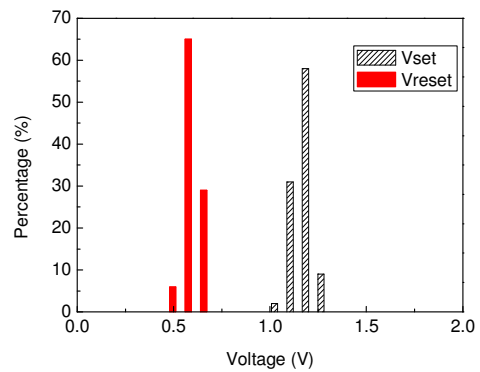
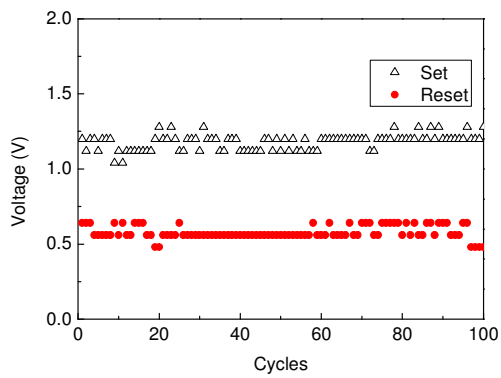
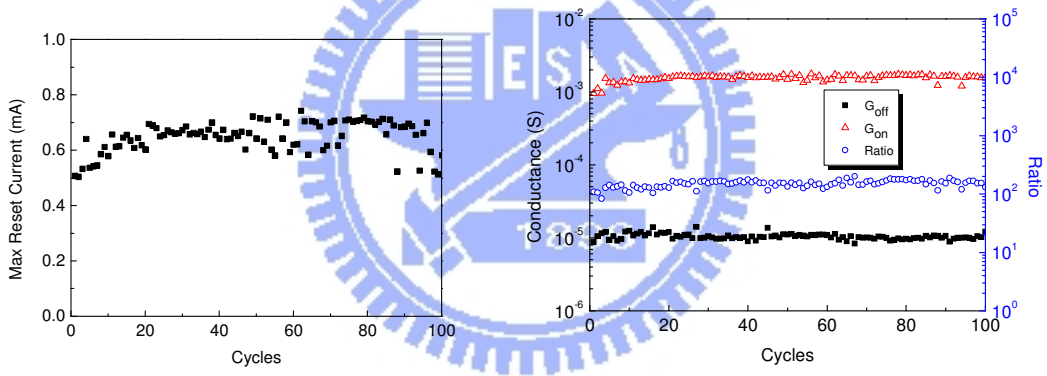
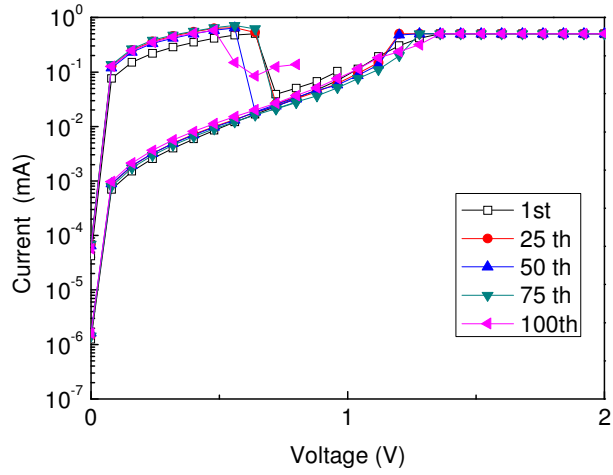
# A-2 Ar, 400°C, 90sec, 0.5mA



# A-3 Ar, 500°C, 90sec, 0.5mA

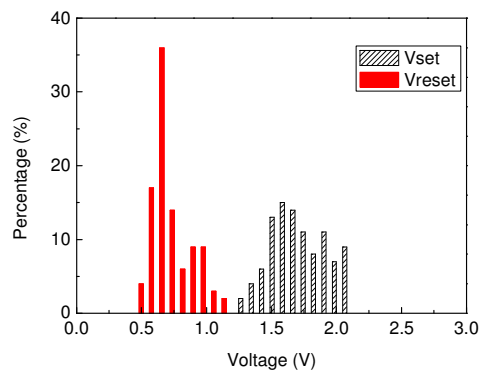
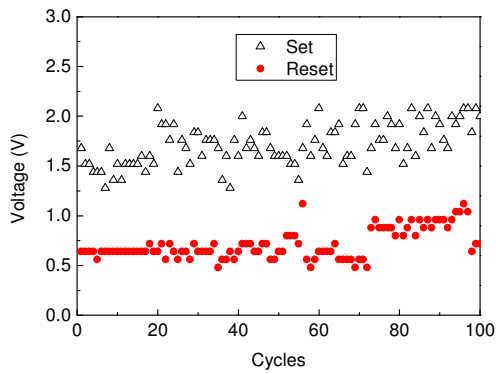
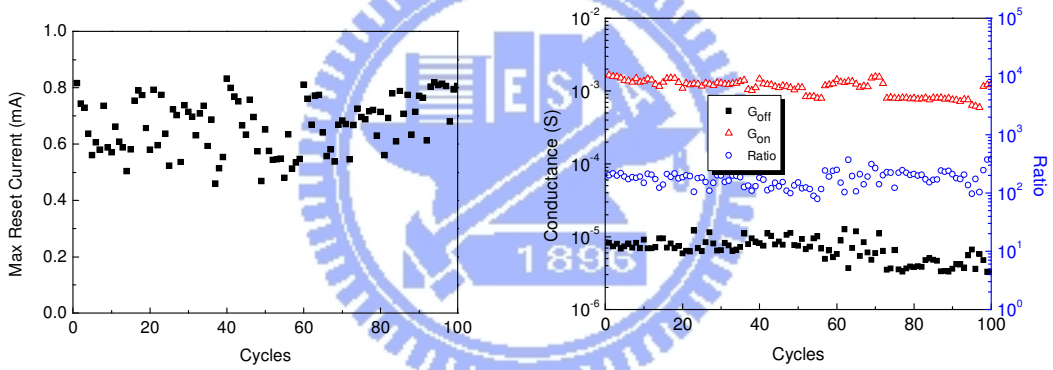
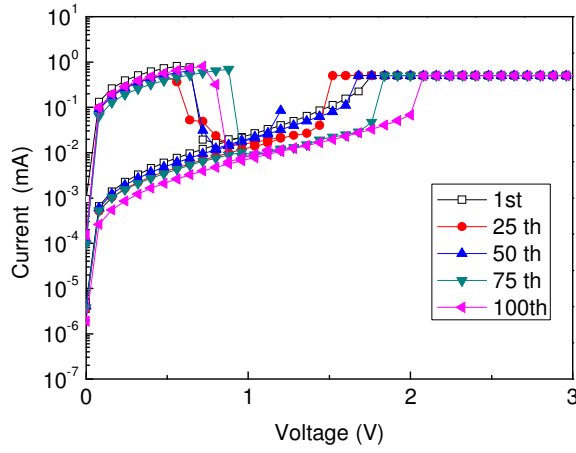


# A-4 O<sub>2</sub>, 400°C, 30 sec, 0.5mA

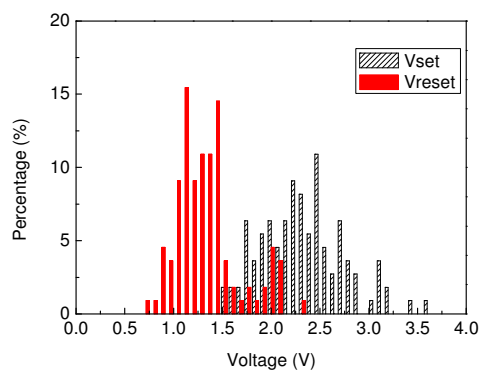
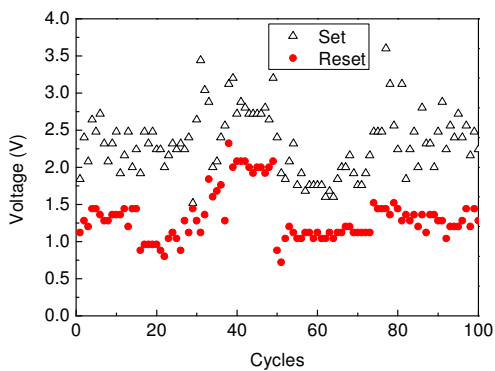
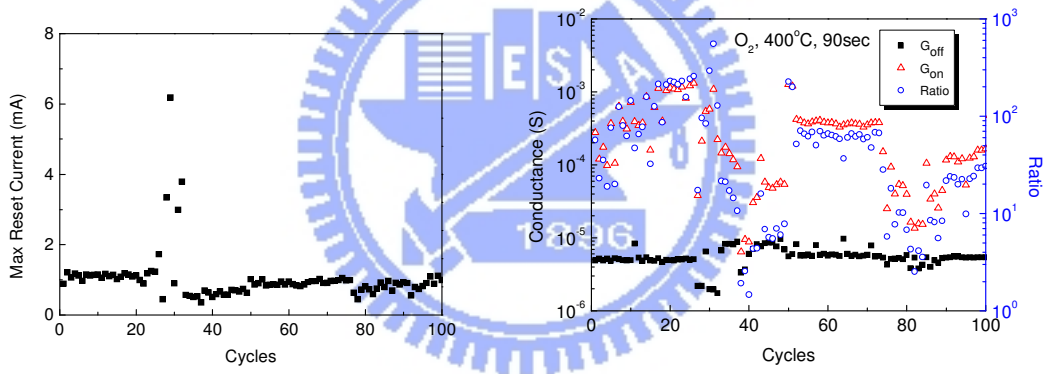
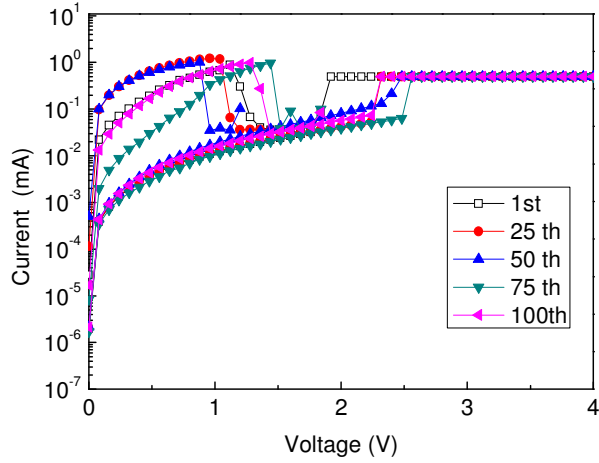




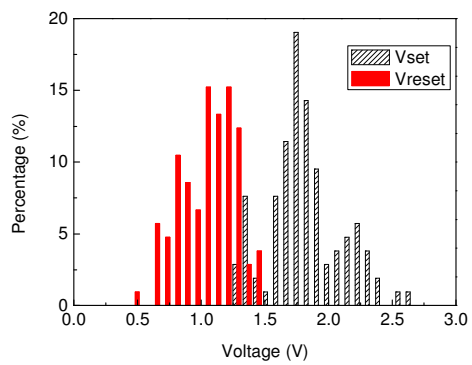
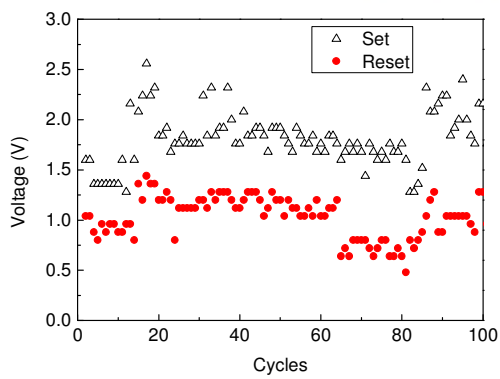
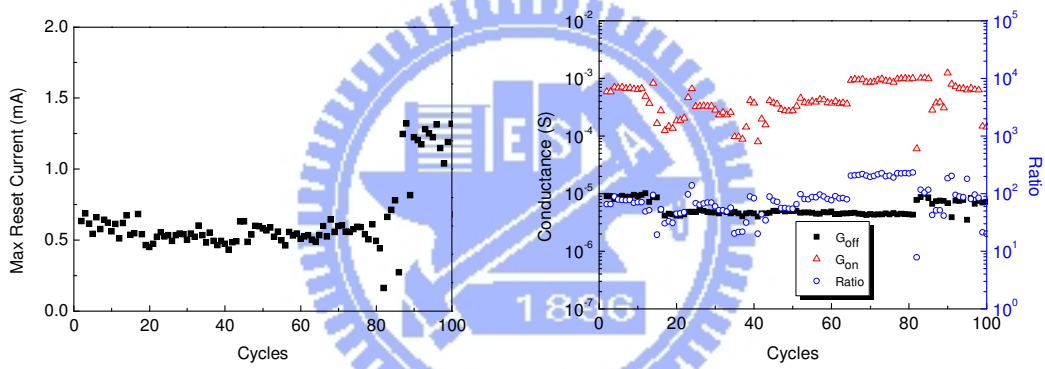
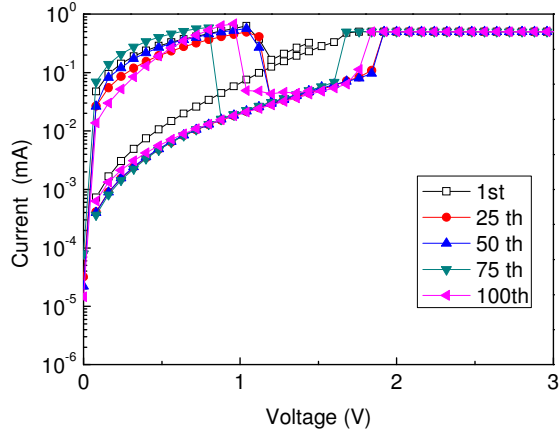
# A-5 O<sub>2</sub>, 400°C, 60 sec, 0.5mA



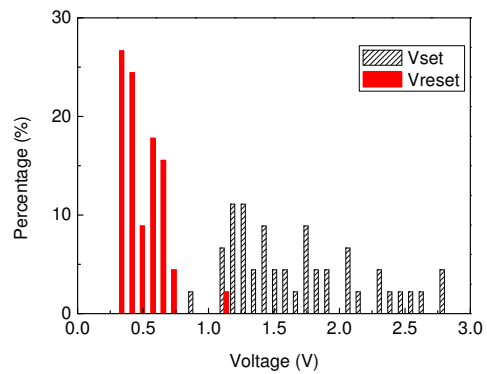
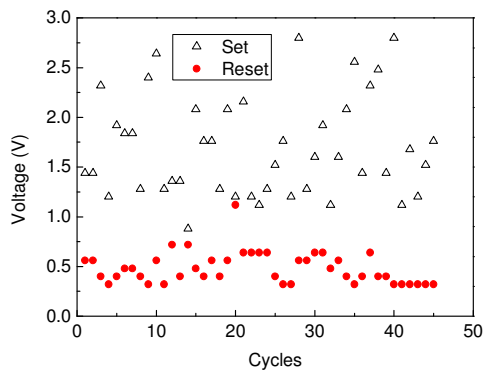
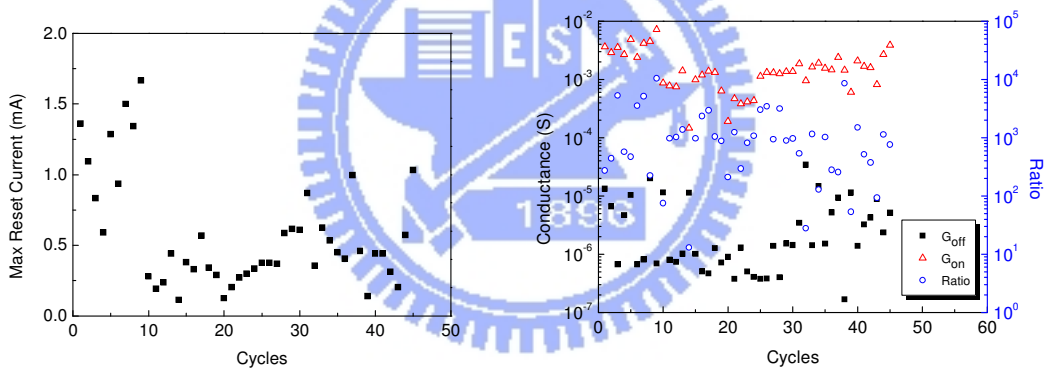
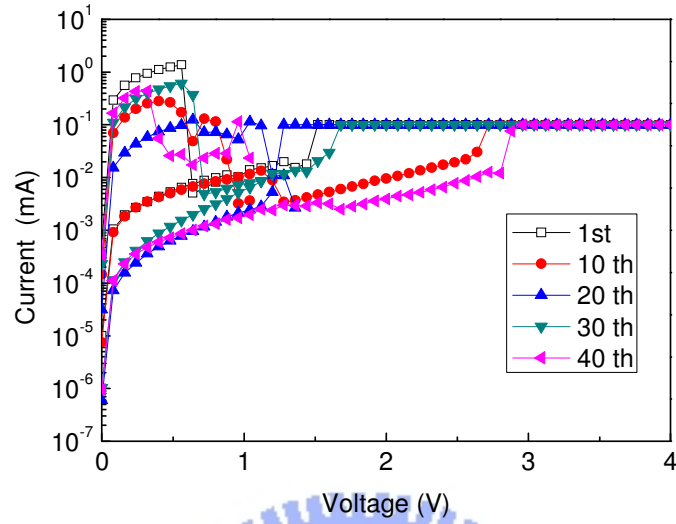
# A-6 O<sub>2</sub>, 400°C, 90 sec, 0.5mA



# A-7 O<sub>2</sub>, 500°C, 30 sec, 0.5mA



# A-8 O<sub>2</sub>, 400°C, 30 sec, 0.1mA



# A-9 O<sub>2</sub>, 400°C, 60 sec, 0.1mA

