

氧空缺和雙極電荷層於多晶矽-氧化矽-氮化矽- 氧化矽-矽形式非揮發性記憶體的应用

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在本篇論文中，我們將由於氫氣退火在二氧化鈦(HfO_2)上產生的氧空缺應用到二氧化鈦(HfO_2)薄膜和二氧化鈦(HfO_2)微晶粒記憶體電容上。我們發現在 300 度 C 氫氣退火一小時後，二氧化鈦(HfO_2)薄膜記憶體電容表現出最大的遲滯電壓差值(hysteresis)。並且在 500 度 C 氫氣退火一小時後，二氧化鈦(HfO_2)微晶粒記憶體電容顯示出最大的遲滯電壓差值。我們確認以二氧化鈦(HfO_2)薄膜或二氧化鈦(HfO_2)微晶粒為電荷捕捉層的 SONOS 型記憶體結構可以藉由以氫氣退火所產生的氧空缺來增大記憶窗口。

再者，我們將在氧化鋁(Al_2O_3)和二氧化矽(SiO_2)的接面上產生的”本質偶極”(intrinsic dipole)的觀念應用到二氧化鈦(HfO_2)薄膜和二氧化鈦(HfO_2)微晶粒記憶體電容上。我們指出經由在 SONOS 型記憶體電容中引入一層極薄(約 1 奈米)的高介電常數材料(HfO_2 或 Al_2O_3)可以有效的調節金屬電極的功函數。我們

發現對於二氧化鈺(HfO_2)薄膜和二氧化鈺(HfO_2)微晶粒記憶體電容來說，經由在電荷捕捉層和穿遂氧化層的中間引入一層極薄(約 1 奈米)的氧化鋁(Al_2O_3)，配合適當的氫氣退火溫度，我們甚至可以得到更大的遲滯電壓差值。

最後，我們提出一種雙極電荷層的新穎 SONOS 型非揮發性快閃記憶體結構。我們在穿遂氧化層之上引入一層約 1 奈米的氧化鋁(Al_2O_3)來造成雙極電荷的產生，並且使得寫入動作較為簡易。因此我們得到較快的寫入速度。更甚的是，這種新穎結構的 SONOS 型記憶體較傳統 SONOS 記憶體有更好的資料持久性。因此我們相信具有雙極電荷層的 SONOS 型快閃記憶體將有機會參與下個世代非揮發性記憶體的應用。



Applications of Oxygen Vacancies and Dipole Layer Engineering on SONOS-type Nonvolatile Memory

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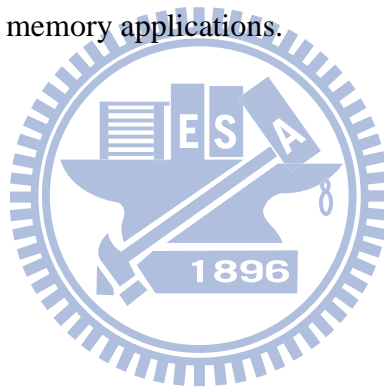


In this thesis, we utilize oxygen vacancies generated by forming gas anneal (FGA) to our HfO₂ thin film and nanocrystal memory capacitors. We find out that HfO₂ thin film memory capacitors show the highest hysteresis for FGA temperature of 300°C for 1 hour. And HfO₂ nanocrystal memory capacitors demonstrate the largest hysteresis for FGA temperature of 500°C for 1 hour. We confirm that it is effective to enlarge the memory window for SONOS-type memory structure with HfO₂ thin film or HfO₂ nanocrystal trapping layer by the FGA-generated oxygen vacancies.

Next, we adopt the concept of “intrinsic dipole” formed at Al₂O₃/SiO₂ interface to our HfO₂ thin film and nanocrystal memory capacitors. We demonstrate that modulating the gate effective work function by incorporating an ultra-thin (~1nm)

high-k layer (HfO_2 or Al_2O_3) in our SONOS-like memory capacitor is valid. We show that both for HfO_2 thin film and nanocrystal memory capacitor, the hysteresis could further be enhanced by incorporating a $\sim 1\text{nm}$ Al_2O_3 layer between trapping layer and tunnel oxide with appropriate temperature of FGA.

Finally, we propose a novel nonvolatile SONOS-type flash memory with dipole layer engineering. We incorporate a $\sim 1\text{nm}$ Al_2O_3 layer upon the tunnel oxide to induce dipole formation and results in easier programming. Thus we get higher programming speed. Furthermore, the novel SONOS-type flash memory exhibits better retention performance than the conventional SONOS memory. Therefore, we believe that SONOS-type flash memory with dipole layer engineering can be a candidate for next-generation nonvolatile memory applications.



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感謝我可愛的女友，謝謝妳在我的研究生涯中不離不棄的照顧我，希望你可以順利的申請到理想的學校。

謹以此論文，獻給所有關心我的人以及我最親愛的家人。

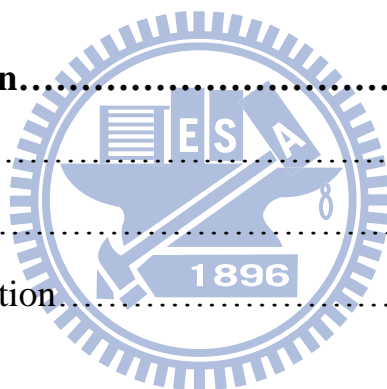
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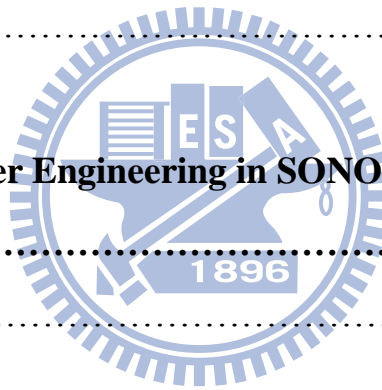
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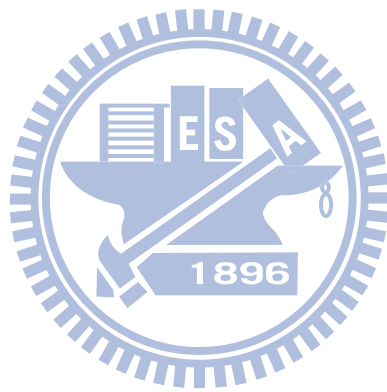


Figure Captions

Chapter 1

Fig. 1.1 (a) Schematic of a basic ETOX flash memory device[5]. (b) Mechanism of programming by CHE injection and erasing by FN tunneling[5].

Fig. 1.2 Band diagrams of program and erase the ETOX device. Electrons flow during programming by CHE injection. Electron or hole flow during erasing by FN tunneling or BTBHH injection.

Fig. 1.3 Current-Voltage characteristics of a memory device in the programmed state and erased state display the V_{th} shift and memory window.

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Chapter 2

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Fig. 2.7 XPS spectra of HfSiO_x film for *Hf 4f* and *Si 2p*. The HfSiO_x is deposited for different flow rate of Hf and Si by MOCVD system.

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Fig. 2.10 Capacitance-Voltage characteristics of HfO₂ nanocrystal memory capacitor for no FGA sample and FGA temperature of 300°C, 400°C, 500°C, and 600°C for 1 hour.

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Fig. 3.7 CV characteristics of HfO₂ thin film memory capacitor with ~1nm Al₂O₃ between HfO₂ and bottom SiO₂ for different FGA temperature from 300°C to 600°C for 1 hour.

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Fig. 3.9 CV characteristics of HfO₂ thin film memory capacitor with ~1nm Al₂O₃ both below and upon HfO₂ trapping layer for different FGA temperature from 300°C to 600°C for 1hour.

Fig. 3.10 CV characteristics of HfO₂ nanocrystal memory capacitor with ~1nm Al₂O₃ between HfO₂ trapping layer and bottom SiO₂ for different FGA temperature from 300°C to 600°C for 1hour.

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Fig. 3.12 CV characteristics of HfO₂ nanocrystal memory capacitor with ~1nm Al₂O₃ between HfO₂ trapping layer and top SiO₂ for different FGA temperature from 300°C to 600°C for 1hour.

Fig. 3.13 CV characteristics of HfO₂ nanocrystal memory capacitor with ~1nm Al₂O₃ both below and upon HfO₂ trapping layer for different FGA temperature from 300°C to 600°C for 1hour.

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Fig. 4.11 Schematic of endurance characteristics of SONOS and SONOS-type memory with dipole layer engineering.

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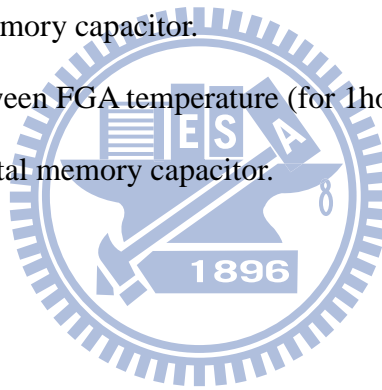
Table 2.1 Relationship between FGA temperature and CV hysteresis for HfO₂ thin film memory capacitor.

Table 2.2 Relationship between FGA temperature and CV hysteresis for Al₂O₃ thin film memory capacitor.

Table 2.3 HfSiO_x atomic composition of Hf and Si for three different flow rates.

Table 2.4 Relationship between FGA temperature and CV hysteresis for HfO₂ nanocrystal memory capacitor.

Table 2.5 Relationship between FGA temperature (for 1hour) and CV hysteresis for HfO₂ nanocrystal memory capacitor.



Chapter 3

Table 3.1 CV hysteresis of HfO₂ thin film memory capacitor with ~1nm Al₂O₃ between HfO₂ and bottom SiO₂ for different FGA temperature from 300°C to 600°C for 1hour.

Table 3.2 CV hysteresis of HfO₂ thin film memory capacitor with ~1nm Al₂O₃ both below and upon HfO₂ trapping layer for different FGA temperature from 300°C to 600°C for 1hour.

Table 3.3 CV hysteresis of HfO₂ nanocrystal memory capacitor with ~1nm Al₂O₃ between HfO₂ trapping layer and bottom SiO₂ for different FGA temperature from 300°C to 600°C for 1hour.

Table 3.4 CV hysteresis of HfO₂ nanocrystal memory capacitor with ~1nm Al₂O₃

between HfO₂ trapping layer and top SiO₂ for different FGA temperature from 300°C to 600°C for 1hour.

Table 3.5 CV hysteresis of HfO₂ nanocrystal memory capacitor with ~1nm Al₂O₃ both below and upon HfO₂ trapping layer for different FGA temperature from 300°C to 600°C for 1hour.

