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碩士論文

適用於正交頻分多工技術為基礎之無線通訊系統 的通道等化器設計

A Channel Equalizer for OFDM-based

Wireless Access Systems

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中華民國九十三年六月

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摘 要

在這篇論文中,我們提出了一個適用於正交分頻多工無線通訊系統的通道等化器,一併的解決在傳輸的過程中受到多路徑衰減、載波頻率以及取樣時脈飄移所造成的訊號失真。針對不同系統標準的需求,我們提出了兩種不同的架構;第一個是針對一般的正交分頻多工系統所提出的一個高效能的通道等化器、它包含了一個利用資料信號來做決策導向的通道估測,相較於一般文獻上常見的一階等化器或是利用導頻信號來做決策的方法,可以再降低 2.0~13.9dB 的估測錯誤量,另外,還包含了一個利用導頻信號的相位誤差追蹤機制,並且一併的回饋到等化作用的複數除法器,來移除三種造成訊號失真的因素。利用這樣一個先補償後偵測的方法,可以有效的提高追蹤機制的精確度。第二種架構是針對超寬頻系統所提出的一個高速的通道等化器,我們利用超寬頻系統只使用相位編碼的特性,資料可以單使用相位的資訊來作解調,因此,我們將原本的複數訊號經過一個正反切函數轉換到相位域,原本的複數乘除法在這樣的轉換之後可以減化為簡單的相位加減,臨界路徑也能從原本等化作用所需的複數除法器縮短為一個以查表運算為基礎的正反切函數設計,因而達到高速度的要求。在硬體實現的部分,使用平行兩套相位域的通道等化器可以達到 554Msymbol/s 的產出率,總邏輯開數目僅為 46.2K。

A Channel Equalizer for OFDM-based

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ABSTRACT

In this thesis, a robust channel equalizer is proposed for OFDM systems, which eliminates

multipath fading, carrier frequency offset and sampling clock offset simultaneously with

channel equalization. In order to meet different requirements of different OFDM applications,

two versions of channel equalizer are proposed. The first version is a high performance

channel equalizer for general OFDM systems. It comprises a decision-directed channel

estimation (CE) with tracking in data subcarriers, which has a better 2.0~13.9dB gain in CE

MSE compared with conventional zero-forcing CE or common pilot tracking CE; a

pilot-aided phase error tracking (PET), which contributes a better 1.9~2.3dB gain compared

with former approaches. The second version is a high-speed channel equalizer for

Ultra-Wideband system. Received data can be converted from complex number to phase

information only through an arc-tangent design for data decision in PSK modulation.

Complex multiplication and division can be simplified to real addition and subtraction with

coordinate conversion. Critical path can be reduced from the complex divider for equalization

to a look-up table based arc-tangent design. Maximum 554Msymbol/s throughput can be

achieved by using two parallel channel equalizers, which have a total gate count of 46.2K.

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兩年的時間一轉眼就過去了,在 Si2 實驗室的日子是充實而溫馨的,首先我要向指導教授李鎮宜博士表達我最誠摯的感謝,因為您兩年來的指導,讓我能找到正確的研究方向並獲益良多;另外,我也要感謝實驗室中的每一位成員,因為你們的幫助與鼓勵,讓我在遭遇到困難跟挫折的時候,能夠很快的找到解決的方法與勇氣;我要特別感謝劉軒字學長在研究過程中給我的建議與幫助,讓我能夠順利的完成我的論文;最後,我要感謝一直陪伴我的家人與朋友,謝謝你們讓我的求學生活多采多姿。

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Chapter 1.

Introduction

In this chapter, we describe the motivation of this research and the basic concepts of OFDM transmission. Advantages and disadvantages of current approaches will be discussed. Thesis organization will be listed in the end of this chapter.

1.1 Motivation

Orthogonal frequency division multiplexing (OFDM) is a multicarrier transmission; this concept of using parallel data transmission and frequency division multiplexing was drawn firstly in 1960s [1-2]. Due to the high channel efficiency and low multipath distortion that make high data rate possible, OFDM is wildly applied in the new generation wireless access systems such as wireless local area network (WLAN) [3-4] and digital broadcasting systems [5-6]. The technique of using orthogonal subcarriers saves the bandwidth, but increases the sensitivity to synchronization errors. Therefore, synchronization plays a very important role in OFDM based systems.

Multipath fading, carrier frequency offset (CFO) and sampling clock offset (SCO) are the three main data distortion issues in OFDM systems. Although basic channel estimation (CE) and time-domain acquisition is applied, additive-white-gaussion-noise (AWGN) and the practical time-variant channel effect cause this initial synchronization imperfect. These synchronization errors make frequency-domain data drift and rotate, hence degrade the system performance.

The object of this thesis is to design a channel equalizer, which eliminates the

frequency-domain compound signal distortion caused by multipath fading, residual CFO and SCO all together. The proposed channel equalizer consists of a robust CE, phase error tracking (PET), and a feedback compensation scheme. With respect to different requirements of different application systems, two versions are proposed for general OFDM systems [3] and high-speed Ultra-Wideband systems [4] respectively.

1.2 Introduction to OFDM systems

OFDM is a multicarrier transmission, which can be seen as either a modulation technique or a multiplexing technique. The basic principle of OFDM is to split a single data stream into several lower data-rate streams and transmit them simultaneously over a number of orthogonal subcarriers. Because of the extension of symbol duration, and the guard time introduced between OFDM symbols, OFDM is robust to against multipath fading channel. Moreover, OFDM data is parallel transmitted over a set of orthogonal sub-channels. The applied orthogonal technique not only increases the channel efficiency but also eliminates the influence of inter-carrier interference (ICI). Due to the advantages of low multipath distortion and high channel efficiency, OFDM is widely applied in high-speed communication systems. For example: High-speed wire communication systems such as ADSL, VDSL, and XDSL; wireless broadcasting systems such as DAB [5] and DVB [6]; high-speed wireless local area networks (WLAN) such as IEEE 802.11a [3], Hiperlan/2 [7]; OFDM is also the main candidate for the Ultra-Wideband (UWB) systems [4].

The basic idea of OFDM is the parallel-transmitted orthogonal subcarriers. Figure 1.2.1 shows the subcarriers of an OFDM symbol in the time interval T. The subcarrier with the lowest frequency is f_I , which equals the inverse of T. Each subcarrier has exactly an integer number of cycles in the interval T and the difference of cycle numbers between any two adjacent subcarriers is exactly one. This is the property of orthogonal subcarriers.

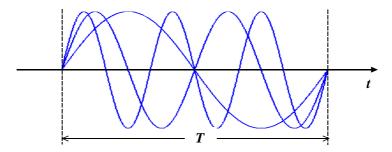


Figure 1.2.1 Example of three orthogonal subcarriers of an OFDM symbol

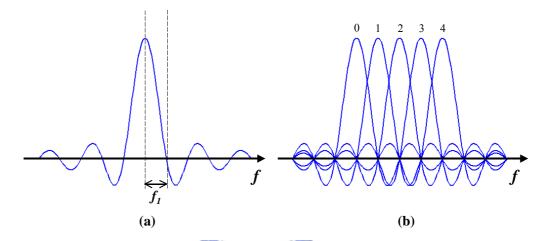


Figure 1.2.2 Spectrum of (a) a single sub-channel (b) orthogonal sub-channels of OFDM systems

In frequency domain, we can discover that the frequency interval between any two adjacent subcarriers is f_I and the center frequency of each subcarrier corresponds to zero crossing of all the other subcarriers. That is, sub-channels are overlapped to save bandwidth without ICI by using the orthogonal technique. Figure 1.2.2(a) shows the data spectrum of an individual subcarrier and Figure 1.2.2(b) shows the data spectrum of orthogonal subcarriers. The orthogonal transform can be performed by the discrete Fourier transform (DFT) instead of using banks of subcarrier oscillators to reduce implementation complexity [8]. That is, an IDFT is applied as an OFDM modulator which carries the N data symbols on a set of N orthogonal subcarriers. DFT is an OFDM demodulator to reverse this operation. Figure 1.2.3 shows the OFDM modulation using IDFT. In practice, the DFT is replaced by the fast Fourier transform (FFT) to reduce hardware complexity.

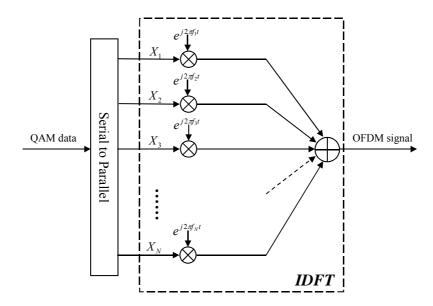


Figure 1.2.3 OFDM modulator using IDFT

One of the most important reasons to do OFDM is the efficient way to deal with multipath delay spread. By dividing the original data stream into several subcarriers, symbol duration extends, which reduces the relative delay spreading. In order to eliminate multipath fading completely, a cyclic prefix (CP) as a guard interval (GI) is introduced after IFFT in OFDM systems. The length of GI is chosen larger than the expected delay spread to avoid ISI. CP is applied to preserve the orthogonal property to avoid ICI. The format of OFDM symbol can be shown in Figure 1.2.4.

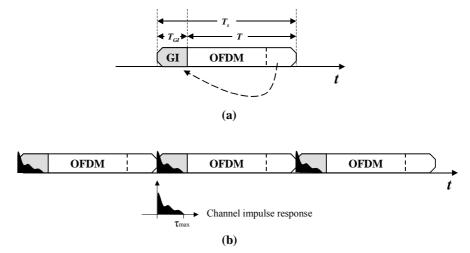


Figure 1.2.4 (a) a OFDM symbol format

(b) ISI caused by multipath fading channel, $T_{GI} > \tau_{max}$

Forward error correction (FEC) is the other main principle of OFDM system. In OFDM transmission, frequency-selective-fading causes different influence on each subcarrier. That is, some data subcarriers may completely be lost due to deep fading, which dominates the overall system performance. FEC is applied to solve this problem. Errors caused by weak subcarriers can be corrected by the coding information. OFDM systems with FEC scheme are often referred as coded OFDM (COFDM) systems. Figure 1.2.5 shows the block diagram of a general COFDM system.

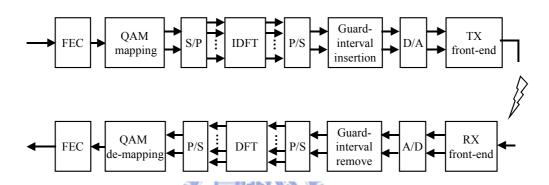


Figure 1.2.5 Bock diagram of a simple OFDM system

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1.3 Reviews of the Channel Equalizer Designs

In wireless environment, the ISI of multipath propagation, the CFO between the RF transmitter and receiver, and the SCO between DAC and ADC are the three main data distortion issues. In order to mitigate these effects, CE and PET are generally contained in the synchronization scheme of an OFDM-based system. However, CE and PET are designed separately in most of the approaches, which requires redundant compensators individually.

In OFDM systems, data subcarriers are equalized by the estimated channel frequency response (CFR) to mitigate the effect of multipath channel. A conventional zero forcing scheme is applied for CE [9]. Although this method does not need much hardware cost, CE error exists due to the additive noise in preamble and the time-variant channel of a practical wireless environment. This CE error causes equalized data drift and leads to performance

degradation. Several schemes have been proposed to enhance CE accuracy. A linear minimum mean-square-error (LLMSE) scheme was proposed in [10]. However, the CE error will saturate to a noise floor in high SNR regions. A programmable-coefficient channel interpolator and tracking method were applied in [11]. With tracking in pilot subcarriers, it achieved 2.5 ~ 3dB gain in CE mean-square-error (MSE) than the conventional zero-forcing approach. However, the performance was limited by the number of pilot subcarriers, and the hardware cost was significantly increased, 18 parallel complex multipliers were required in the channel interpolator.

Time-domain acquisition is generally applied in the synchronization of CFO and SCO [11 -12]. However, the influence of additive noise and channel effect cause this acquisition imperfect. The residual CFO and SCO cause phase rotation in frequency-domain data subcarriers, which seriously degrade the system performance. Both feedforward and feedback schemes were applied to mitigate these effect [12-13]. In the feedforward PET, it achieves a high performance when dealing with small phase errors, but failed when phase rotation exceed π . In the feedback PET, a higher tracking range can be achieved by the pre-compensation scheme. However, the tracking accuracy should be enhanced to avoid prediction error.

To solve the problems mentioned above, a robust channel equalizer is proposed in this thesis. It comprises a high accuracy data-aided decision-directed CE, and a pilot-aided PET with fixed-coefficient loop filters and prediction scheme. Multipath fading, residual CFO and SCO can be eliminated with equalization simultaneously, which simplified the compensation complexity by removing the individual compensation operations [18]. The detail algorithm and architecture will be described in the following chapters.

1.4 Organization of This Thesis

This thesis is organized as follows. In Chapter 2, the simulation platform and detail specifications of the IEEE 802.11a WLAN and the multi-band OFDM-based Ultra-Wideband system will be introduced. Algorithms of the proposed channel equalizers for different requirements will be described in Chapter 3 and Chapter 4 respectively. The simulation result and performance analysis will be discussed in Chapter 5. Chapter 6 will introduce the design methodology, hardware architecture, and the chip summary of the proposed design. Conclusion and future work will be given in Chapter 7.



Chapter 2.

System Platform

In this chapter, we introduce the two system platforms for design analysis and performance simulation. The first one is developed complaint to IEEE 802.11a physical layer (PHY) [1]. The second is complaint to the Ultra-Wideband (UWB) with multi-band OFDM modulation proposed by Texas Instrument (TI) [2]. The detail block diagrams and system specification will be described as follows.

2.1 Introduction to IEEE 802.11a System

IEEE 802.11a is an OFDM-based indoor WLAN system. The block diagram of the baseband transceiver can be illustrated in Figure 2.1.1.

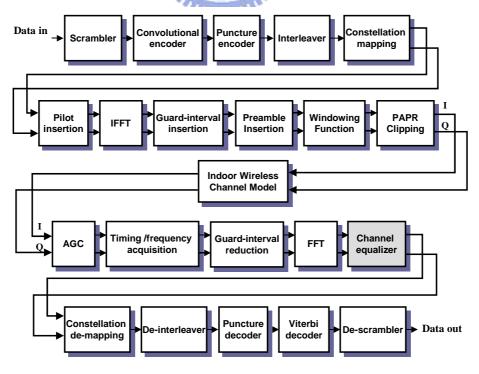


Figure 2.1.1 System platform of IEEE 802.11a PHY

The system platform includes a COFDM modem and an indoor radio channel model. The COFDM modem comprises a 64-point DFT-based QAM-OFDM modem and a forward-error correction (FEC) coding. The supported data rate is from 6Mbits/s to 54 Mbits/s with coding rate equals 1/2, 2/3 and 3/4. The system parameters can be listed in Table 2.1

Table 2-1 System parameters of IEEE 802.11a PHY

Constellation mapping method	BPSK, QPSK, 16QAM, 64QAM
Date rate (Mbits/s)	6, 9, 12, 18, 24, 32, 48, 54
FEC coding rate (R)	1/2, 2/3, 3/4
FFT size (N)	64
Number of used subcarriers (N _{ST})	52
Number of data carriers (N _{SP})	48
Number of pilot carriers (N _{SD})	4
IFFT/FFT period (T _{FFT})	3.2us
GI duration (T _{GI})	0.8us (T _{FFT} /4)
PLCP preamble duration	16us (T _{SHORT} + T _{LONG})

The PLCP preamble is a training sequence used for synchronization. It comprises ten short symbols and two long symbols. The two long symbols are used for zero forcing CE. The training structure can be shown in Figure 2.1.2.

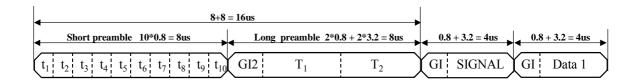


Figure 2.1.2 Training structure

The indoor radio channel model comprises a time-variant Rayleigh fading channel, CFO,

SCO and AWGN. The detail channel model blocks will be discussed in the later section. In the baseband receiver, first, time-domain synchronization estimates and compensates signal distortion from AGC error, symbol-timing drift, and CFO. And then FFT is applied to make signal transformation from time domain to frequency domain. After FFT, the proposed channel equalizer is applied to remove frequency domain data distortion issues, such as multipath fading, residual CFO and SCO. Later, the data subcarriers are de-mapped and decoded by the FEC design. Finally, the received system parameters and data bits are sent to MAC.

2.2 Introduction to Ultra WideBand System

UWB is a new generation wireless communication systems, which is used for high-speed, short-range data access. It will be wildly used in the future digital home electronics appliance industry. UWB has not been standardized; we focus on the multi-band OFDM UWB in this thesis [2]. The block diagram of the UWB PHY is similar to the IEEE 802.11a WLAN system. The key differences between these two systems can be listed as follow,

 OFDM symbols are interleaved across both frequency and time. An example of the time-frequency interleaving (TFI) can be shown in Figure 2.2.1.

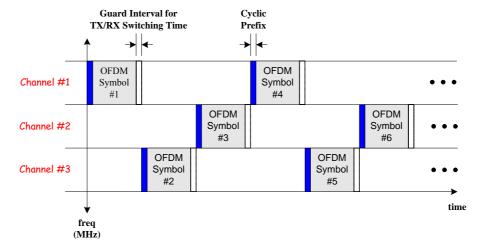


Figure 2.2.1 TFI example of the UWB PHY [2]

- The supported data rate is up to 480Mbits/s, which is almost ten times of the data rate in IEEE 802.11a systems. A 128-point FFT is applied and only PSK (BPSK, QPSK) is used in the UWB system.
- In the 55Mbits/s and 80Mbits/s transmission mode, data subcarriers are duplicated four times within an OFDM symbol. In the 110Mbits/s, 160Mbits/s and 200Mbits/s mode, data subcarriers are duplicated twice within a OFDM symbol

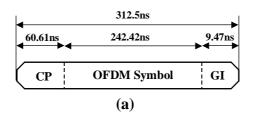
The detail specifications of the multi-band OFDM UWB PHY can be listed in Table 2.2.

Table 2-2 System parameters of the multi-band OFDM UWB PHY

Constellation mapping method	BPSK, QPSK
Date rate (Mbits/s)	55, 80, 110, 160, 200, 320, 480
FEC coding rate (R)	1/2, 3/4, 5/8, 11/32
FFT size (N)	128
Number of used subcarriers (N _{ST})	112
Number of data carriers (N _{SP})	100
Number of pilot carriers (N _{SD})	12
Data bytes per packet	1024
IFFT/FFT period (T _{FFT})	242.42ns
Cyclic prefix duration (T _{CP})	60.61ns (T _{FFT} /4)
Guard interval duration (T _{GI})	9.47ns
PLCP preamble duration	9.375us $(T_{SHORT} + T_{LONG})$

Because of the time interleaving, the data format is a little different from the format in 802.11a system. The preamble and data format can be shown in Figure 2.2.2. In IEEE 802.11a system, GI is the cyclic prefix of each OFDM symbols, which is used for the concern of

multipath spreading. In UWB system, cyclic prefix (CP) is for multipath concern and the GI is particularly referred to the time between band switching. Because of the time interleaving, six OFDM symbols are used for basic channel estimation. For example, CE₀, CE₄ is for channel #1; CE₁, CE₅ are for channel #2; and so on.



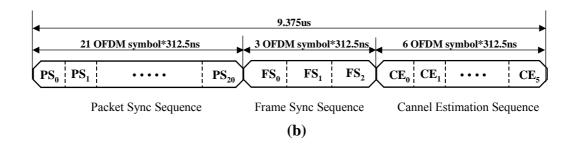


Figure 2.2.2 UWB PHY (a) OFDM symbol format (b) training structure

Although the signal flow of UWB is very similar to the flow in 802.11a system. There are some particulars that can be used for design modification and algorithm improvement to achieve the very high-speed and low-cost requirements of UWB system. This part will be discussed in chapter 4.

2.3 The Indoor Wireless Channel Model

In order to simulate the data transmission in the real environment, an indoor wireless channel model is established, which includes a time-variant multipath fading [14-15], CFO, SCO, and AWGN. The detailed are introduced individually below

2.3.1 Multipath Fading Channel Model

In wireless transmission, transmitted signal arrives at receiver through several paths with different time delay and power decay, which is called multipath interference. The received signal can be modeled as

$$y(t) = x(t) + \sum_{N} \beta_{N} \cdot x(t - \tau_{N})$$
(2-1)

ISI and a frequency-selective fading occur when the maximum delay spread is larger than the symbol period or the channel coherent bandwidth is smaller than the data bandwidth. The applied multipath fading channel is established according to the IEEE specification. The applied multipath fading channel consists of 13 independent taps, which has Rayleigh distributed magnitude, exponentially decayed power and random uniformly distributed phase [14]. The channel impulse response (CIR) and the channel frequency response (CFR) with RMS delay equals 50ns are shown in Figure 2.3.1.

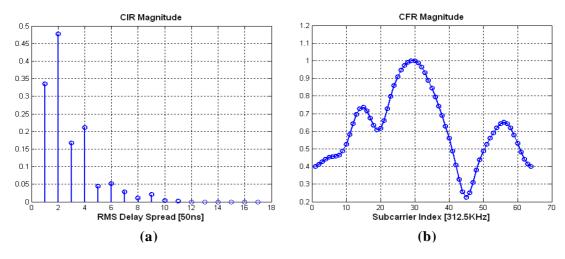


Figure 2.3.1 (a) CIR (b) CFR example of the multipath fading channel

In order to bring the practical time-variant characteristic to the applied channel model, the Doppler effect is modeled according to Jake's Doppler spectrum [15]. 50Hz Doppler frequency caused by 10 KM/hr opposite speed in 5GHz band is modeled for indoor

environments.

2.3.2 Carrier Frequency Offset Model

The sensitivity to carrier frequency offset (CFO) is one of the main drawbacks of the OFDM system. The orthogonal property of OFDM system is based on the perfect frequency carrier sampling. When transmission with CFO, the received data will be influenced by the data in other subcarriers, which is referred as ICI. The subcarriers spectrum can be shown in Figure 2.3.2.

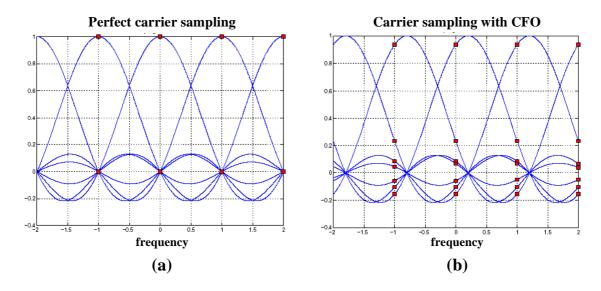


Figure 2.3.2 The received data (a) without CFO (b) with CFO (ICI)

CFO is caused by the radio frequency mismatch and the relative speed between a single transmitter and a receiver. The received data y(t) can be modeled as

$$y(t) = x(t) \cdot \exp\left(-j2\pi(f_1 + f_{\varepsilon} - f_2) \cdot t\right)$$
(2-2)

Where f_1 is the transmitter carrier frequency, f_2 is the receiver carrier frequency and f_{ξ} is the frequency shift caused by Doppler effect. From equation (2-2), CFO causes a linear phase shift in time-domain. From Moose's law [16], the linear phase shift in time-domain converts to the ICI in frequency-domain. The received data after FFT with CFO can be

derived as

$$Y(N,k) = e^{-j2\pi(N \cdot \ell_s) \cdot (\Delta f)T} \cdot (X(N,k) + ICI(N,k))$$
(2-3)

When CFO is smaller than 2ppm of the 5GHz, the ICI in frequency-domain can be neglected. The mean phase error will dominate the signal error. Figure 2.3.3 shows the frequency-domain signal distortion with CFO equal to 20ppm and 0.4ppm respectively.

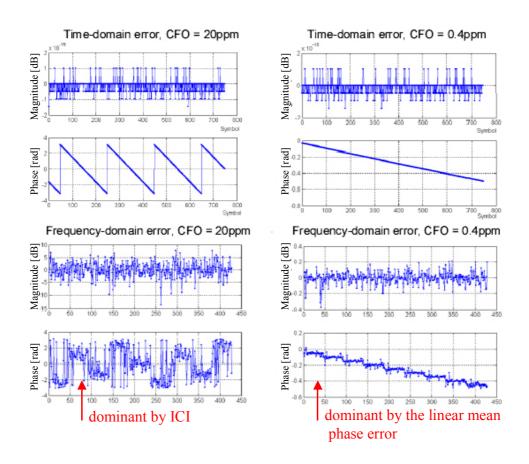


Figure 2.3.3 Signal distortions with CFO equal to 20ppm and 0.4ppm respectively.

2.3.3 Sampling Clock Offset Model

SCO is the sampling clock rate mismatch between the digital to analog converter (DAC) in transmitter and the analog to digital converter (ADC) in receiver. Because of the SCO, even if the initial sampling point is optimized, the following sampling points will slowly shift with

time. This shift in time-domain becomes a phase rotation in frequency-domain. The SCO model is established by the concept of oversampling with interpolation. Figure 2.3.4 shows the time-domain oversampled received data and the frequency-domain linear phase shift caused by SCO.

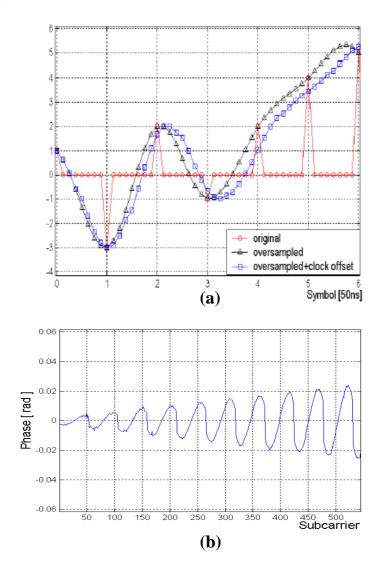


Figure 2.3.4 (a) the time-domain sampling offset (b) the frequency-domain linear phase shift

2.3.4 AWGN Model

The AWGN channel model is established by the random generator in Matlab. The output random signal is normally distributed with zero mean and variance equal to 1. The complex AWGN noise can be modeled as

$$w(t) = \left[\operatorname{randn}(1, \ell) + j \cdot \operatorname{randn}(1, \ell) \right] \cdot \sqrt{\frac{10^{\frac{P_s - SNR}{20}}}{2}}$$
 (2-4)

Where P_S is the data signal power, SNR is the signal to noise power ratio, and ℓ is the data signal length.



Chapter 3.

A Channel Equalizer Design for OFDM WLAN Systems

In this chapter, a high performance channel equalizer design for general OFDM WLAN systems is proposed. It includes a data-aided decision-directed channel estimation (DDCE) and a pilot-aided phase error tracking (PET). Data distortion caused by multipath fading, residual CFO, and SCO can be eliminated together with channel equalization.

3.1 Decision-Directed Channel Estimation

Multipath fading is one of the data distortion issues in OFDM systems. ISI occurs due to the multiple transmission paths between a single transmitter and a receiver. The channel frequency response (CFR) of the multipath environment is so called as a frequency-selective fading. In OFDM WLAN systems, zero forcing CE with known training sequence is generally applied to estimate the CFR and equalization is applied to remove the multipath influence. The zero forcing (ZF) channel estimation [9] and equalization can be derived as

$$H_A(k) = \frac{Y_L(k)}{X_L(k)} \tag{3-1}$$

$$X_{e}(k) = \frac{Y_{D}(k)}{H_{A}(k)}$$
 (3-2)

In equation (3-1), Y_L is the received training sequence, X_L indicates the defined (transmitted) training sequence and H_A is the estimated CFR. After CE, received data Y_D is

then divided by the estimated CFR to eliminate multipath fading as equation (3-2). Under the assumption of a packet-time invariant CFR in WLAN systems, CE is applied in the beginning of every packet and never updated until the next packet. However, CE error exists since the additive noise in training sequence and the time-variant characteristic in a practical wireless environment. The example of CE error between the estimated CFR and the real CFR can be shown in Figure 3.1.1.

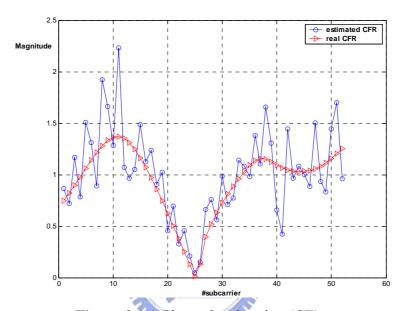


Figure 3.1.1 Channel estimation (CE) error

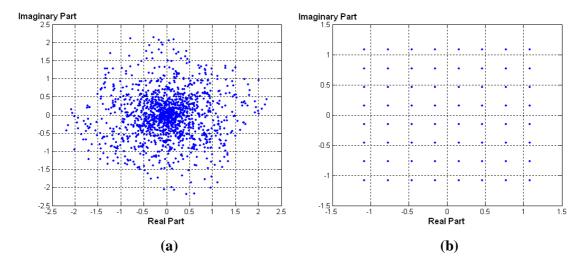


Figure 3.1.2 64QAM data constellation: (a)without CE (b)with CE

CE error makes equalization imperfect and degrades the system performance. Figure 3.1.2 shows the 64QAM data constellation distribution. Data is seriously distorted without CE or without an accurate CE.

A data-aided DDCE is proposed to enhance system performance. CE accuracy can be improved and equalization error can be reduced through this scheme. The block diagram of the proposed DDCE can be illustrated in Figure 3.1.3.

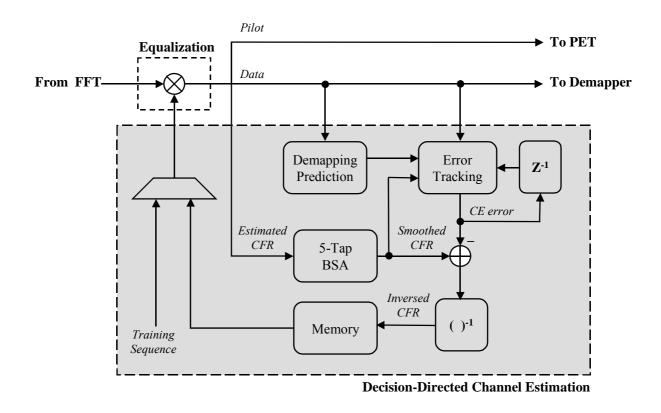


Figure 3.1.3 Block diagram of the proposed DDCE

This proposed DDCE consists of a basic ZF scheme, smoothed filter and a data-aided decision-directed error-tracking scheme. In the initial training sequence, CFR is estimated by the ZF scheme. The estimated CFR can be derived as equation (3-1). Based on the continuity of the real CFR, large variations between adjacent subcarriers can be taken as the influence of noise. Therefore, a smoothed filter is proposed to reduce additive noise. The estimated CFR is smoothed by a low pass filter after ZF. This smoothed filter is realized by a 5-tap bit-selection

and adder (BSA) without any multiplier in the concern of low complexity. The impulse response of this 5-tap BSA can be derived as

$$s(k) = \frac{\sum_{m=-2}^{2} \delta(k-m) \cdot e^{-|m|/2}}{\sum_{m=-2}^{2} e^{-|m|/2}}$$
(3-3)

After BSA, the smoothed CFR is inversed and stored to do later data equalization. If the data subcarriers are perfectly equalized, that is, multipath influence is completely removed and only additive noise influence left in the data constellation as shown in figure 3.1.4, the mean of data subcarriers should be exactly at the defined constellation point due to the zero mean additive noise. However, CE error exists and causes equalized data drift.

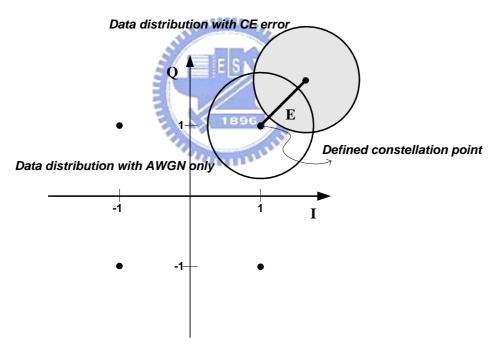


Figure 3.1.4 Data constellation distribution with and without CE error

Based on the idea mentioned above, a data-aided decision-directed error tracking is proposed to eliminate CE error further. The equalized subcarriers $X_e(k)$ with estimated CFR $H_A(k)$ can be shown as

$$X_{e}(k) = \frac{Y_{D}(k)}{H_{A}(k)} = \frac{X_{D}(k) \cdot H(k) + W_{i}(k)}{H_{A}(k)} = \frac{X_{D}(k) \cdot [H_{A}(k) - \Delta H(k)] + W_{i}(k)}{H_{A}(k)}$$
(3-4)

Where $Y_D(k)$ is the received data from FFT, H(k) is the real CFR and $W_i(k)$ indicates the frequency-domain additive noise in data subcarriers. The estimated CFR error $\Delta H(k)$ causes the de-mapping error vector between the equalized carrier $X_e(k)$ and the predicted de-mapping result $X_D(k)$. This error vector can be derived as

$$\varepsilon(k) = X_e(k) - X_D(k) = \left(\frac{-X_D(k) \cdot \Delta H(k)}{H_A(k)} + \frac{W_i(k)}{H_A(k)}\right)$$
(3-5)

For CE error estimation, the mean of normalized constellation error vectors is evaluated to eliminate the zero-mean additive noise $W_i(k)$. The mean of the normalized constellation error vectors during ℓ OFDM symbols is listed as

$$\frac{1}{\ell} \sum_{N=1}^{\ell} \varepsilon_{N}(k) \cdot X_{D,N}(k)^{-1} = \frac{-\Delta H_{N}(k)}{H_{A}(k)} + \frac{\sum_{N=1}^{\ell} W_{i,N}(k)}{H_{A}(k) \cdot X_{D,N}(k)}$$

$$\approx \frac{-\Delta H_{N}(k)}{H_{A}(k)} \qquad \ell \to \infty$$
(3-6)

Where N is the OFDM symbol number and k is the data subcarrier index. For a packet-invariant CFR, such an infinite average method can reduce the zero-mean noise efficiently. However, for a practical time-variant wireless channel, this method will eliminate channel variations as noise. Therefore, the tracking length β depends on the applied channel condition of different system specifications. The estimated channel error can be derived as

$$\Delta H_N(k) = \frac{1}{\beta} \sum_{N=N-\beta+1}^{N} -\frac{\varepsilon_N(k) \cdot H_A(k)}{X_{DN}(k)}$$
(3-7)

This loop filter can be realized as equation (3-8) to reduce computation complexity. Since $X_D(k)$ is the predicted de-mapping result, $X_D^{-1}(k)$ can be implemented by table-look-up (TLU) method with an inversed-defined-constellation-point table. N^{-1} and $(N-1) \cdot N^{-1}$ terms can

also be generated by look-up tables to avoid using high cost dividers.

$$\Delta H_N(k) = -\frac{\varepsilon_N(k) \cdot H_A(k)}{X_D(k)} \cdot \frac{1}{N} + \Delta H_{N-1}(k) \cdot \frac{N-1}{N}$$
(3-8)

The estimated CE error is then feedback to the channel equalization; update the estimated CFR in memory. The updated CFR can be listed as

$$H_{A,N}(k) \approx H_A(k) - \Delta H_{N-1}(k) \tag{3-9}$$

Because of $H_A(k)$ is updated every OFDM symbol, equation (3-8) can be modified as follows

$$\Delta H_N(k) = \left(\Delta H_{N-1}(k) - \frac{\varepsilon_N(k) \cdot H_{A,N}(k)}{X_D(k)}\right) \cdot \frac{1}{N} + \Delta H_{N-1}(k) \cdot \frac{N-1}{N}$$
(3-10)

Under this feedback loop, CE error can be compensated with the original channel equalization multiplier without any additional compensation scheme. Moreover, the accuracy of such a decision-directed based algorithm highly depends on the correctness of input data. That is, CE error is decreased before the decision-directed error tracking and later pilot-based PET in each data OFDM symbol; both tracking scheme accuracy can be enhanced under the proposed feedback scheme.

3.2 Phase Error Tracking

CFO and SCO are the other two data distortion issues in OFDM systems, which are mainly induced by crystal oscillator frequency mismatch and relative motion between transmitter and receiver. ICI occurs and causes received data distortion. Time-domain acquisition is generally applied to mitigate CFO, however, the residual CFO and SCO still cause data rotation in frequency-domain and make received data incorrect. PET is generally applied to trace the phase rotation; the constellation distortion can be recovered after PET compensation. Figure 3.2.1 shows the 64QAM data constellation with and without PET.

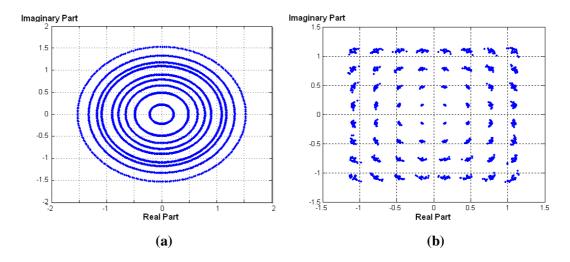


Figure 3.2.1 64QAM data constellation: (a) without PET (b) with PET

The time domain received data after preamble-aided synchronization can be listed as

$$y(n) = x(n)e^{-j2\pi n\Delta fT} = x(n)e^{-j2\pi\Delta fT(\lambda+1)}$$

$$\lambda = \frac{T'-T}{T}$$
(3-11)

Where Δf is the residual CFO after time domain CFO acquisition. T and T' are the sampling clock period of transmitter and receiver individually, λ is the relative SCO defined in [17]. The frequency domain received data subcarriers can be indicated as

$$Y_{N}(k) = \sum_{n=0}^{\ell-1} x(N \cdot \ell_{s} + n) \cdot e^{-j2\pi(N \cdot \ell_{s})\Delta f(\lambda+1)T} \cdot e^{-j\frac{2\pi k}{N}(N \cdot \ell_{s} + n)\lambda}$$

$$= e^{-j2\pi(N \cdot \ell_{s})\Delta f(\lambda+1)T} \cdot e^{-j\frac{2\pi k}{\ell}(N \cdot \ell_{s})\lambda} (X_{N}(k) + ICI_{N}(k))$$
(3-12)

 ℓ_s is the symbol length including GI, while ℓ is the number of FFT point. The second term is the influence caused by ICI and can be neglected if the remaining CFO is small enough. However, the remaining CFO and SCO will also bring the problem of increasing phase rotation with symbol index, which cannot be neglected even with a slight residual CFO and SCO. The phase rotation of the N_{th} OFDM symbol and k_{th} subcarrier can be derived as

$$\angle Y_N(k) = -2\pi (N\ell_s) \Delta f T(\lambda + 1) - \frac{2\pi k}{\ell} (N\ell_s) \lambda$$
 (3-13)

The first term is caused by residual CFO, which is a constant phase rotation within an OFDM symbol. This constant phase increases with OFDM symbol index. The second term is caused by SCO, which is a linear phase rotation with subcarrier index. The slope of this linear phase increases with OFDM symbol index. The phase rotation of subcarriers and OFDM symbol can be shown in Figure 3.2.2.

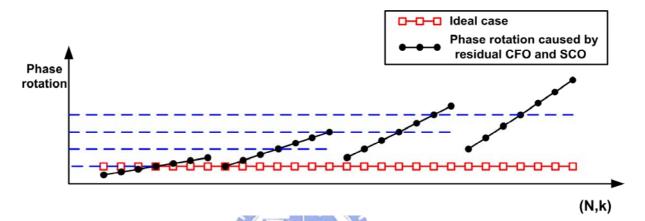


Figure 3.2.2 Phase rotation caused by residual CFO and SCO

The data flows of conventional PET approaches are shown in Figure 3.2.3. The conventional feedforward PET traces the pilot rotations without any pre-compensation. Data subcarriers are compensated with the present tracking information which has a high accuracy. However, the tracking value will be seriously miscalculated if the phase rotation exceeds $\pm\pi$. In IEEE 802.11a 6Mbits/s and 1Kbytes/packet transmission, the transmission time of one packet is 1.33ms. In general condition, residual CFO is more than 0.2ppm of the 5GHz RF frequency, maximum SCO is 40ppm, and the pilot phase error will arrive at $\pm 3.4\pi$ in such a 1.33ms transmission time. In this transmission process, the phase evaluation of this feedforward PET failed and seriously degraded the system performance. Moreover, data subcarriers have to be buffered under this feedforward scheme which costs additional registers. Different from the feedforward design, the conventional feedback PET traces pilot rotations with a pre-compensation in pilot which increases the phase tracking range. Data

subcarriers are compensated with the phase rotation of the previous symbol [12]. Tracking accuracy becomes very important in such a feedback scheme.

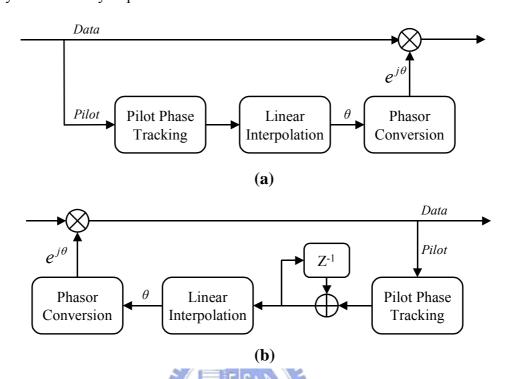


Figure 3.2.3 conventional (a) feedforward PET (b) feedback PET

To avoid phase evaluation failure and keep high accuracy in the full phase error range, a high accuracy PET with feedback compensation scheme is proposed. It comprises a tracking scheme and a prediction scheme. The block diagram can be illustrated in Figure 3.2.4. Pilot subcarriers are pre-compensated with the phase rotation of the previous OFDM symbol to increase the tracking range. The pilot pre-compensation can be derived as

$$P(N,k) = Y_P(N,k) \cdot \exp[-j(\phi_{N-1} + \gamma_{N-1} \cdot k)]$$
(3-14)

 ϕ is the mean phase error caused by residual CFO, and γ is the linear phase rotation caused by SCO. After pilot pre-compensation, the detected pilot phase is the difference between two adjacent OFDM symbols. The tracking scheme with fixed-coefficient loop filters can be listed as

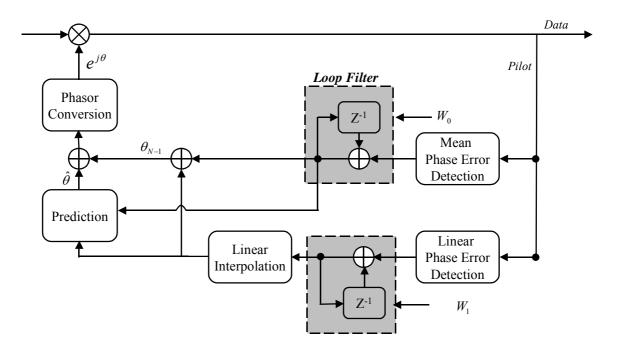


Figure 3.2.4 Block diagram of the proposed PET

$$\phi_{N} = W_{0} \cdot \frac{\sum_{i}^{M} \angle P(N, i)}{M} + (1 - W_{0}) \cdot \phi_{N-1}$$

$$\gamma_{N} = W_{1} \cdot \frac{\sum_{i}^{M} i \cdot \angle P(N, i)}{\sum_{i}^{M} i^{2}} \cdot k + (1 - W_{1}) \cdot \gamma_{N-1}$$
(3-16)

 ϕ_N is the total mean phase error, while γ_N is the total linear phase error of the N_{th} OFDM symbol. The first term of equation (3-16) is the linear regression method to find the slope of the curve, which has a minimum mean-square-error (MMSE) from it to the phase samples. A linear interpolation is then used to find the phase error of all subcarriers. The fixed-coefficient loop filters are applied to enhance tracking accuracy [18].

A prediction scheme is applied to estimate the increase rate C_0 of the mean phase error and the increase rate C_I of the linear phase slope. With the prediction scheme, data subcarriers are compensated with the present phase rotation completely. The joint phase rotation caused

by residual CFO and SCO can be modeled as a linear equation

$$\theta_{Nk} = C_0 \cdot N + C_1 \cdot N \cdot k \tag{3-19}$$

Applying linear regression method, C_0 and C_1 can be derived as

$$C_{0,N} = \frac{\sum_{i=1}^{N} i \cdot \phi_{N}}{\sum_{i=1}^{N} i^{2}}$$
 (3-20)

$$C_{1,N} = \frac{\sum_{i=1}^{N} i \cdot \gamma_{N}}{\sum_{i=1}^{N} i^{2}}$$
 (3-21)

It can predict the phase rotation of the present data subcarriers by the previous tracking information. Moreover, since the two coefficients will saturate to a stable value after a certain number of OFDM symbol (S), the tracking scheme of the PET can be stopped and compensation can be continued by this prediction scheme. The feedback compensation loop can be listed as

$$\widetilde{Y}(N,k) = Y(N,k) \cdot \exp[j\theta]$$
 (3-22)

$$\begin{cases} \theta = (\phi_{N-1} + C_{0,N-1}) + (\gamma_{N-1} + C_{1,N-1}) \cdot k & N = < S \\ \theta = (C_{0,S} + C_{1,S} \cdot k) \cdot N & N > S \end{cases}$$

Pilot subcarriers may be seriously distorted by additive noise and cause wrong estimations, especially in low SNR region. These wrong estimations will be accumulated in such a feedback loop. We proposed two check schemes to reduce the probability of making wrong estimations. First, compensation is conducted after getting a stable estimation by observing through a certain number of symbols. Moreover, we check the validity of estimations and accept estimations only when the phase of pilots in a symbol becomes converged.

Under the proposed PET, a full phase error tracking range can be achieved by the pilot pre-compensation scheme. Tracking and prediction accuracy can be enhanced by the proposed loop filters and check schemes. In hardware consideration, the compensation scheme can be combined with channel equalization. No additional data buffering or reordering registers, or individual compensators [18] are used in the proposed PET.

3.3 The Joint Channel Equalizer Design

After the in-depth discussion of DDCE and PET respectively, the operation of the proposed joint channel equalizer will be introduced in this section. The channel equalizer consists of the DDCE and PET, which eliminates all the three frequency-domain data distortion issues together with channel equalization. The architecture of the proposed channel equalizer can be illustrated in Figure 3.3.1.

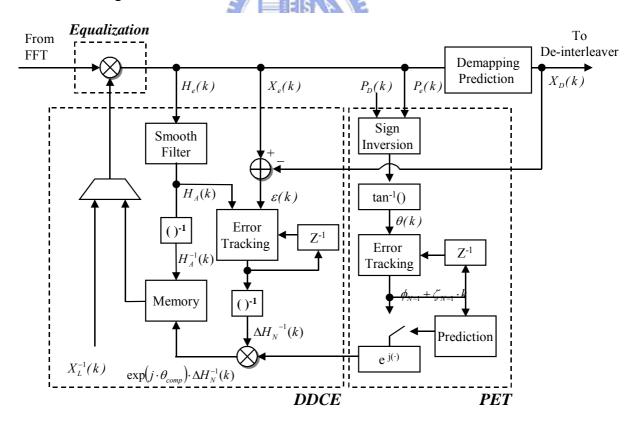


Figure 3.3.1 The architecture of the proposed channel equalizer

The operation can be listed as

- For initial CE, the inverse of the defined training sequence is selected to multiply with the received training sequence $Y_L(k)$.
- In low SNR region, a smoothed filter is applied to reduce the high noise in estimated CFR.
- In decision-directed error tracking, data subcarriers are used to trace the CE error by the difference between the received data $X_e(k)$ and the predicted de-mapping result $X_D(k)$.
- In PET, pilot subcarriers are used to trace the phase error by the information between the received pilot $P_e(k)$ and the defined pilot $P_D(k)$.
- A joint compensation value, which includes a more accurate CFR and the phase rotation due to residual CFO and SCO, is then feedback and update the original CFR in memory.

3.4 Summary

Under the proposed channel equalizer, multipath fading, residual CFO and SCO can be eliminated with channel equalization simultaneously. No additional compensators or buffering registers are required; therefore, low cost can be achieved. Moreover, both the tracking accuracy of DDCE and PET can be enhanced through such a feedback compensation loop. For low power consideration, tracking operations of DDCE and PET can be stopped after a certain symbol of evaluation, compensation can be continued by prediction.

Chapter 4.

A High Speed and Low Complexity Channel Equalizer for UWB System

In chapter 3, we proposed a channel equalizer for general OFDM-based wireless access systems. Based on this design, modifications can be made when dealing with different applications with particular requirements and specifications. In this chapter, a high-speed and low-complexity version channel equalizer is proposed for high-speed UWB systems.

4.1 Motivation

The trend of wireless communication is towards low power and high speed. UWB is the new generation wireless access system, which is designed for short-range personal area networks. The specified data rate is up to 480Mbits/s, which are almost ten times of the supported data rate in general WLAN systems. In the design of UWB systems, the concern of high speed and low complexity becomes extraordinarily important. In chapter 3, we proposed a high performance channel equalizer with a relatively low cost compensation scheme for general wireless access systems. However, the speed is limited by the complex divider (complex inverse + complex multiplier) for channel equalization; and the complexity reduction will be bounded in the different operation domains of DDCE and PET. Table 4.1 shows the gate count of the proposed channel equalizer and the 802.11a baseband processor for example.

Table 4-1 Gate count statistics of the 802.11a baseband transceiver

	Logic gate count	Memory (Byte)
802.11a transceiver	370K	3.3K
Channel equalizer	120K	0.56K

From Table 4.1, the channel equalizer occupied almost 1/3 of the total gate count, which will be the primary problem in UWB system. Therefore, the architecture of the proposed channel equalizer in chapter 3 must be improved based on the special specifications in the UWB standard draft to meet the high speed and low complexity requirement.

4.2 Coordinate Conversion

In Chapter 2, we have already introduced the UWB system. Unlike IEEE 802.11a PHY, only M-PSK is used for constellation. PSK is a digital modulation scheme, which has constant amplitude with various phase shifts for different possible signals. That is, phase information determines the constellation result, magnitude information can be neglected. Based on this idea, data can be converted from a complex value to a phase value only after FFT for later channel equalizer design and FEC. The coordinate conversion can be listed as

$$X(k) = A + j \cdot B = M \cdot e^{j\theta} \tag{4-1}$$

$$M = \sqrt{A^2 + B^2} \tag{4-2}$$

$$\theta = \tan^{-1} \frac{B}{A} \tag{4-3}$$

Where θ is the phase information for later designs, M is the magnitude information which can be neglected. A complex division in the Real-Imaginary coordinate will become a phase subtraction in the Magnitude-Phase domain. That is, the critical path can be reduced from the

complex division in the channel equalizer proposed in chapter 3, to a TLU-based arc-tangent for coordinate conversion. Moreover, the computation complexity of the proposed channel equalizer will be significantly reduced in the Magnitude-Phase domain. The detail algorithm will be explained in the next section.

For a complex value $A + j \cdot B$, the relationship between the phase result and the quadrant can be shown in Figure 4.2.1.

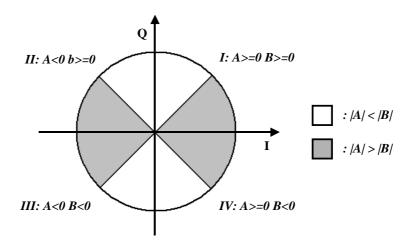


Figure 4.2.1 The phase result of different quadrants

$$\phi = \begin{cases} \tan^{-1} \frac{|B|}{|A|} & for |A| > |B| \\ \frac{\pi}{2} - \tan^{-1} \frac{|A|}{|B|} & for |B| > |A| \end{cases}$$

$$(4-4)$$

$$\theta = \begin{cases} \phi & for \quad I \\ \pi - \phi & for \quad II \\ -\pi + \phi & for \quad III \\ -\phi & for \quad IV \end{cases}$$

$$(4-5)$$

Where θ is the phase of $A+j\cdot B$. That is, only a 45° table (A>= B) should be established instead of the whole phase range form $-\pi \sim \pi$. The architecture of the conventional arc-tangent approaches can be divided into a direct-TLU method and a division-based TLU method [19]. In the direct-TLU design, all the phase results of A>=B are established in the

table. The table size will be significantly increased with the wordlength of the input value. In order to reduce the table size, a division-based TLU is applied to reduce the possible input pattern. The phase results of B/A are established as a look-up table instead of the results of A, B directly. However, a real divider is required in this method.

To achieve the high-speed and low-cost requirements of UWB system, a logarithm-based TLU method is proposed. The architecture can be shown in Figure 4.2.2. Based on the logarithm property in equation (4-6), the table size can be reduced and the real divider can be removed by a simple subtraction.

$$\log B - \log A = \log \frac{B}{A} \tag{4-6}$$

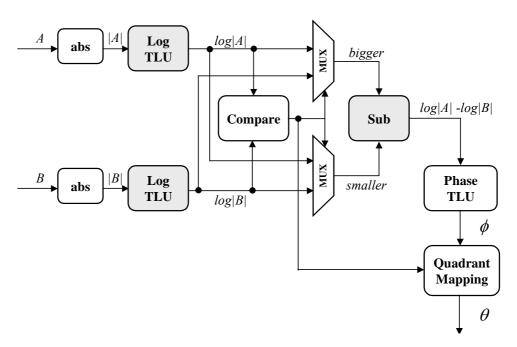


Figure 4.2.2 The architecture of the proposed logarithm-based TLU arc-tangent

4.3 The Proposed High-speed and Low-complexity Channel Equalizer Design

The block diagram of the channel equalizer proposed in chapter 3, and the channel

equalizer with coordinate conversion for the high-speed and low complexity requirement can be shown in Figure 4.3.1.

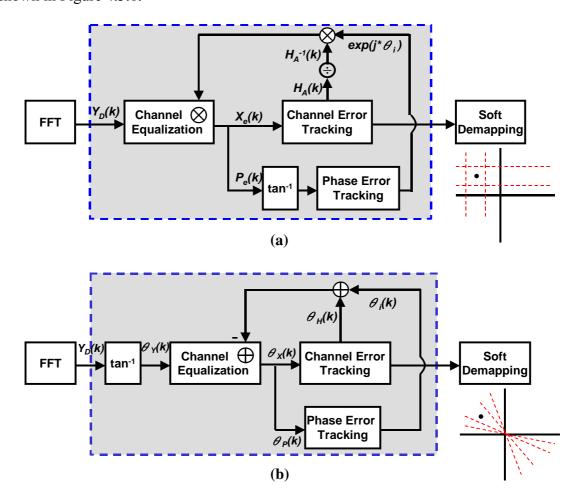


Figure 4.3.1 Block diagram of the proposed channel: (a) channel equalizer for general OFDM system (b) channel equalizer for PSK based OFDM system

All the complex number computations can be replaced by simple real number additions and subtractions by coordinate conversion. A soft de-mapping with phase variations is supported. This design can be used for PSK based OFDM wireless systems. For QAM systems such as IEEE 802.11a, the completely equivalent constellation map conversion from I-Q to magnitude-phase cannot be achieved. That is, another coordinate conversion from phase to complex number is applied after the channel equalizer for later de-mapping, which is even more complicated. The detail algorithm of the DDCE and PET with phase information only

can be discussed as follows.

In the original DDCE, which includes the basic ZF scheme and the data-aided decision-directed tracking loop, the estimated CFR is estimated by the division between the received training sequence and the defined sequence and is updated by the tracking loop. With coordinate conversion, the complex division can be replaced by a subtraction.

$$X_{e}(k) = \frac{Y_{L}(k)}{H_{A}(k)} \xrightarrow{Phase} \exp(j \cdot (\theta_{Y}(k) - \theta_{HA}(k)))$$
(4-7)

The decision-directed tracking loop can be modified as below

$$\frac{X_e(k)}{X_D(k)} = \exp(j \cdot (\theta_{Xe}(k) - \theta_{XD}(k)))$$

$$= \exp(j \cdot (\theta_{HA}(k) - \theta_H(k))) = \exp(j \cdot \Delta \theta_H)$$
(4-8)

$$\Delta \theta_{H,N} = \Delta \theta_{H,N-1} \cdot \left(1 - \frac{1}{N}\right) + \left(\Delta \theta_{H,N-1} + \Delta \theta_{H}\right) \cdot \frac{1}{N}$$

$$\theta_{HA,N} = \theta_{HA} - \Delta \theta_{H,N-1}(k)$$

$$(4-9)$$

$$\theta_{HA,N} = \theta_{HA} - \Delta \theta_{H,N-1}(k) \tag{4-10}$$

Where $\theta_{HA}(k)$ is the estimated CFR phase, $\theta_{H}(k)$ is the phase of real CFR, and $\Delta\theta_{H}(k)$ is the CFR error of the present OFDM symbol. After the applied loop filter, $\theta_{HA,N}$ is then feedback to the channel equalization to update the estimated CFR.

The algorithm of the PET is the same as the design proposed in chapter 3. From the equations listed above, both DDCE and PET operate in the phase coordinate. The complexity of DDCE is significantly reduced by the coordinate conversion. The phase error calculated by PET can be directly combined with the updated CFR and feedback to the channel equalization without any redundant conversion. The modified channel equalization which removes multipath fading, residual CFO and SCO all together can be listed as

$$\theta_{Xe} = \theta_{Y} - \theta_{He, N-1} - \theta_{PET} \tag{4-11}$$

In the 55Mbits/s \sim 200Mbits/s transmission modes, data are duplicated within an OFDM symbol, the data format can be shown in Figure 4.3.2.



Figure 4.3.2 Date format of a OFDM symbol in (a) 55Mbits~88Mbits/s

(b) 110Mbits/s~200Mbits/s transmission modes

An average method with the concern in deep fading subcarreirs is applied to reduce the additive noise in data subcarriers base on the duplicate property. In data transmission, some subcarriers may be seriously distorted by the deep fading problem in a frequency-selective-fading channel. The noise level of deep fading subcarreirs is much higher, resulting in affecting the correctness of other subcarriers in such an average method. Figure 4.3.3 shows the deep fading problem in CFR.

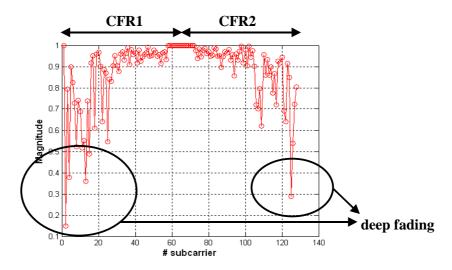


Figure 4.3.3 Deep fading subcarriers in frequency-selective-fading channel

Take 110Mbits/s~200Mbits/s for example, data are repeated in CFR1 and CFR2. If the power ratio of CFR1 and CFR2 is much smaller or bigger than "1", one of the subcarriers of the duplicated data is seriously distorted, which cannot be averaged. In order to keep the operation in phase domain, the geometric average method with the deep fading detect scheme is applied. The algorithm can be derived as follow.

$$X_{e}(k) = \sqrt{X_{e}(k) \cdot X^{*}_{e}(101 - k)} \qquad k = 1 \sim 50$$

$$\Rightarrow \frac{\theta_{Xe}(k) - \theta_{Xe}(101 - k)}{2} \qquad (4-12)$$

$$X_{e}(k) = \begin{cases} \frac{\theta_{Xe}(k) - \theta_{Xe}(101 - k)}{2} & for & th_{L} < |CFR1| / |CFR2| < th_{U} \\ \theta_{Xe}(k) & for & |CFR2| / |CFR1| < th_{L} & or & |CFR2| / |CFR1| > th_{U} \\ \theta_{Xe}(101 - k) & for & |CFR1| / |CFR2| < th_{L} & or & |CFR1| / |CFR2| > th_{U} \end{cases}$$

$$(4-13)$$

4.4 Summary

With the coordinate conversion from complex number to phase information only, a high-speed and low-complexity channel equalizer can be achieved. The critical path can be reduced from a complex division to a TLU-based arctangent. The computation complexity of the channel equalizer can be significantly reduced in the phase coordinate. The detail performance and complexity analysis will be discussed in the next chapter.

Chapter 5.

Simulation Result and Performance Analysis

In order to verify the proposed design, complete system platforms are established complaint to the IEEE 802.11a and the UWB proposal on Matlab. The platform has been introduced in chapter 2. CE accuracy, PET performance and the system PER of the proposed design will be simulated and compared with conventional approaches in the following performance analysis.

5.1 Performance Analysis of the Proposed Channel Equalizer for OFDM-based Wireless Systems

The proposed channel equalizer for general OFDM-based wireless systems is simulated in the system platform compliant to the IEEE 802.11a PHY. The PER analysis will focus on the 10% PER, which is the requirement in IEEE 802.11a standard.

5.1.1 Channel Estimation Accuracy Analysis

To analyze the CE accuracy of the proposed channel equalizer, mean-square-error (MSE) between the estimated CFR and the real CFR is measured. An exponentially decayed Rayleigh fading channel model with 50ns RMS delay spread is applied. The detail information of the applied channel model is in chapter 2. The MSE value of CE can be derived as equation (5-1), where H(k, n, p) is the real CFR and $H_e(k, n, p)$ is the estimated CFR updated each OFDM symbol n.

$$MSE(dB) = 10 \cdot \log_{10} \left[\frac{\sum_{p=1}^{P} \sum_{n=1}^{N} \sum_{k=1}^{K} \left(\left| H(k, n, p) - H_{e}(k, n, p) \right|^{2} \right)}{\sum_{p=1}^{P} \sum_{n=1}^{N} \sum_{k=1}^{K} \left| H(k, n, p) \right|^{2}} \right]$$
(5-1)

k is the number of data subcarriers, n is the total OFDM symbol number per packet, and p is the packet number per SNR conditions.

MSE Analysis with Packet-invariant multipath channel

The MSE curves of different CE designs measured in BPSK transmission with a packet-invariant multipath channel are shown in Figure 5.1.1.

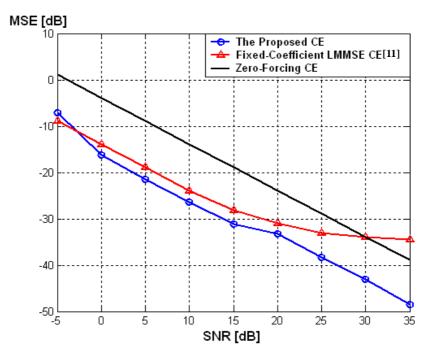


Figure 5.1.1 MSE analysis of the proposed design

The proposed DDCE achieves 9.0~13.0dB and 6.0~10.0dB gain in MSE compared with ZF CE and pilot-tracking CE [11] respectively. Compared with the fixed-coefficient LMMSE in [10], the proposed design achieves 2.0~13.9dB gain in MSE when SNR is higher than 0 dB. Applying the smoothed filter in low SNR region and the decision-directed tracking with data subcarriers in high SNR region, the proposed channel equalizer is robust to reduce CE error in

all SNR regions.

MSE Analysis with Packet-variant multipath channel

In order to verify the proposed design in a practical time-variant indoor wireless channel model, 50Hz Doppler frequency caused by 10 KM/hr opposite speed in 5GHz band is modeled. The MSE result under a packet-variant multipath channel with 50Hz Doppler frequency can be illustrated in Figure 5.1.2.

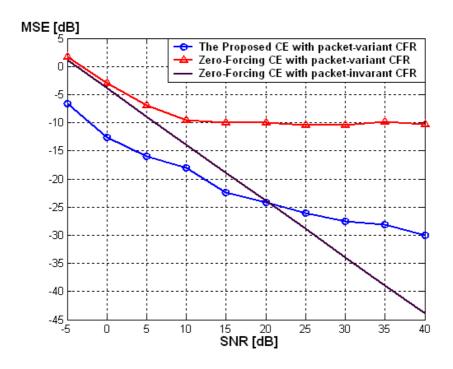


Figure 5.1.2 MSE analysis with packet-variant CFR

As Doppler frequency equals 0Hz, the MSE of ZF approach decreases as SNR increases. However, as Doppler frequency rose to 50Hz, the MSE of ZF approach is saturated since only CFR estimation in the initial preamble will lose the time-variant information of CFR. Appling the moving average algorithm to trace the channel variation, the proposed DDCE achieves better $8 \sim 20 \, \text{dB}$ gain in MSE estimation than ZF approach. It also achieves better $5 \sim 17 \, \text{dB}$ gain in MSE estimation than the conventional channel interpolator [11], which only achieves better $2.5 \sim 3 \, \text{dB}$ gain in MSE estimation than the ZF approach in the concern of a

time-variant CFR.

• PER Analysis with Different compensation scheme

For system performance analysis of the proposed DDCE with feedback compensation scheme, PER with different CE approaches in 54 Mbits/s transmission mode is simulated and illustrated in Figure 5.1.3. A feedforward compensation scheme [19] is compared with the proposed feedback compensation scheme. In the feedforward scheme, data subcarriers are compensated with the estimated equalization error instead of using a feedback loop to update the estimated CFR. The feedforward compensation can be shown as follows

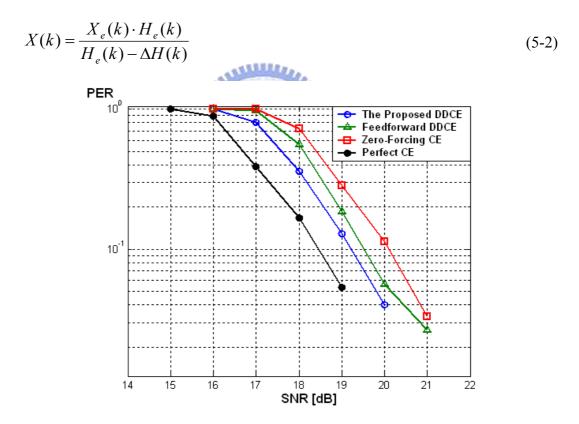


Figure 5.1.3 PER analysis of different CE approaches (54Mbits/s)

The proposed DDCE with feedback compensation scheme reduces 50% design SNR loss (0.9dB) compared with ZF CE, and reduces 25% design SNR loss (0.3dB) compared with a feedforward compensation scheme. The accuracy of such a decision-directed tracking

algorithm highly depends on the correctness of input data. That is, a feedback compensation scheme can reduce the CE error, which enhances the tracking accuracy. Compared with the feedforward scheme, the proposed feedback scheme enhances the CE accuracy and reduces compensation complexity by using the original equalization.

5.1.2 Phase Error Tracking Performance

In order to verify the PET performance, the design is simulated under 40ppm (200KHz) CFO and 40ppm (800Hz) SCO, which is the standard requirement. After the time-domain acquisition, the amount of the residual CFO is lower than 10KHz. This residual CFO and SCO cause the phase rotation in frequency-domain.

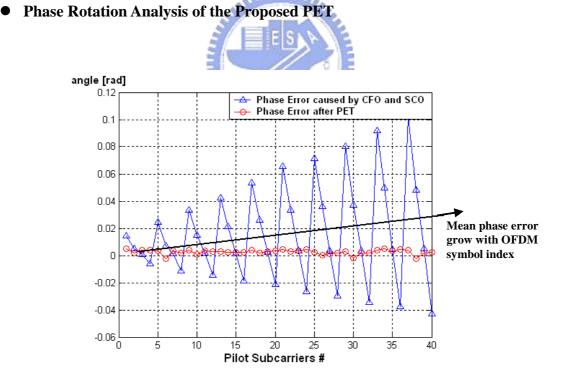


Figure 5.1.4 The phase deviation caused by residual CFO and SCO

The frequency-domain phase rotation can be divided into a mean phase error caused by residual CFO and a linear phase error caused by SCO. In Figure 5.1.4, both the mean phase

error and the increase rate of linear phase error grow with the OFDM symbol indexes. The phase error can be eliminated after the proposed PET. Figure 5.1.5 shows the frequency-domain phase rotation in the 6Mbits/s transmission mode. This phase deviation grows with the increasing of OFDM symbol indexes, which exceeds $\pm \pi$ within a packet. With the pilot pre-compensation in the proposed PET, the tracking range can be extended. That is, the proposed PET achieves a high accuracy in the full-phase range.

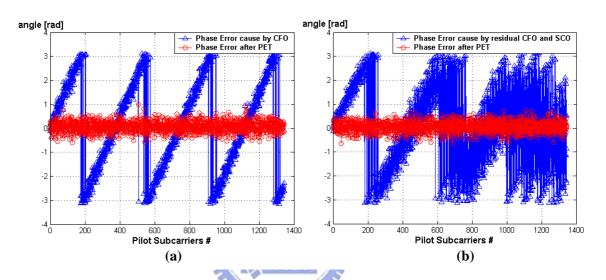


Figure 5.1.5 The phase deviation of (a) residual CFO (b) residual CFO and SCO

PER Performance of the Proposed PET

For performance analysis of the propose PET, PER is simulated with AWGN channel, 40ppm CFO, and 40ppm SCO. The applied CFO model includes the practical phase-noise effect. The PER curves of 6Mbits/s and 54Mbit/s with different PET approaches can be shown in Figure 5.1.6.

In the 54Mbits/s mode, the transmission time of one packet is 147.78us with maximum pilot error equals to $0.373\,\pi$. The feedfoward PET without pre-compensation will have a better PET accuracy compared with feedback scheme. However, in 6Mbits/s mode, the transmission time of one packet is 1.33ms, which is much longer than the 54Mbits/s mode. Since the phase error increases with time, the phase rotation will exceed π that causes phase

evaluation failed in the feedforward PET. With the proposed pilot pre-compensation which increases the tracking range, the fixed-coefficient loop filter which enhances tracking accuracy, and a predication scheme in data subcarriers, the proposed PET achieves a better 1.9~2.3dB gain in SNR compared with feedback PET.

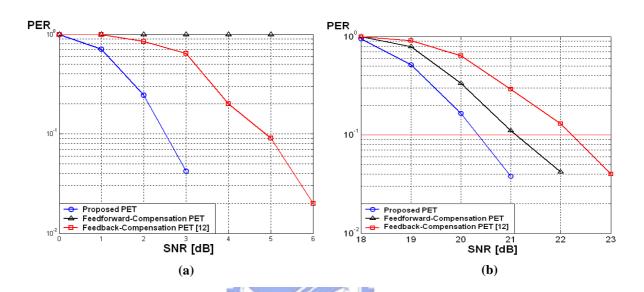


Figure 5.1.6 PER of PET approaches in (a) 6Mbit/s (b) 54Mbits/s transmission mode

5.1.3 System Performance

To verify the complete system performance of the proposed channel equalizer, PER of a complete IEEE802.11a baseband processor are measured with the typical indoor wireless channel model that contains 50ns multipath RMS delay spread, 40ppm CFO and 40ppm SCO. The PER curves of different transmission mode can be shown in Figure 5.1.7.

The design SNR for 10% PER is listed in Table 5-1. The proposed baseband system achieves 1.35~7.16dB average gain in SNR compared with standard requirement and current approaches [12] [13]. High performance can be achieved by the proposed channel equalizer with the combination of DDCE and PET. Both the tracking accuracy can be enhanced by the proposed feedback compensation loop with channel equalization.

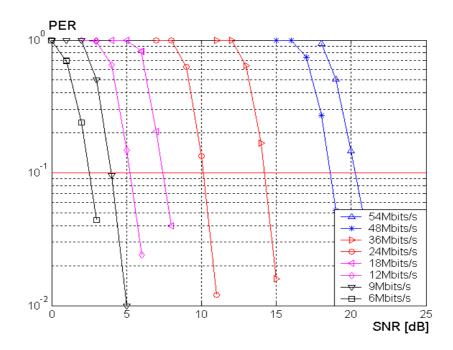


Figure 5.1.7 PER performance of the proposed baseband processor

Table 5-1 Required SNR for 10% PER of the proposed baseband processor

Data Rate (Mbits/s)	The Proposed Design SNR (dB)	Design SNR [12] (dB)	Design SNR [13] (dB)	IEEE 802.11a Requirement
6	2.5	4.9	5.4	9.7
9	4.0	5.8	5.8	10.7
12	5.2	8.6	7.0	12.7
18	7.4	9.9	9.5	14.7
24	10.1	12.4	11.3	17.7
36	14.2	15.9	14.9	21.7
48	18.6	20.3	18.6	25.7
54	20.3	21.7	20.6	26.7
Average gain		2.15	1.35	7.16

5.2 Performance Analysis of the Proposed Channel Equalizer for UWB System

To verify the proposed high-speed and low-complexity channel equalizer for PSK-based OFDM systems, a system platform complaint to the multi-band OFDM UWB PHY is established. System performance will focus on the 8% PER which is the requirement of the UWB system. Computation analysis with the proposed two channel equalizers will also be discussed.

5.2.1 PER performance of Different CE approaches

The PER performance of different CE approaches can be shown in Figure 5.2.1. The PER curves are simulated under a multipath fading channel with 5ns RMS delay spread.

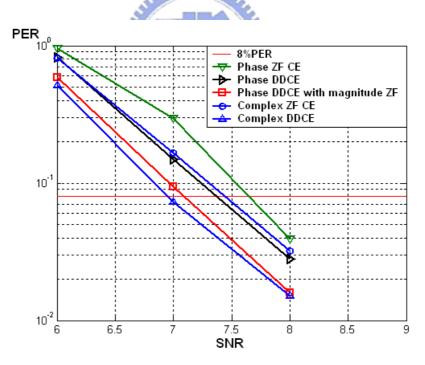


Figure 5.2.1 PER curves of different CE approaches in 480Mbit/s mode

The five different CE approaches are listed below.

• Phase ZF CE: Basic channel estimation with known training sequence, only phase

subtraction in equalization.

$$X_e(k) = \exp(j \cdot (\theta_Y(k) - \theta_{HA}(k))) \tag{5-3}$$

 Phase DDCE: The proposed high-speed and low-complexity DDCE, phase subtraction with updated CFR phase by the proposed tracking scheme.

$$X_{e}(k) = \exp(j \cdot (\theta_{V}(k) - \hat{\theta}_{H4N-1}(k)))$$
 (5-4)

Phase DDCE with complex ZF: Basic equalization with both magnitude and phase, only
 CFR phase updated by the tracking scheme.

$$X_{e}(k) = \frac{|Y(k)|}{|H_{A}(k)|} \exp(j \cdot (\theta_{Y}(k) - \hat{\theta}_{HA,N-1}(k)))$$
 (5-5)

• Complex ZF CE: Basic channel estimation with known training sequence, complex division for equalization.

$$X_e(k) = \frac{Y(k)}{H_A(k)}$$
 (5-6)

 Complex DDCE: The proposed DDCE for general OFDM systems, with updated complex CFR to reduce CE error.

$$X_e(k) = \frac{Y(k)}{\hat{H}_{N-1}(k)} \tag{5-7}$$

From Figure 5.2.1, we can discover that the proposed phase DDCE has a slightly better performance than the complex ZF CE. Performance can be maintained with the reduction in critical path and computation complexity under the proposed design.

5.2.2 Computation Analysis

In this thesis, we propose two channel equalizers. The first one is the high-performance

version for general OFDM system, the second one is the high-speed version for UWB system. The detail computation analysis of the proposed two channel equalizer versions with an 18Mbits/s packet (QPSK, coding rate = 3/4) in IEEE 802.11a system can be listed in Table 5-2. We can change the complex operations to equivalent number of real operations and calculate the reduced percentage of each operation.

1 complex division = 6*real multiplication + 2*real division + 4*real add/sub1 complex multiplication = 4*real multiplication + 2*real add/sub

Table 5-2 The computation analysis of the proposed two channel equalizers

	Proposed High-speed version	Proposed High-performance version	Complex ZF CE & PET
Complex multiplication	Online	22200	5772
Complex division	O ES N	5876	5876
Real multiplication	11544	11544	888
Real add/sub	27188 1896	11988	444

Table 5-3 The computation reduced percentage analysis

The same of the sa

	Reduce % from Proposed	Reduce % from complex ZF	
	High-performance version	& PET	
Real division	100%	100%	
Real multiplication	91.5%	80.5%	
Real add/sub	65.97%	23.4%	

Form Table 5-3, the proposed high-speed and low-complexity channel equalizer reduces 100% of the real divisions, 80.5% of the real multiplications and 23.4% of the real add/subs compared with conventional complex ZF CE and PET.

5.2.3 Deep Fading Analysis

The deep fading subcarriers are seriously distorted by additive noise. A geometric average method with a deep fading detection scheme is applied to enhance system performance. The CFR power ratio ($|CFR_{small}|/|CFR_{big}|$) of the duplicated subcarriers is measured. In figure 5.2.2, we can discover that the power ratios are close to "1" in correct packets. In error packets, the amount of small power ratios increase, deep fading may occur.

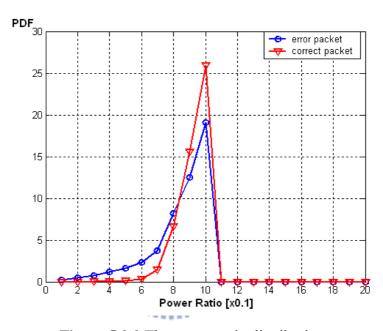


Figure 5.2.2 The power ratio distribution

We can decide the threshold value of deep fading subcarriers by the power ratio distribution. From figure 5.2.3, the proposed geometric average method with deep fading detection achieves a better 1.3dB gain in SNR than the general average method without deep fading detection.

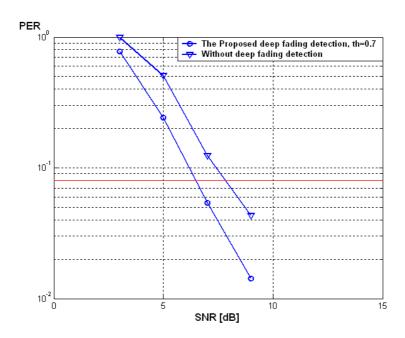


Figure 5.2.3 PER of the proposed deep fading detection (th=0.7)

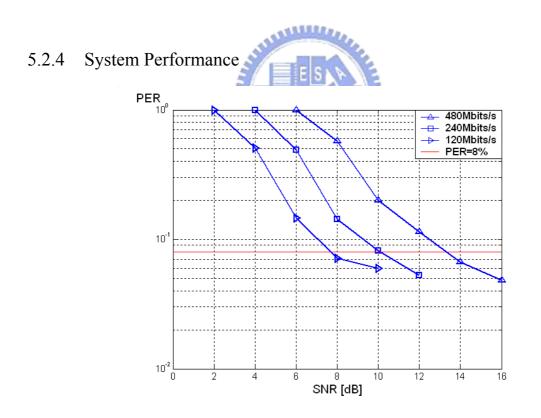


Figure 5.2.4 PER performance of the proposed UWB PHY

To verify the complete system performance of the proposed channel equalizer, a complete TFI-OFDM UWB PHY with a Low-Density Parity Check code (LDPC) in FEC is

established. LDPC achieves a better 2.0dB gain in SNR compared with the Viterbi encoder. There is no puncturing in the LDPC scheme, only a fixed coding rate (3/4) can be supported. Therefore, the supported data rates of the proposed UWB PHY are 120, 240, and 480Mbits/s. PER curves are simulated under Intel channel model with 5ns RMS delay spread, 40ppm CFO, and 40ppm SCO. The simulation result can be shown in Figure 5.2.4

5.3 Summary

In the performance analysis of the proposed channel equalizer for general wireless access systems, the proposed DDCE achieves (i) 9~13dB gain in MSE compared with ZF CE, (ii) 2.0~13.9 dB gain in MSE compared with fixed-coefficient LMMSE CE [10]. The proposed PET achieves a better 1.9~2.3dB gain in SNR for 10% PER compared with conventional approaches [5]. Applying the proposed channel equalizer, the IEEE 802.11a baseband processor achieves 1.35~7.16dB average gain in SNR compared with current approaches and the standard requirement [12][13].

In the proposed high-speed and low-complexity channel equalizer, the PER performance is slightly better than conventional complex ZF approach with less than 50% of the computation complexity. The proposed deep fading detect scheme achieves a better 1.0~1.3dB gain in SNR than the conventional blind average method. Applying the proposed channel equalizer with coordinate conversion, the requirement of performance, high-speed, and low-complexity can be achieved in the UWB baseband processor.

Chapter 6.

Hardware Implementation

In this chapter, we will introduce the platform based design flow. The architecture of the proposed design, hardware synthesis information and chip summary will be shown in the following sections.

6.1 Design Methodology

The trend of IC technology is towards to System-on-Chip (SoC). System-level simulation becomes very important in today's design flow. Our design methodology from system simulation to hardware implementation can be shown in Figure 6.1.1.

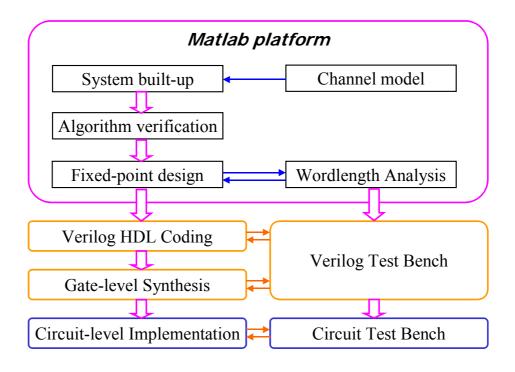


Figure 6.1.1 Platform-based design methodology

First, the system platform with channel modals should be established according to the system specification, which ensures the design in the practical condition. Algorithm and architecture developments of each function block should be verified in the system platform to ensure the whole system performance. Fixed-point simulation is applied before hardware implementation to make a trade-off between system performance and hardware cost. An example of the wordlength distribution analysis can be shown in Figure 6.1.2. Based on the signal distribution analysis and the PER simulation, a reasonable wordlength of each signal can be decided.

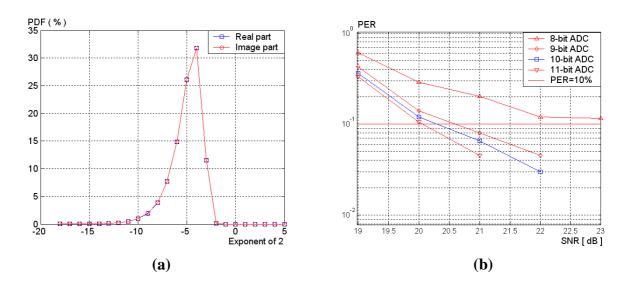


Figure 6.1.2 (a) Signal distribution analysis (b) PER analysis of different wordlength

In hardware implementation, the HDL modules are verified with the test benches dumped from the equivalent Matlab blocks to ensure the correctness.

6.2 The Channel Equalizer for OFDM-based Wireless Systems

6.2.1 Architecture of the Proposed Channel Equalizer

Figure 6.2.1 shows the hardware architecture of the proposed channel equalizer for general OFDM-based wireless systems.

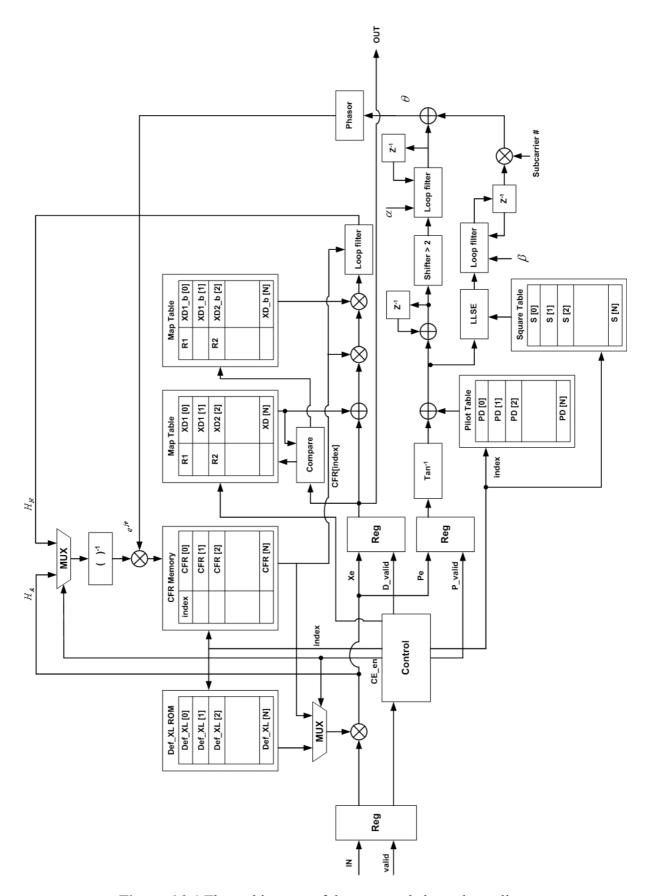


Figure 6.2.1 The architecture of the proposed channel equalizer

In the initial training sequence, CE_en is set to logic "1". CFR is calculated by the complex multiplication with the inverse of the defined training sequence "Def_XL[N]" and the received data. The estimated CFR $H_A[N]$ is saved in the CFR Memory. In normal OFDM symbols, CE_en is set to logic "0". Input data is equalized with the estimated CFR. After the equalization, data subcarriers are used for DDCE and pilot subcarriers are used for PET. In DDCE, the new estimated CFR is calculated by the difference between the received data and the predicted mapping result. A loop filter is applied to reduce the influence of AWGN. In PET, "tan⁻¹" is used for coordinated conversion. The mean phase error and the linear phase error are calculated with fixed-coefficient loop filters, which enhance the tracking accuracy. The estimated phase error θ is then converted to $e^{j\theta}$ and combined with the inverse of the new CFR to update the CFR memory for next symbol equalization.

6.2.2 Hardware Synthesis

In this section, we discuss the implementation of the DDCE with feedforward compensation scheme, which has been accepted in 2003 ISCAS [19]. The proposed high performance channel equalizer with feedback compensation loop presented in the previous chapters is the algorithm improvement based on this design.

Table 6-1 shows the synthesis result of the six-stage pipelined DDCE with feedforward compensation scheme in a 0.18um cell library with clock rate equals to 20MHz (Tapeout 2003. Aug). The combinational logic includes 4 complex multipliers, 4 real multipliers, and 6 real add/sub. The non–combinational (sequential) logic includes look-up tables, pipeline registers, and storage register files. The compensation complex divider is shared with the ZF CE, which is the critical delay. The synthesis result of the 22 bit complex divider can be shown in Table 6-2.

Table 6-1 The gate count of DDCE

	Gate count
Combinational	54296
Complex multiplier (14 bit)	8470
Non-Combinational	17756
Total	72052

Table 6-2 Gate count of the complex divider

	Gate count	Gate delay
Complex divider	38700	40ns

6.3 The High-Speed and Low-Complexity Channel Equalizer for UWB System

6.3.1 Architecture of the Proposed Channel Estimation

Figure 6.3.1 shows the hardware architecture of the proposed high-speed and low-complexity channel estimation. A 6-stage pipeline is applied for clock rate and data flow consideration. Applying the proposed design, the required throughput can be achieved by using only 2 parallel CE.

First, output from 4 parallel FFT are sampled with the double rate for CE design. Based on the CEP definition of UWB specification, the complex divider used for ZF CE can be implemented as a simple sign inversion. Because of the total CFR size is less than 0.4K Byte, register files are used to save the estimated CFR instead of using memory. Arc-tangent is then

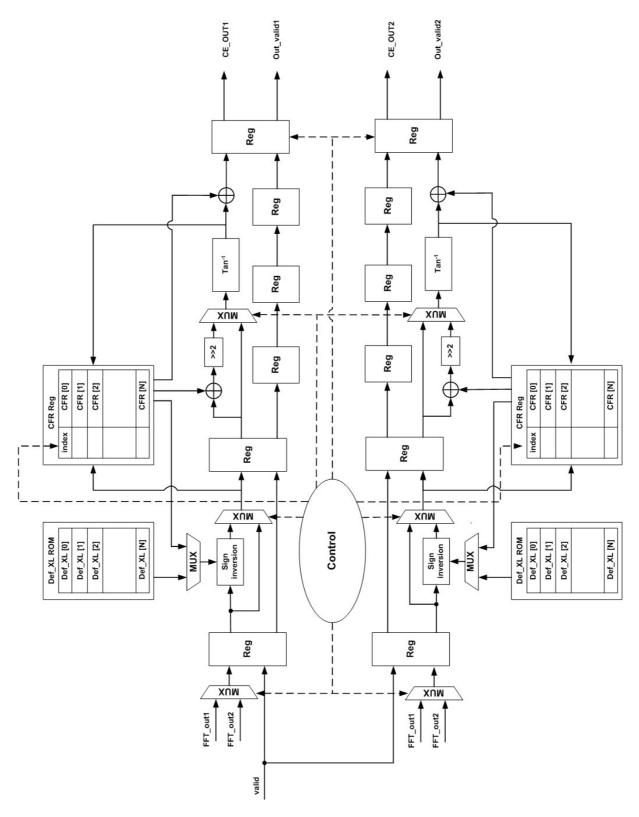


Figure 6.3.1 The architecture of the proposed CE

applied to make coordinate conversion form complex number to phase information only.

Therefore, channel equalization becomes a simple subtraction in phase domain. No complex

arithmetic blocks are used in the proposed design. Tables (multiplexers) and datapath registers will dominate the CE size. The critical path will be the arc-tangent design, which is 3-stage pipelined, and will be discussed in the next section.

6.3.2 Architecture of the Proposed Arc-tangent Design

Figure 6.3.2 shows the hardware architecture of the proposed logarithm TLU-based arc-tangent, which is used for coordinate conversion. In order to meet the clock rate requirement, a 3-stage pipeline is applied. Only real adder/subtraction are used in the proposed design. The gate count and critical time will be listed in the next section.

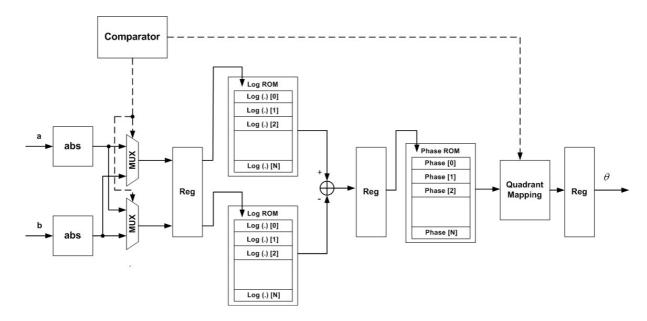


Figure 6.3.2 The architecture of the proposed logarithm TLU-based arc-tangent

6.3.3 Hardware Synthesis

Table 6-3 shows the hardware synthesis result of the proposed high-speed channel equalizer in a 0.18um cell library with successful gate-level simulation. With a parallel-two architecture, the throughput can reach 554Msymbol/s, which is higher than the requirement of UWB specification (528Msymbol/s).

Table 6-3 The gate count of the high-speed channel equalizer (parallel x2)

	Gate co	ount
Combinational	27884	
Non-Combinational	18305	
Arc-tangent	Combinational	1118
	Non-Combinational	315
	Total	1433
Total	46189	

The combinational logic includes look-up tables, multiplexers and simple real arithmetic blocks (add/sub). The sequential logic includes datapath and CFR registers. The critical delay is 3.41ns and has been successfully simulated with 277MHz (3.6ns) clock rate. Compared with the conventional complex ZF CE, it achieves more than ten times of the operation clock rate with only 50% of the hardware cost.

6.4 IEEE 802.11a Baseband Processor

Based on the proposed channel equalizer, OFDM modem and a low-power FEC design [20], a COFDM baseband processor compliant to IEEE 802.11a was designed using 0.18um standard CMOS process and tested completely. The microphoto is shown in Figure 6.4.1 with features listed in Table 6-4. The proposed low-cost baseband processor only contains 370K logic gates and 3.3Kbytes memory, which is less than the solution proposed in [21].

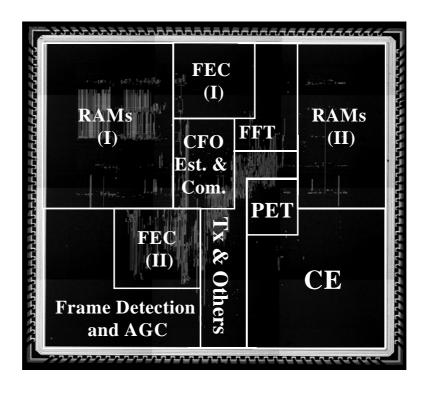


Figure 6.4.1 Microphoto of the test chip for 802.11a baseband processor.

Table 6-4 Chip Feature

Technique	0.18um CMOS, 1P6M
Transistan Count	2.1M
Transistor Count	(Include I/O)
Package	144-pin CQFP
Core Size	$3.8 \times 3.8 \text{ mm}^2$
Clock Speed	80MHz
Supply Voltage	1.8V Core, 3.3V I/O
Core power at 54Mbits/s (Tx/Rx)	52.4mW/123.5mW
I/O Power	61mW

6.5 UWB Basedband Processor

The gate-level simulation of the UWB baseband processor including the proposed high-speed channel equalizer has been completed. The developed UWB baseband processor comprises an OFDM modem and a LDPC FEC design, which provides three data rate including 120Mbits/s, 240Mbits/s and 480Mbits/s in 528MHz baseband bandwidth. The required 528Msymbol/s throughput of UWB specification can be achieved by using two parallel channel equalizers with operating clock rate equals to 264MHz. The gate count is 46.2K, which is 18.48% of the total gate count of the OFDM modem (250K).



Chapter 7.

Conclusion and Future Work

After the algorithm illustration and performance analysis, the proposed channel equalizer is robust to solve multipath fading, CFO, and SCO due to the algorithm improvement and the combination of DDCE and PET. In the proposed CE, equalization error can be reduced efficiently by the novel data-aided tracking algorithm. In the proposed PET, a full tracking range can be achieved by the pilot pre-compensation scheme, and the tracking accuracy can be enhanced by the applied fixed-coefficient loop filters. With the proposed design, multipath fading, CFO and SCO can be eliminated simultaneously with channel equalization, which reduces the compensation complexity compared with former approaches. In performance measurement, the proposed CE achieves (i) 9~13dB gain in MSE compared with zero forcing CE, (ii) 2.0~13.9 dB gain in MSE compared with fixed-coefficient LMMSE CE. The proposed PET achieves a better 1.9~2.3dB gain in SNR for 10% PET compared with conventional approaches. Applying the proposed channel equalizer, the IEEE 802.11a baseband processor achieves 1.35~7.16dB average gain in SNR compared with current approaches and the standard requirement.

A high-speed and low-complexity phase-domain channel equalizer is proposed based on the previous algorithm with coordinate conversion. The proposed modified algorithm can achieve the same performance of conventional zero forcing with lower than 50% of the computation complexity. In hardware implementation, the gate count is 46.2K with maximum throughput of 554Msymbol/s, which is 50% of the hardware cost, ten times of the operation clock rate compared with conventional zero forcing approach.

In this thesis, we focus on indoor OFDM-based wireless access systems. The applied wireless channel model is relatively simple compared with outdoor, mobile systems. In the future, OFDM may be widely applied in mobile communications, which has a much more complicated transmission environment. Therefore, a more robust synchronization scheme including channel estimation, CFO and SCO compensation will certainly be needed to further explore in the future.



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