

Appendix

Comparison between SRAM and DRAM

DRAM	SRAM
(a) The implementation of DRAM needs a special CMOS technology process.	(a) It can be designed with a general CMOS technology process.
(b) It needs to refresh data after every read data operation.	(b) It doesn't need to refresh data after every read data operation.
(c) Its control is complex.	(c) Its control is simple.
(d) It has a smaller area than SRAM.	(d) It has a larger area than DRAM.
(e) It is cheap.	(e) It is expensive.

From the above analysis, the control of SRAM is simple and we use SRAM to implement our FFT processor.