參考文獻

- [1] R.-B. Wu and F.-L. Chao, "Ladder wave in serpentine delay line," *IEEE Trans. Comp., Packag., Manufact. Technol. B*, vol. 18, pp. 664-650, NOV. 1995.
- [2] R.-B Wu and F.-L. Chao, "Flat spiral delay line design minimum crosstalk penalty," *IEEE Trans. Comp., Packag., Manu Technol B.* vol.19, pp. 397-402, May 1996.
- [3] B.J.Rubin and B.Singh, "Study of meander line delay in circuit boards," *IEEE Trans. Microwave Theory Tech.*, vol. 48, pp. 1452-1460,Sept. 2000.
- [4] O. M. Ramahi, "FDTD analysis of conventional and novel delay lines," *IEEE Antennas Propagat. Soc. Int. Symp.* 2000, pp. 1994-1997.
- [5] H. Lee and J. Kim, "Unit cell approach to full-wave analysis of meander delay line using FDTD periodic structure modeling method," *IEEE Trans. Comp., Packag., Manufact. Technol. B*, vol. 25,pp. 215-222, May 2002.
- [6] David M. Pozar, Microwave Engineering, 2nd Edition, John Wiley& Sons, Inc.1998.
- [7] Terry Edwards, Foundations for Microwave circuit Design, 2nd Edition John Wiley& Sons, Inc. 1991.
- [8] Vincent F. Fusco, Microwave circuits Analysis and Computer-aided Design, Prentice-Hall International. 1957.

- [9] Clayton R. Paul, Introduction to Electromagnetic compatibility John Wiley& Sons, Inc,1992.
- 〔10〕模擬軟體 Microwave OfficeTM 2000,Applied Wave Research ,網址:<u>http://www.mwoffice.com</u> .
- 〔11〕 CIC 課程講義 ADS Training Manual 2003.

