

Vita

Jiunn-Jye Luo was born in Hsinchu, Taiwan, Republic of China, in 1958. He received his B.S. degree in Physics from the Catholic Fu-Jen University in 1980, and M. S. degree in Electro-Optical Engineering from the National Chiao-Tung University in 1985. From 1985 to 1996, he joined the project to develop infrared detectors in Chung Shan Institute of Science and Technology (CSIST). He received the Ph. D. degree in Electronics Engineering at the National Chiao-Tung University in July, 2006. His research interests include process development and device physics in the infrared detectors and image sensors. The title of his Ph.D. dissertation is “Development of Small Pixel Infrared Detector for Mid-Wavelength IR Imaging Applications”.

