圖 目 錄

圖 1 (Figure 1) 4 The intersection of (2.5) and (2.6). The slope of L_1 is m_1 and the slope of L_2 is m_2 . (Figure 2) Partitioning parameter space for one-dimensional 圖 2 6 SD-RDE. 圖 3 (Figure 3) Attaching the two basic patterns, where $\bullet = +, \times, -.$ 11 The intersection of (2.5) and (2.6). The slope of L_3 12 圖 4 (Figure 4) is m_3 and the slope of L_4 is m_4 . (Figure 5) $g_1 \in B_i^{\bullet}$, where $i \in \{-4, -3, -2, -1, 0, 1, 2, 3, 4\}$ and 16 圖 5 \bullet = +, \times , -. Left and right boundary of basic patterns on T_{k-s} that 24 圖 6 (Figure 6) satisfy the Neumann boundary condition. 圖 7 Left and right boundary of basic patterns on T_{k-s} that (Figure 7) 25 satisfy the periodic and Dirichlet boundary conditions. Left and right boundary of basic patterns on T_{k-s} that (Figure 8) 圖 8 26 satisfy the Neumann boundary condition. 圖 9 (Figure 9) Left and right boundary of basic patterns on T_{k-s} that 26 satisfy the periodic and Dirichlet boundary conditions. 圖 10 (Figure 10) Left and right boundary of basic patterns on T_{k-s} that 27 satisfy the Neumann boundary condition. (Figure 11) Left and right boundary of basic patterns on T_{k-s} that 圖 11 28 satisfy the periodic and Dirichlet boundary conditions. 圖 12 (Figure 12) Left and right boundary of basic patterns on T_{k-s} that 28 satisfy the periodic boundary condition.

圖	13	(Figure 13)	Left and right boundary of basic patterns on T_{k-s} that satisfy the Dirichlet boundary conditions.	29
置	14	(Figure 14)	In I_3^1 , the pattern T_{k-2} is surrounded by two layers "×".	31
啚	15	(Figure 15)	Patterns in color.	31
啚	16	(Figure 16)	Patterns in color.	32
啚	17	(Figure 17)	Checkerboard with horizontal interface.	32
圖	18	(Figure 18)	Basic patterns of Fig. 17.	33
置	19	(Figure 19)	The values $\{\overline{u}_{i,j}\}_{1 \le i,j \le 7}$ in Example 5.1.	33
圖	20	(Figure 20)	Vertical and horizontal stripes with vertical interface.	34
圖	21	(Figure 21)	Basic patterns of Fig. 20.	34
圖	22	(Figure 22)	The values $\{\bar{u}_{i,j}\}_{1 \le i,j \le 8}$ in Example 5.2.	35
圖	23	(Figure 23)	Checkerboard with thin horizontal interface.	36
圖	24	(Figure 24)	Basic patterns of Fig. 23.	36
啚	25	(Figure 25)	Checkerboard with diagonal interface.	37
圖	26	(Figure 26)	Basic patterns of Fig. 25.	37
圖	27	(Figure 27)	Quad junction.	38
圖	28	(Figure 28)	Basic patterns of Fig. 27.	39