國　立　交　通　大　學

電子工程學系　電子研究所碩士班

碩　士　論　文

氧化銦錫擴散電流層厚度對氮化鎵發光二極體光電特性的影響

Influence on Optical and Electrical Characteristics of GaN-based Light-emitting Diodes by Varying the Thickness of Indium-tin-oxide Current Spreading Layers

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摘　　要

近年來，隨著氮化鎵發光二極體的發展，高透明度且電阻率小的氧化銦錫已經被廣泛的用作為透明電流擴散層。在此篇研究中，於Ｐ型氮化鎵上蒸鍍了不同厚度的氧化銦錫薄膜，並且製造成傳統的發光二極體，用以研究氧化銦錫薄膜和發光二極體元件的光電特性。我們發現氧化銦錫薄膜的片電阻值會隨著其厚度增加而降低，並影響到電流擴散能力和動態串接電阻值，氧化銦錫晶粒尺寸也和其厚度有關。另外，接觸電阻係數也受到在氧化銦錫和氮化鎵界面間氧含量的支配。然而，電流擴散長度或氧化銦錫厚度似乎都和量測到的元件亮度沒有關連，我們嘗試找出造成此種不一致的可能原因。

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ABSTRACT

With the development of nitride-based light-emitting diodes (LEDs) in the past year, indium-tin-oxide (ITO) has been widely applied as a transparent current spreading layer due to its high transparency and low electrical resistivity. In this research, ITO films with different thicknesses are evaporated on p-type GaN, and then the conventional GaN-based LEDs are fabricated. Optical and electrical characteristics of these ITO films and LED devices have been analyzed. We find that sheet resistance has an obviously decrease with adding the thickness of ITO film, which has effect on the current spreading ability and dynamic series resistance. The grain size of ITO is also corresponding to its thickness. In addition, the specific contact resistance is dominated by the oxygen proportion in the ITO/p-GaN interface. However, the measured light luminance of devices seems no relationship with the current spreading length, or ITO film thickness. We tend to find the possible reason for the discrepancy.

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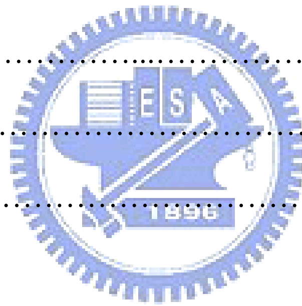

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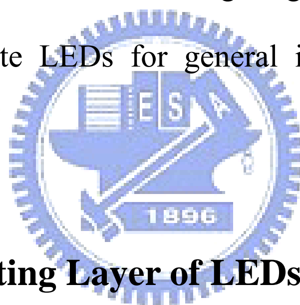
Chapter 1

Introduction

1.1 An Overview of GaN-based Light-Emitting Diodes

Group III-nitride semiconductors have attracted much attention for versatile applications because of their unique properties such as wide direct band gap, high thermal conductivity, and chemical stability. GaN-based materials have also been of great interest and play an important role in solid-state lighting in the green, blue and violet light regions [1-5]. Since the brightness of GaN-based light emitting diodes is increased, the application of LEDs is widened such as displays, traffic signals, backlight for cell phones, exterior automotive lighting and so on. Especially, there is a lot of speculation about white LEDs for general illumination by using the high brightness LEDs.



1.2 Transparent Conducting Layer of LEDs

Although the brightness of LEDs continues increasing, the total light output is still quite low compared to that of a conventional light source [6]. Most commercial GaN-based LEDs use an Mg-doped GaN layer as a p-type semiconductor [7-9]. However, the high activation energy (~180 meV) of Mg atoms leads to a low ionization rate. There is only 1% of Mg atoms can be ionized, and therefore the carrier concentration of p-type GaN is quite low and the poor conductivity will limit current spreading laterally. It is necessary to deposit a conductive layer on p-GaN for current spreading. This layer should not only form good ohmic contacts to p-GaN but also be transparent to the emitted light from the active layer. Considering the high work function of p-GaN, metals of high work functions such as Ni, Pd, and Pt were used to form p-type ohmic contacts [10-14]. The specific contact resistance around $10^{-2} \sim 10^{-6}$

$\Omega \cdot cm^2$ were obtained when the samples were annealed at temperatures of 400 ~ 600℃ in air. Thin Ni/Au films were deposited on Mg-doped GaN to act as transparent conductive layers in the conventional GaN-based LEDs structures. The mechanism of ohmic contacts formation is studied by Ho *et al*. The main concept is to use p-type NiO with nickel vacancies and/or oxygen interstitials contacts to p-GaN to reduce interface resistance. The high carrier concentration of NiO makes a metal easy to achieve an ohmic on it. However, the Au-based contacts generally have poor thermal stability, leading to poor device reliability. And it has also been reported that the transmittance of Ni/Au is only around 60% to 85% in the 450-550 nm wavelength [15]. The transmittance of conducting layer is important for the LEDs since photons generated in the active region will be partially absorbed by this layer. Although the thickness of Ni/Au can be reduced to increase the transmittance, the contact reliability could be a serious issue when the thickness becomes too small. If we could increase the conducting layer transmittance, we should be able to increase the LED output intensity. In other words, to achieve high performance GaN-based LEDs, it is required to reduce p-contact resistance and to enhance transmission efficiency of p-contact metal layer simultaneously. To reduce p-contact resistance, we have recently demonstrated GaN-based LEDs incorporated with and n^+-InGaN-GaN short-period-superlattice (SPS) tunneling contact layer which have low operating voltages [16, 17]. By growing such SPS structure on top of the p-GaN cap layer, one could achieve a good ohmic contact via tunneling when the n^+(InGaN/GaN)-p(GaN) junction was properly reverse biased. And to improve the transmittance of current spreading layer, it is feasible to replace the conventional Ni/Au contact by a better transparent conductive contact. Nowadays, transparent conductive oxide materials (TCO), such as aluminum-doped zinc-oxide (AZO) [18], gallium-doped zinc-oxide [19], cadmium-tin-oxide (CTO) [20], and indium-tin-oxide (ITO) [21], are applied to

optical electrical devices. Moreover, the ITO film has been widely used as a transparent conductor due to its high transparency (~90%) in the visible spectrum and its low electrical resistivity ($< 5\times10^{-4}\ \Omega\cdot cm$). The ITO film can also provide high carrier concentration (10^{20}-$10^{21}\ cm^{-3}$) and applicable mobility (25-50 $cm^2/V\cdot s$). All of these advantages render ITO an attractive material for fabricating GaN-based LEDs. However, a good ohmic contact can only be achieved when ITO is deposited on n-GaN, while a Schottky contact is formed when ITO is deposited instead on p-GaN. As a result, the operating voltages for GaN-based LEDs with ITO p-contacts were still too large for practical device applications. The other possible way is to use Ni/ITO. Previously, Horng *et al.* have reported that Ni/ITO could form good ohmic contact on p-GaN [22]. The Ni metal was utilized as an intermediate layer between ITO and p-GaN for the study of optimum ohmic contact. The ITO film not only provides the oxide source for NiO formation, but also preserves high conductivity and high transparency. By using Ni/ITO instead of ITO as the transparent conducting layer (TCL) in GaN-based LEDs, the lower forward voltage and higher reliability can be achieved [23-26]. This intermediate metal can be replaced by other materials. Song *et al.* have reported that using Ag/ITO as TCL exhibits greatly improved electrical characteristics, yielding specific contact resistance of $\sim10^{-4}\ \Omega\cdot cm^2$ [27].

1.3 Motivation

ITO has been widely used as TCL in the fabrication of GaN-based LEDs. However, the optimal thickness for high performance device is still not clearly known. In this study, we adopt different thicknesses of ITO films as TCLs and investigate the influence on optical and electrical characteristics of devices. In addition, the oxygen flow rate during ITO evaporations and post annealing temperature are also discussed. The thickness is set to be a quarter light wavelength as following,

$$t_{ITO} = \frac{\lambda}{4 \times n_{ITO}} \times m \quad (m = 1, 3, 5.....) \qquad \text{(Eq. 1-1)}$$

where λ, t_{ITO}, and n_{ITO} is the wavelength of the incident light, the thickness and refractive index of ITO, respectively. The calculated thickness is about 60 nm, 180 nm, and 300 nm when the factor *m* is chosen to be 1, 3, and 5, respectively. These thicknesses are designed to avoid additional light deprivation.

In our experiment, we used Cr-Au as the bonding pad. Chromium was employed for current blocking layer (CBL). In conventional designs, light generated under the opaque p-pad metal electrode is absorbed or reflected by the contact and lost. However, the Cr/p-GaN interface forms a schottky contact. In the CBL design, the area under the p-pad metal electrode is selectively given a relative high resistance, reducing current flow and light generation under the contact. Thus, the current normally passing through the CBL region is instead distributed over the useful area of the device. The similar ideas also appeared in other papers [28, 29].

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1.4 Thesis Organization

In chapter 1, a brief overview of LEDs and related developments were introduced.

In chapter 2, the fabrication process flow of LEDs, experimental recipes, and device parameter extraction methods will be described.

In chapter 3, we will show and discuss the characteristics of ITO film, ITO/p-GaN interface, and LED device, including sheet resistance, transmittance, specific contact resistance, I-V and L-I characterization.

In chapter 4, a short conclusion is addressed.

Finally, future works as well as suggestion for further research are given in chapter 5

Chapter 2

Experiment

2.1 The Process Flow of LEDs

The GaN-based LED epi-wafers used in this study were all grown on a 2-inch-diameter c-face sapphire (0001) substrates by metal-organic chemical vapor deposition (MOCVD) system. Details of the growth procedures could be found elsewhere [30, 31]. The epitaxial structure is consisted of 4-μm-thick n-type GaN layer, 0.1-μm-thick InGaN-GaN multiple-quantum-wells (MQWs), and 0.1-μm-thick p-type GaN layer. The carrier concentrations of the p-type and n-type GaN were 5×10^{17} and 3×10^{18} cm^{-3}, respectively. The as-grown samples were rapidly thermal annealed (RTA) at 800℃ for Mg-type activation. All wafers with a peak wavelength at 465 nm were chosen and cut into four pieces for different processes. One piece of each wafer was prepared for the conventional LEDs with Ni/Au (4nm/4nm) contact structure as a reference sample. The other pieces were prepared as experimental samples with ITO contacts of different thicknesses and annealed at different temperatures.

GaN-based LEDs (300 × 300 μm) with different conducting layers were fabricated. Figure 2-1 shows the cross section and process flow of LEDs. By means of photoresist lithography, the mesa region could be defined after removing photoresist. The surface of p-type GaN layer was partially etched by an inductance-coupled plasma (ICP) etcher until the n-type GaN layer was exposed. Then these samples were immersed in H_2SO_4 : H_2O_2 and NH_4OH : H_2O solutions to remove photoresist mask and residue chemical oxides. Next, ITO was deposited on samples by E-beam evaporator at 300℃ in oxygen ambience. The ITO source consisted of 95% In_2O_3 and 5% SnO_2. The thicknesses of ITO were 60 nm, 180 nm and 300 nm with different

oxygen flow rates, as listed in table 2-1. The base pressure was 5×10^{-6} torr and working pressure was 2.2×10^{-4} torr and 4.5×10^{-4} torr when oxygen flow rate was 15 sccm and 30 sccm, respectively. ITO was also deposited on double-side-polished GaN wafers and quartz substrates for transmission studies. The Ni/Au (4nm/4nm) contact layer was deposited by E-beam evaporator with a base pressure of 2×10^{-6} torr. After lithography process, these ITO and Ni/Au contact samples were wet etched by ITO etchant and KI : HCl solution, respectively. Then, all samples with different thicknesses of ITO films were subsequently annealed in nitrogen ambience for 10 minutes at 400℃, 500℃, and 600℃, respectively. As for the Ni/Au contact sample, it was annealed at 540℃ in nitrogen ambience to achieve the optimal ohmic contact to p-GaN. Finally, Cr-Au (0.1 μm/1.4 μm) was used as the n-type contact layer, p- and n-bonding pads. Cr was used for the CBL, as mentioned above.

After fabrication processes, the sheet resistance was measured using a four-point probe. In order to truly exhibit the effects of absorption, internal reflection and interference of the conducting films on GaN-based LEDs, the p-i-n GaN with a double-side-polished sapphire substrate was used in the transmittance measurement. The transmission spectra of the deposited films were measured by a Hitachi U3010 dual-beam spectrophotometer. Scanning electron microscope (SEM) was used to observe the grain size of ITO with different thicknesses.

Test keys consisting of transmission line model (TLM) patterns were used for calculating the specific contact resistance of the conducting layers on p-GaN and sheet resistance of p-GaN. After the sheet resistance and specific contact resistance of all samples were known, we created a calculation for checking the behavior of current distribution and calculating the current spreading length.

The current-voltage (I-V) characteristics were measured at room temperature using a HP4156A semiconductor parameter analyzer with a voltage source. The

relationship between LED output power and injection current (L-I) was also be measured by the integrated sphere detector from top of the devices.

The detailed fabrication process flow is listed as follows:

1. As-grown GaN wafer on (0001) sapphire substrate.
2. Initial cleaning (H_2SO_4 : H_2O_2).
3. Mask #1: Define mesa region.
4. Dry etching by ICP-RIE.
5. Photoresist removing and cleaning (H_2SO_4 : H_2O_2, NH_4OH : H_2O).
6. TCL evaporation (ITO with 60~300 nm and Ni/Au with 4/4 nm).
7. Mask #2: Define TCL region.
8. Etching TCL and removing photoresist.
9. TCL alloy for 10 minutes in furnace (400~600℃ for ITO and 540℃ for Ni/Au).
10. Mast #3: Define pad region.
11. P/N bonding pad metal evaporation (Cr-Au with 0.1/1.4 μm).
12. Removing metal by lift-off procedure.
13. P/N bonding pad alloy at 510℃ for 5 minutes in furnace for Ni/Au samples.

2.2 Methods of Measurements and Analysis

2.2.1 The Transmittance of ITO Film

Before every optical transmittance measurement by the Hitachi U3010 spectrophotometer, the wavelength calibration had been done to ensure the credibility of measured data. The transmittance of each film was normalized with respect to air, as shown in Fig. 2-2. The light passed through ITO film to the air, as same as the actual situation.

2.2.2 Transmission Line Model (TLM)

The transmission line model (TLM) was used for calculating the specific contact resistance of the conducting layers on p-GaN and sheet resistance of p-GaN. The pad size was 280×75 μm and the spacings between the pads were 10, 20, 30, 40, and 65 μm. A simple diagram of TLM pattern is shown in Fig. 2-3. We found the total resistance between any two contacts to be

$$R_T = \frac{R_S d}{Z} + 2R_C \approx \frac{R_S}{Z}\left(d + 2L_T\right) \qquad \text{(Eq. 2-1)}$$

where R_T and R_S is the total resistance and contact resistance of p-GaN, and we had used the approximation (The transfer length $L_T = \sqrt{\frac{r_c}{R_S}}$, r_c is the specific contact resistance). The total resistance was measured for various contact spacings d and R_T was plotted as function of d as illustrated in Fig. 2-4. Three parameters could be extracted from such a plot. The slope $\Delta(R_T)/\Delta(d) = R_S/Z$ led to the sheet resistance with the contact width Z independently measured. The intercept at d = 0 was $R_T = 2R_C$ giving the contact resistance. The intercept at $R_T = 0$ gives $-d = 2L_T$, which in turn could be used to calculate the specific contact resistance with R_S known from the slope of the plot.

2.2.3 Calculation of Current Spreading Length

After obtaining the sheet resistance of ITO film and the contact resistivity of ITO/p-GaN, we calculated the current distribution and defined current spreading length by creating an equivalent LED circuit [32-35]. The current distribution can be denoted by this equation

$$J(x) = J(0)\exp\left(\frac{-x}{\sqrt{(\rho_c + \rho_p t_p)\left|\frac{\rho_n}{t_n} - \frac{\rho_t}{t_t}\right|^{-1}}}\right) \quad \text{(Eq. 2-2)}$$

where J(0) is the current density at p-pad site and the parameters ρ_p, ρ_n, ρ_t, t_p, t_n, and t_t are the electrical resistivities and thicknesses of p, n, and transparent layer. Variable x represents the distance from p-pad. Denoting L_s, current spreading length, as the length where the current density has dropped to the 1/e value of the current density at the p-pad, i.e., $J(L_s)/J(0) = 1/e$, yields

$$L_s = \sqrt{(\rho_c + \rho_p t_p)\left|\frac{\rho_n}{t_n} - \frac{\rho_t}{t_t}\right|^{-1}} \quad \text{(Eq. 2-3)}$$

We will check if the current spreading length corresponds to device luminance later.

2.2.4 Current-Voltage Characteristic Measurement

The current-voltage (I-V) characteristics were measured at room temperature using a HP4156A semiconductor parameter analyzer with a voltage source. Ten devices of each sample were measured in order to choose a most reliable one to serve as the result. The relationship between LED output power and injection current (L-I) was also be measured by the integrated sphere detector from top of the devices. Each ITO sample was compared and normalized with Ni/Au sample which were cut from the same wafer to ensure a reliable result.

2.2.5 The Introduction of Scanning Electron Microscope (SEM)

Electron microscopy takes advantage of the wave nature of rapidly moving electrons. Where visible light has wavelengths from 4,000 to 7,000 Angstroms, electrons accelerated to 10,000 KeV have a wavelength of 0.12 Angstroms. Optical

microscopes have their resolution limited by the diffraction of light to about 1000 diameters magnification. Electron microscopes, so far, are limited to magnifications of around 1,000,000 diameters, primarily because of spherical and chromatic aberrations. Scanning electron microscope resolutions are currently limited to around 25 Angstroms, though, for a variety of reasons. A schematic diagram of a typical SEM is shown in Fig.2-5.

The scanning electron microscope generates a beam of electrons in a vacuum. That beam is collimated by electromagnetic condensor lenses, focussed by an objective lens, and scanned across the surface of the sample by electromagnetic deflection coils. The primary imaging method is by collecting secondary electrons that are released by the sample. The secondary electrons are detected by a scintillation material that produces flashes of light from the electrons. The light flashes are then detected and amplified by a photomultiplier tube.

By correlating the sample scan position with the resulting signal, an image can be formed that is strikingly similar to what would be seen through an optical microscope. The illumination and shadowing show a quite natural looking surface topography.

There are other imaging modes available in the SEM. Specimen current imaging using the intensity of the electrical current induced in the specimen by the illuminating electron beam to produce an image. It can often be used to show subsurface defects. Backscatter imaging uses high energy electrons that emerge nearly 180 degrees from the illuminating beam direction. The backscatter electron yield is a function of the average atomic number of each point on the sample, and thus can give compositional information.

Scanning electron microscopes are often coupled with x-ray analysers. The energetic electron beam - sample interactions generate x-rays that are characteristic of

the elements present in the sample. Many other imaging modes are available that provide specialized information.

2.2.6 The Introduction of HP4156A

HP4156A is an electronic instrument for measuring and analyzing the characteristics of semiconductor devices. This one instrument allows us to perform both measurement and analysis of measurement results.

HP4156A has four highly accurate source/monitor units (SMUs), two voltage source units (VSUs), and two voltage measurement units (VMUs). The HP4156A is designed for Kelvin connections and has high-resolution SMUs (HRSMUs), so HP4156A is especially suited for low resistance and low current measurements. We can measure voltage values with a resolution of 0.2 μV by using the differential measurement mode of VMUs.

HP4156A can perform stress testing. That is, can force a specified dc voltage or current for the specified duration.

Also, we can force ac stress by using pulse generator units (PGUs), which are installed in HP41501A SMU/Pulse Generator Expander. The HP41501A is attached to HP4156A, and can be equipped with a ground unit (GNDU), high power SMU (HPSMU), two medium power SMUs (MPSMUs), or two PGUs.

HP4156A can print and store, in addition to performing measurement and analysis. We can store measurement setup information, measurement data, and instrument setting information on a 3.5-inch diskette inserted into the disk drive of HP4156A. And we can print the setting information and measurement results on a plotter or printer that is connected to HP4156A.

HP4156A can be controlled by an external controller via HP-IB by using remote control commands. These commands are based on Standard Commands for

Programmable Instruments (SCPI), so we can easily develop measurement programs.

HP4156A has internal HP instrument BASIC, so we can develop and execute measurement programs by using the HP4156A only, without using an external controller.

Chapter 3

Results and Discussion

3.1 Properties of Films

3.1.1 Sheet Resistance of ITO Film

Figure 3-1 shows the sheet resistance of 180 nm and 300 nm thick ITO films which were evaporated on quartz substrates at two oxygen flow rates. When the flow rate is 30 sccm, we can find the sheet resistance decreasing after annealing at higher temperature. On the other hand, if the oxygen flow rate is 15 sccm, sheet resistance will have a maximum value after 500℃ annealing. The appearances are similar on different thicknesses of ITO films. It is well known that the oxygen content in ITO films is a critical control parameter of the resistivity of the film. Conduction is partially a result of unfilled oxygen vacancies, so a high incorporation of oxygen into the film will result in few vacancies and a highly resistive film [36]. When annealing at higher temperature, the oxygen atoms may easily evaporate to the air. It will increase the amount of oxygen vacancies and attribute to a better conductivity of ITO film. Figure 3-2 shows the relationship between ITO film thickness and the sheet resistance. By any means, the sheet resistance decreases while increasing the thickness of ITO film due to a higher conductivity, as our preconception. The measured sheet resistances of all ITO films are listed in table 3-1.

3.1.2 Scanning Electron Microscope (SEM) Analysis

Figure 3-3 shows SEM pictures of ITO films with different thicknesses. ITO films were observed to be polycrystalline and showed a similar crystal structure to that of undoped In_2O_3. The grain size is about 70 nm, 120 nm, and 160 nm for 60 nm, 180 nm, and 300 nm ITO film, respectively. These pictures indicate that the grain size

of the films increased with growing film thickness. In the thin film growth, it is generally observed that the grain size increases with increasing film thickness [37, 38]. The larger grain size results in a lower density of grain boundaries, which behave as traps for free carriers and barriers for carrier transport in the film [39]. Hence, an increase in the grain size can cause a decrease in grain boundary scattering, which leads to an increase in the conductivity. It may be one reason of why thicker ITO film got a smaller sheet resistance.

3.1.3 Transmittance of ITO Film

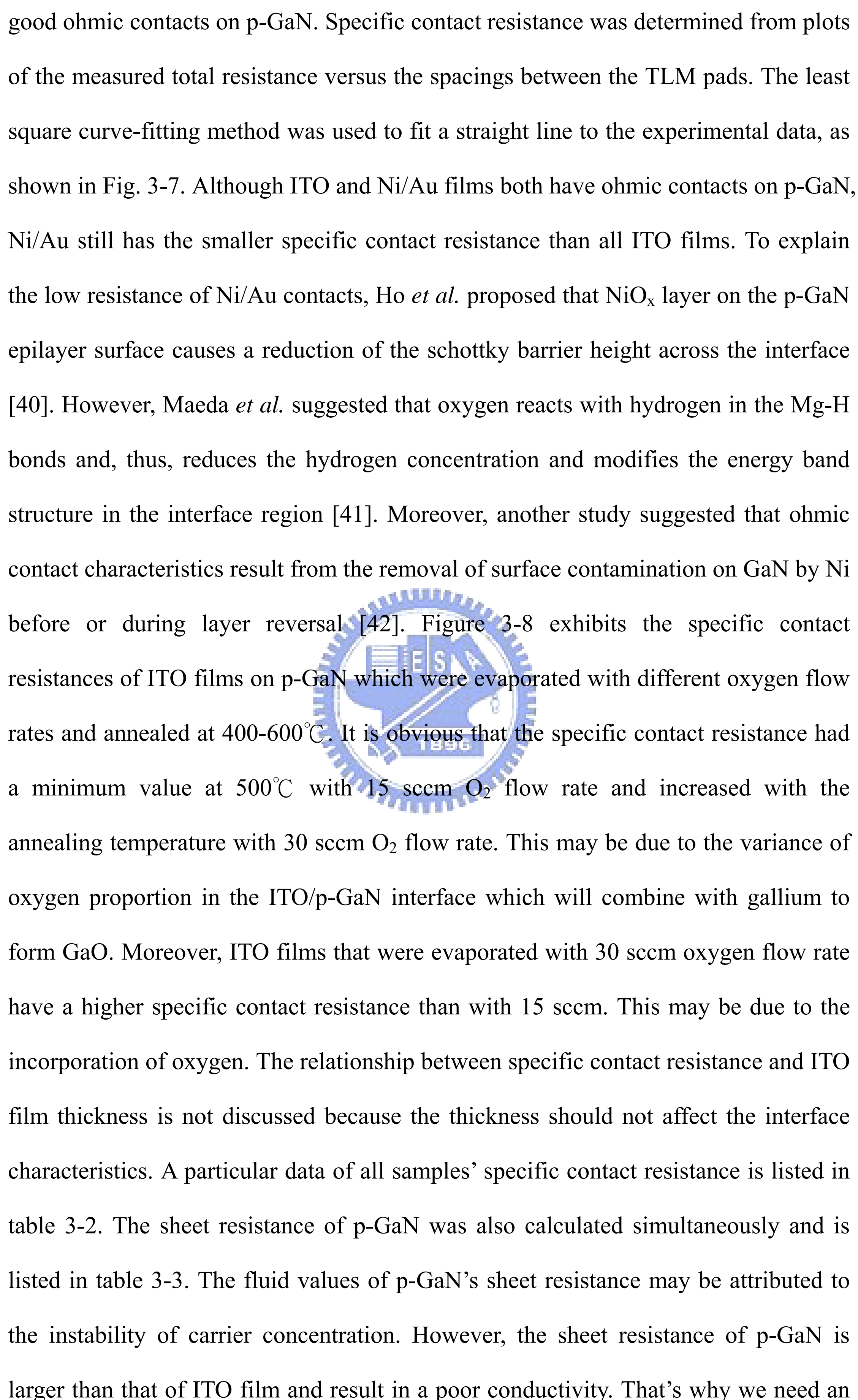
Figure 3-4 shows the optical transmittance as a function of wavelength of the evaporated ITO film on GaN substrate with different annealing temperatures. In this figure, the transmittance of each film was directly compared with air. By varying the annealing temperature, the transmittances are almost the same. However, the curves were long-wavelength shifted after annealed at higher temperature. It means an increase of the refractive factor of ITO film. The transmittance of different ITO film thicknesses are showed in Fig. 3-5. For comparison, the most commonly used Ni(4 nm)-Au(4 nm) contact layer that was annealed at 540℃ in nitrogen ambience was also examined. The transmittances are about 80% at a wavelength of 465 nm of all thicknesses of ITO films. Compared with Ni/Au film, transmittance of ITO has an enhancement of about 20%.

3.2 Interfacial Characteristics

3.2.1 Specific Contact Resistance of ITO or Ni/Au Layer on p-GaN

Figure 3-6 shows the current-voltage (I-V) characteristics of Ni/Au (4 nm/4 nm) and ITO (300 nm) contacts on p-GaN after annealing at temperatures of 400-600℃ for 10 minutes in N_2 ambient. It was found that both ITO and Ni/Au films could form

good ohmic contacts on p-GaN. Specific contact resistance was determined from plots of the measured total resistance versus the spacings between the TLM pads. The least square curve-fitting method was used to fit a straight line to the experimental data, as shown in Fig. 3-7. Although ITO and Ni/Au films both have ohmic contacts on p-GaN, Ni/Au still has the smaller specific contact resistance than all ITO films. To explain the low resistance of Ni/Au contacts, Ho *et al.* proposed that NiO_x layer on the p-GaN epilayer surface causes a reduction of the schottky barrier height across the interface [40]. However, Maeda *et al.* suggested that oxygen reacts with hydrogen in the Mg-H bonds and, thus, reduces the hydrogen concentration and modifies the energy band structure in the interface region [41]. Moreover, another study suggested that ohmic contact characteristics result from the removal of surface contamination on GaN by Ni before or during layer reversal [42]. Figure 3-8 exhibits the specific contact resistances of ITO films on p-GaN which were evaporated with different oxygen flow rates and annealed at 400-600℃. It is obvious that the specific contact resistance had a minimum value at 500℃ with 15 sccm O_2 flow rate and increased with the annealing temperature with 30 sccm O_2 flow rate. This may be due to the variance of oxygen proportion in the ITO/p-GaN interface which will combine with gallium to form GaO. Moreover, ITO films that were evaporated with 30 sccm oxygen flow rate have a higher specific contact resistance than with 15 sccm. This may be due to the incorporation of oxygen. The relationship between specific contact resistance and ITO film thickness is not discussed because the thickness should not affect the interface characteristics. A particular data of all samples' specific contact resistance is listed in table 3-2. The sheet resistance of p-GaN was also calculated simultaneously and is listed in table 3-3. The fluid values of p-GaN's sheet resistance may be attributed to the instability of carrier concentration. However, the sheet resistance of p-GaN is larger than that of ITO film and result in a poor conductivity. That's why we need an



additional transparent conducting layer upon p-GaN.

3.2.2 Current Spreading Behavior by Calculation

After the sheet resistances and specific contact resistances were known, we started the current spreading calculation. The calculated current distribution of the LED is shown in Fig. 3-9. As the results by calculation, we can conclude that thicker ITO film contributes to a uniform current spreading phenomenon. This instance is chiefly caused by the smaller sheet resistance of thicker film. It can be seen that current crowding can be alleviated via the use of a thicker ITO film. For simple expression, we define current spreading length Ls as the length where the current density has dropped to the 1/e value of the current density at the p-pad. Figure 3-10 shows the calculated current spreading length of different thicknesses of ITO films that were evaporated with 15 sccm oxygen flow rate. This figure indicates that no matter at what annealing temperature, current will spread farther by increasing ITO film's thickness due to the lower sheet resistance. Figure 3-11 shows Ls of 300 nm ITO films which were evaporated with different oxygen flow rates. With a 30 sccm oxygen flow rate during evaporation, sample annealed at 600℃ has a longest Ls compared with other temperatures because of its largest specific contact resistance. The large specific contact resistance makes the current tend to flow horizontally. Same theory can also explain the shortest Ls at 500℃ with 15 sccm oxygen flow rate by having the smallest specific contact resistance. Ls of ITO samples are listed together in table 3-4.

3.3 Electrical and Optical Properties of Devices

3.3.1 Reverse Current-Voltage Characteristic of LEDs

Not only forward voltage was applied, reverse voltage was also used to check the

value of leakage current, as shown in Fig. 3-12. The leakage current is only several nA which is small enough as our toleration, and not affected by annealing temperature. The fabricated LEDs' leakage currents are generally small enough except two wafers. Figure 3-13 points out the high leakage current of these devices with 180 nm ITO film evaporated with 15 sccm oxygen flow rate and annealed at 400℃. It may be caused by the bad epitaxy property of GaN wafer and will result in the output power's degradation and a serious thermal effect.

3.3.2 Forward Current-Voltage Characteristic of LEDs

Figure 3-14 depicts the room-temperature forward current-voltage characteristics of the fabricated GaN-based LEDs. These samples were all annealed at 400℃. In Fig. 3-14(a), the 20-mA forward voltage measured from LEDs with 60 nm ITO, 180 nm ITO, 300 nm ITO, and Ni/Au on p-GaN was 3.45, 3.41, 3.32, and 3.14 V, respectively. Apropos of LEDs with films which were evaporated with high oxygen flow rate, the forward voltage was 3.5, 3.31, 3.26, and 3.08 V, respectively, as shown in Fig. 3-14(b). The forward voltage decreases with an increase of ITO film's thickness. As the specific contact resistance doesn't form an order, the forward voltage may be affected by sheet resistance, or conductivity of TCL. We can read from the figure that Ni/Au film still has the smallest V_f due to its corking conductivity. Figure 3-15 shows the comparison of forward voltage with different annealing temperature. In Fig. 3-15(a), the 400℃-annealed sample's large forward voltage is attributed to the large specific contact resistance. Similarly, in Fig. 3-15(b), the forward voltage is also affected by the specific contact resistance.

3.3.3 Dynamic Resistance and Series Resistance of LEDs

Figure 3-16 shows forward voltage versus dynamic resistance (dV/dI) in LEDs

with different thicknesses of ITO films annealed at 400℃. The dynamic resistance decreases while the voltage increases because more and more free electron-hole pairs are generated. Values at the tail of the curves are close to their own series resistance. It can be seen that the series resistance is in association with the thickness of ITO film. The approximated series resistances are listed in table 3-5. All series resistances have a decrease with thicker ITO films. While the ITO's thickness is fixed, annealing temperature almost has no influence on the series resistance. Thus, we can conclude that the series resistance is mainly dominated by sheet resistance. Hence, Ni/Au film's small series resistance is reasonable due to its good current spreading ability.

3.3.4 Luminance-Current Characteristic of LEDs

Figure 3-17 presents the normal light output as a function of injection current for fabricated LEDs with 300 nm ITO film annealed at different temperatures. Devices with ITO film are all brighter than that with Ni/Au film. Figure 3-18 shows the luminance enhancement as a function of injection current for LEDs with different thicknesses of ITO films. The ITO samples were compared and normalized with Ni/Au samples which were cut from the same wafer to ensure a reliable result. We could achieve a factor of about 1.3-1.5 times luminance enhancement by the difference of optical transmittance between ITO and Ni/Au films. Notice that the enhancement of devices with 60 nm ITO film has a decrease when the injection current increasing. On the other hand, devices with 300 nm ITO film don't have such a decrease. It is supposed that because the thin ITO film has a heavier thermal effect than thick ITO film and Ni/Au film. The heavy thermal effect is attributed to the poor conductivity which forms a large resistance. Figure 3-19 presents the luminance enhancement with different ITO films at an injection current of 20-100 mA. No matter what the evaporating oxygen flow rate is, the thermal effect is serious with thin

(60 nm) ITO films while thick (300 nm) ITO films almost remain the enhancement factor in constant. Normal luminance of all ITO samples at different injection current is shown in Fig. 3-20. From Fig. 3-13, we have known that the leakage current in condition B is extremely high, especially in the 400℃ annealing one. It will cause the degradation of output light, as shown in Fig. 3-21. The high density of black dots in Fig. 3-21(a) is caused by the high leakage current. Hence, the circled data in Fig. 3-20 may not be accurate. In addition, it seems that the luminance of each device doesn't form a regular relationship. Figure 3-22 shows the relationship between calculated current spreading length and forward voltage or normal luminance. From Fig. 3-22(a), we find that L_S decreases while the forward voltage increasing, where the straight line is an approximation line fitted by least square method. This may be resulted by the specific contact resistance. A large r_C will cause a long current spreading length and a small 20-mA forward voltage. However, in Fig. 3-22(b), it seems that the optical performance is not corresponding to current spreading length. A possible reason is that most of the current spreading lengths are larger than the device size (300 × 300 μm), so almost all devices have a uniform enough current distribution. Figure 3-23 shows the current converting efficiency (dL/dI) of LEDs as a function of injection current. When the injection current becomes larger, a decrease of the converting efficiency could be discovered. It may be a reason of why the luminance has no relationship with L_S. Although thicker ITO film has longer current spreading length, the increment in light output is not as much as in the current density. Then, such declined light enhancement may be disrupted because the thickness of evaporated ITO film might not be exact enough as our settings.

Chapter 4
Conclusion

In this experiment, we have fabricated GaN-based LEDs with different thicknesses of ITO films and also found the optical and electrical characteristics of them. First, we observe that the sheet resistance of ITO films on quartz has a decrease when the films grow thicker, which indicates a better conductivity. However, the sheet resistance is also dominated by the annealing temperature due to the change of the amount of oxygen vacancies and grain size of ITO film. From SEM pictures, we can discover that the grain size of ITO film has an obvious increase with its thickness. Next, the transmittances of these ITO films are all about 80% at a wavelength of 465 nm and have 20% larger than Ni/Au films.

Moreover, the specific contact resistance increases with the annealing temperature, which is attributed to a thin GaO layer formed in the ITO/p-GaN interface. The calculated current distribution and current spreading length are affected by sheet resistance and specific contact resistance, especially the sheet resistance. A small sheet resistance, or a thick ITO film, demonstrates a good current spreading ability. Some of the calculated current spreading lengths are even larger than the size of LED device. The thickness of ITO film is also corresponding to the operating voltage due to the variance of conductivity. However, the film thickness seems no relationship with the normal light output. Compared with the references, all LEDs with different ITO films have a factor of about 1.3-1.5 times luminance enhancement by the difference of optical transmittance. By generalizing the above results, the best thickness of ITO film can be 300 nm.

Chapter 5

Future Work

In the future work, the similar experiment for other TCL structures with an intermediate metal layer, such as Ni/ITO and Ag/ITO, will be carried out. Larger size LEDs can even be fabricated because the current spreading length of a thick ITO film can be in excess of 1000 μm. In addition, after finding the best thickness of ITO film, it will be combined with surface roughening technique to achieve higher light extraction efficiency. This may help fabricating LEDs with an additional luminance enhancement.

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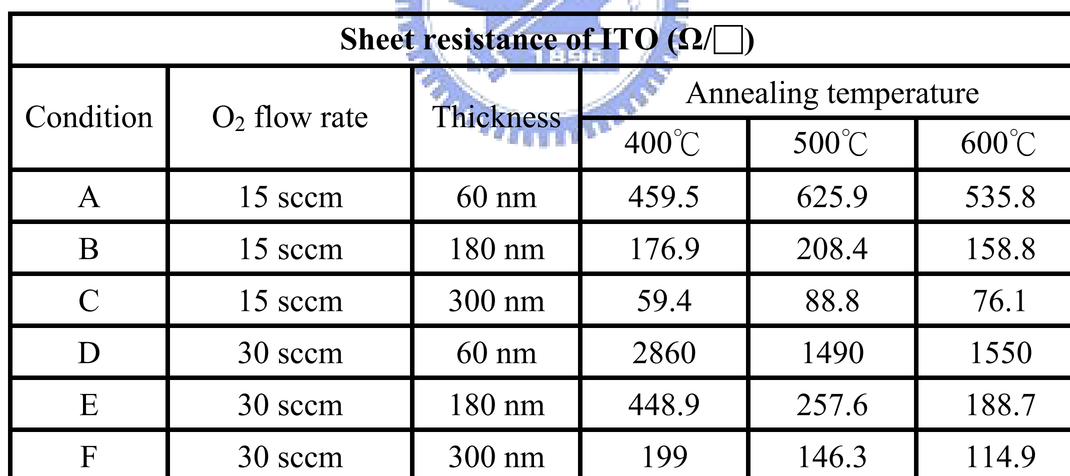
Condition	Oxygen flow rate	ITO thickness
A	15 sccm	60 nm
B	15 sccm	180 nm
C	15 sccm	300 nm
D	30 sccm	60 nm
E	30 sccm	180 nm
F	30 sccm	300 nm

Table 2-1. Settings of evaporation parameters.



Sheet resistance of ITO (Ω/□)					
Condition	O_2 flow rate	Thickness	Annealing temperature		
			400℃	500℃	600℃
A	15 sccm	60 nm	459.5	625.9	535.8
B	15 sccm	180 nm	176.9	208.4	158.8
C	15 sccm	300 nm	59.4	88.8	76.1
D	30 sccm	60 nm	2860	1490	1550
E	30 sccm	180 nm	448.9	257.6	188.7
F	30 sccm	300 nm	199	146.3	114.9

Table 3-1. The list of sheet resistance of all ITO samples.

Condition	O_2 flow rate	Thickness		Annealing temperature 400℃	500℃	600℃	Ni/Au
A	15 sccm	60 nm	**r_C of ITO/p-GaN (Ω•cm^2)**	0.0636	0.0642	0.0685	0.0088
			Error (%)	14.62	12.39	11.9	79.45
B	15 sccm	180 nm	**r_C of ITO/p-GaN (Ω•cm^2)**	0.1238	0.1307	0.1320	0.0412
			Error (%)	4.19	10.5	14.47	100.48
C	15 sccm	300 nm	**r_C of ITO/p-GaN (Ω•cm^2)**	0.3818	0.1725	0.232	0.0139
			Error (%)	2.66	5.49	13.25	11
D	30 sccm	60 nm	**r_C of ITO/p-GaN (Ω•cm^2)**	0.3858	0.2737	0.8921	0.0109
			Error (%)	1.87	11.9	5.88	46.39
E	30 sccm	180 nm	**r_C of ITO/p-GaN (Ω•cm^2)**	0.2737	0.4169	0.9112	0.0080
			Error (%)	12.47	11.47	12.23	97.27
F	30 sccm	300 nm	**r_C of ITO/p-GaN (Ω•cm^2)**	0.2768	0.3255	0.3965	0.0060
			Error (%)	5.46	19.28	6.8	52.97

Table 3-2. Specific contact resistance of all ITO and Ni-Au films on p-GaN.

Condition	O_2 flow rate	Thickness		Annealing temperature			Ni/Au
				400℃	500℃	600℃	
A	15 sccm	60 nm	**Rs of p-GaN (kΩ/□)**	761	787	971	983
			Error (%)	1.04	0.87	0.78	1.93
B	15 sccm	180 nm	**Rs of p-GaN (kΩ/□)**	999	895	1602	899
			Error (%)	0.35	0.96	1.02	5.39
C	15 sccm	300 nm	**Rs of p-GaN (kΩ/□)**	381	661	864	349
			Error (%)	0.56	0.65	1.61	0.55
D	30 sccm	60 nm	**Rs of p-GaN (kΩ/□)**	414	699	487	592
			Error (%)	0.38	1.71	1.5	1.6
E	30 sccm	180 nm	**Rs of p-GaN (kΩ/□)**	653	466	413	600
			Error (%)	1.84	6.4	3.52	2.88
F	30 sccm	300 nm	**Rs of p-GaN (kΩ/□)**	822	796	1235	862
			Error (%)	0.73	2.81	0.89	1.14

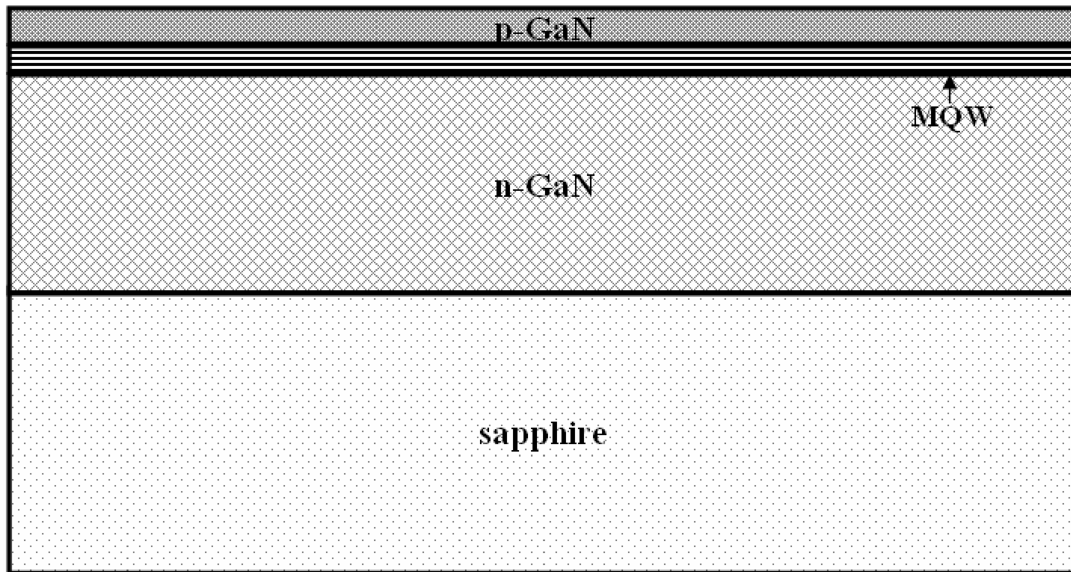
Table 3-3. Sheet resistance of p-GaN of all samples.

Ls (μm)					
Condition	O_2 flow rate	Thickness	Annealing temperature		
			400℃	500℃	600℃
A	15 sccm	60 nm	120.4533	103.236	115.415
B	15 sccm	180 nm	285.7622	266.282	314.143
C	15 sccm	300 nm	1049.698	532.695	673.864
D	30 sccm	60 nm	121.7222	135.264	226.454
E	30 sccm	180 nm	268.255	517.967	644.056
F	30 sccm	300 nm	391.0211	517.653	618.622

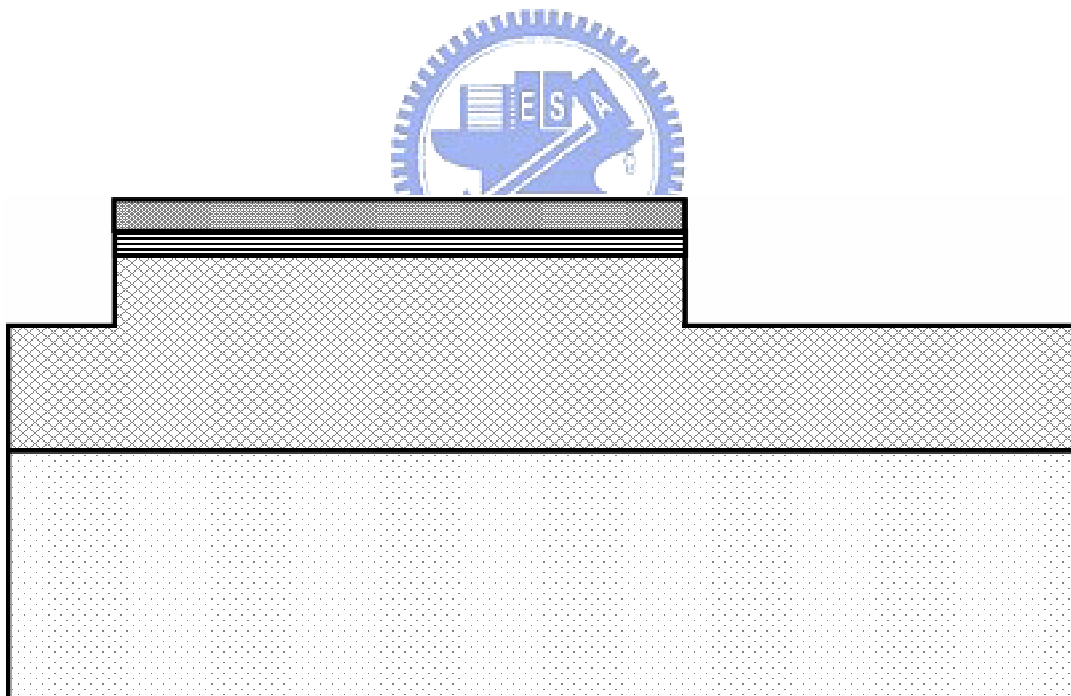
Table 3-4. Simulated current spreading length of all ITO samples.

Dynamic series resistance (Ω)						
Condition	O_2 flow rate	Thickness	Annealing temperature			Ni/Au
			400℃	500℃	600℃	
A	15 sccm	60 nm	11.93	11.79	11.31	9.53
B	15 sccm	180 nm	10.17	10.66	10.39	10.11
C	15 sccm	300 nm	9.86	10.44	9.87	9.87
D	30 sccm	60 nm	12.86	12.27	12.26	9.45
E	30 sccm	180 nm	11.02	10.44	10.75	9.24
F	30 sccm	300 nm	10.04	9.8	9.84	9.67

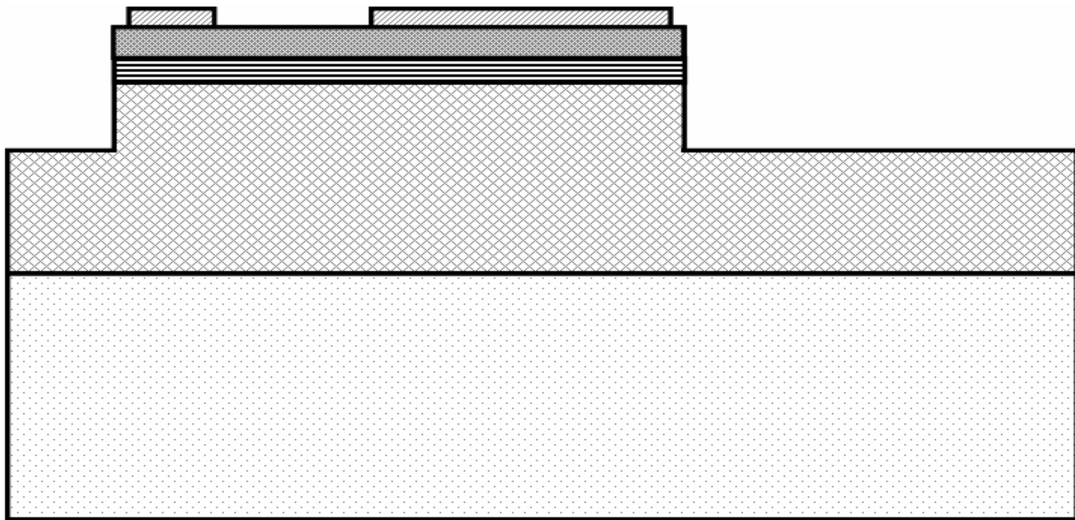
Table 3-5. Approximated series resistance of fabricated LEDs.



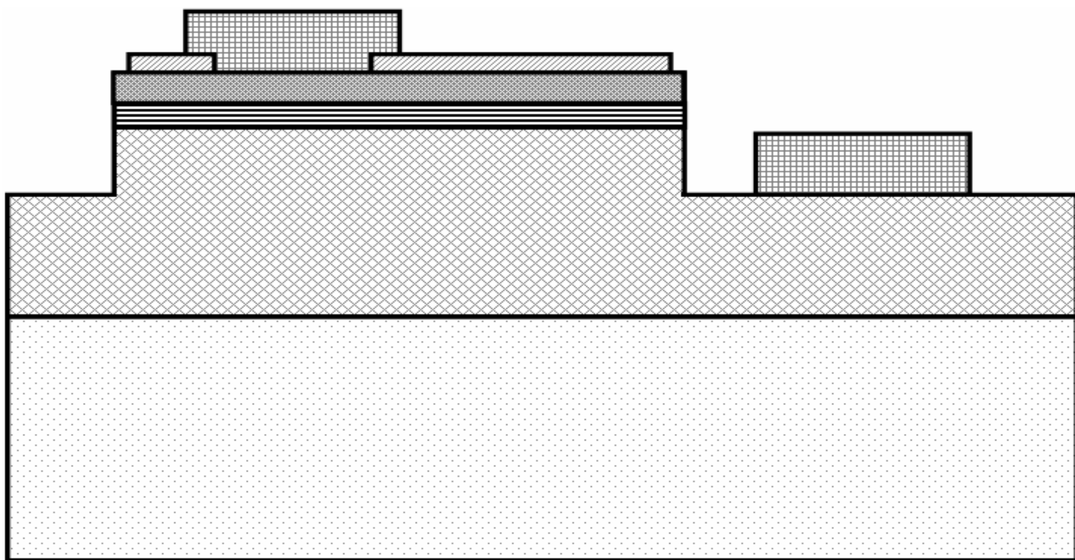
(a) As-grown GaN on sapphire substrate.



(b) Define mesa region and dry etched by ICP-RIE.



(c) ITO or Ni/Au TCL evaporated and wet etched.



(d) Cr-Au evaporated and partially removed by lift-off procedure to be formed as bonding pad.

Fig. 2-1. Process flow and cross section of nitride-based LEDs.

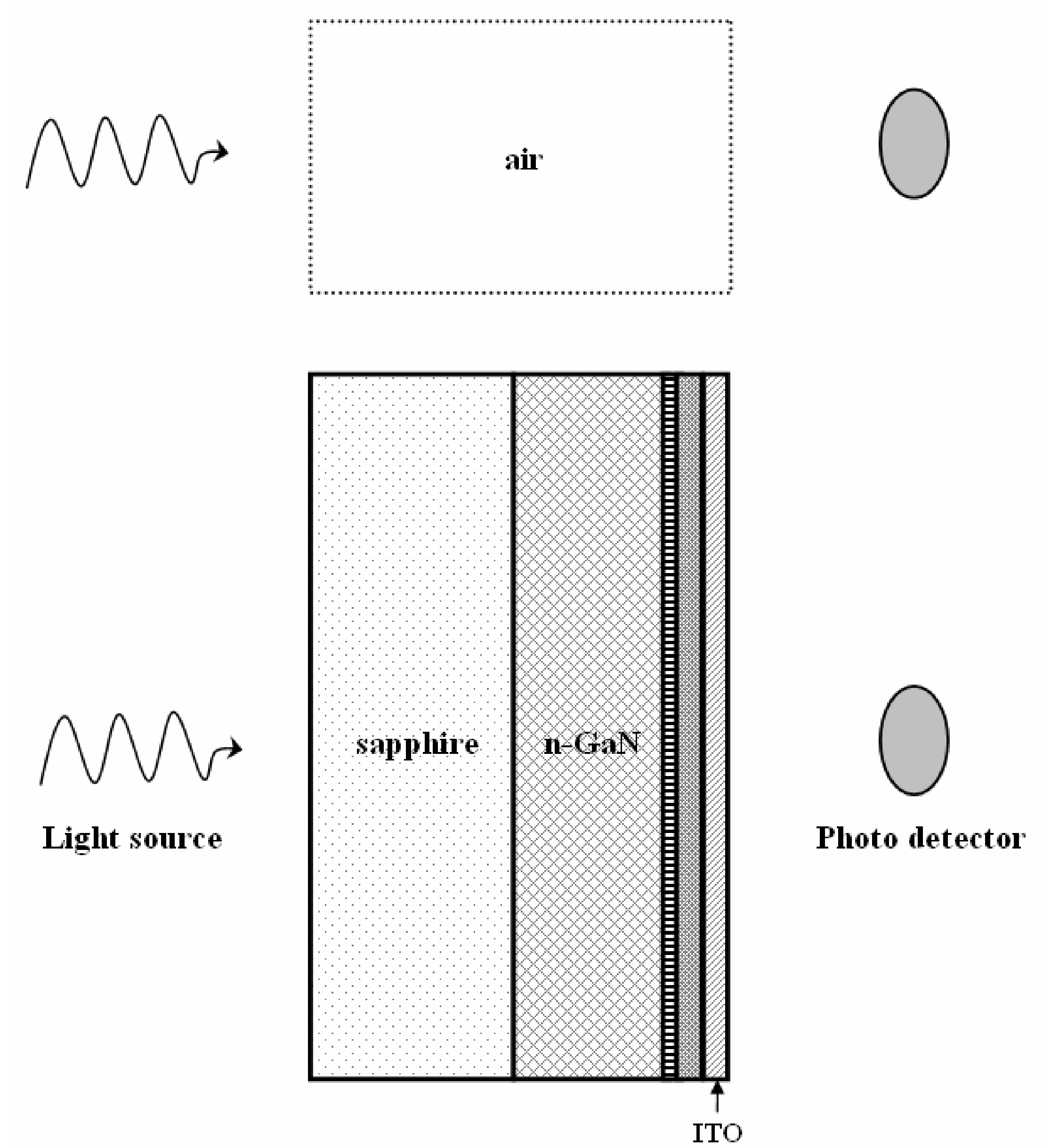


Fig. 2-2. The scheme of transmittance measurement method.

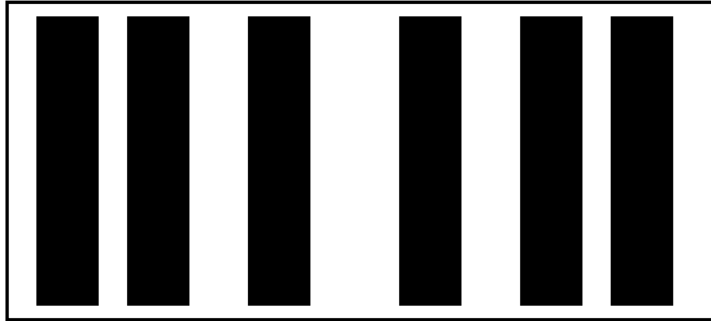


Fig. 2-3. A transmission line model test structure.

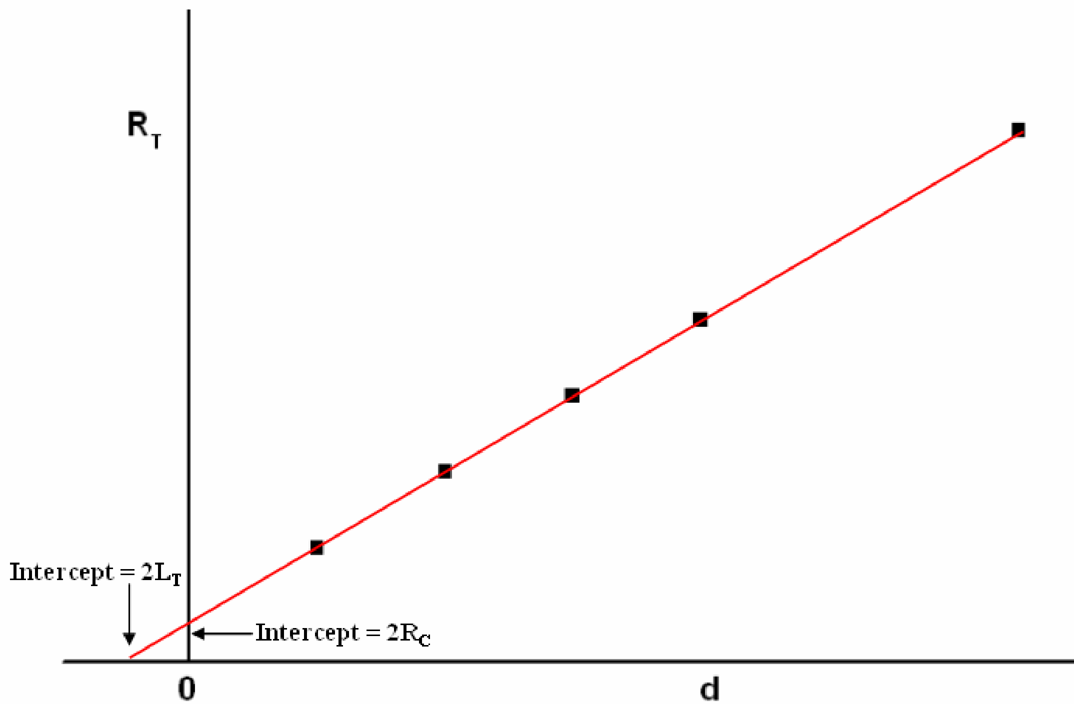


Fig. 2-4. A plot of total resistance as a function of contact spacing, d.

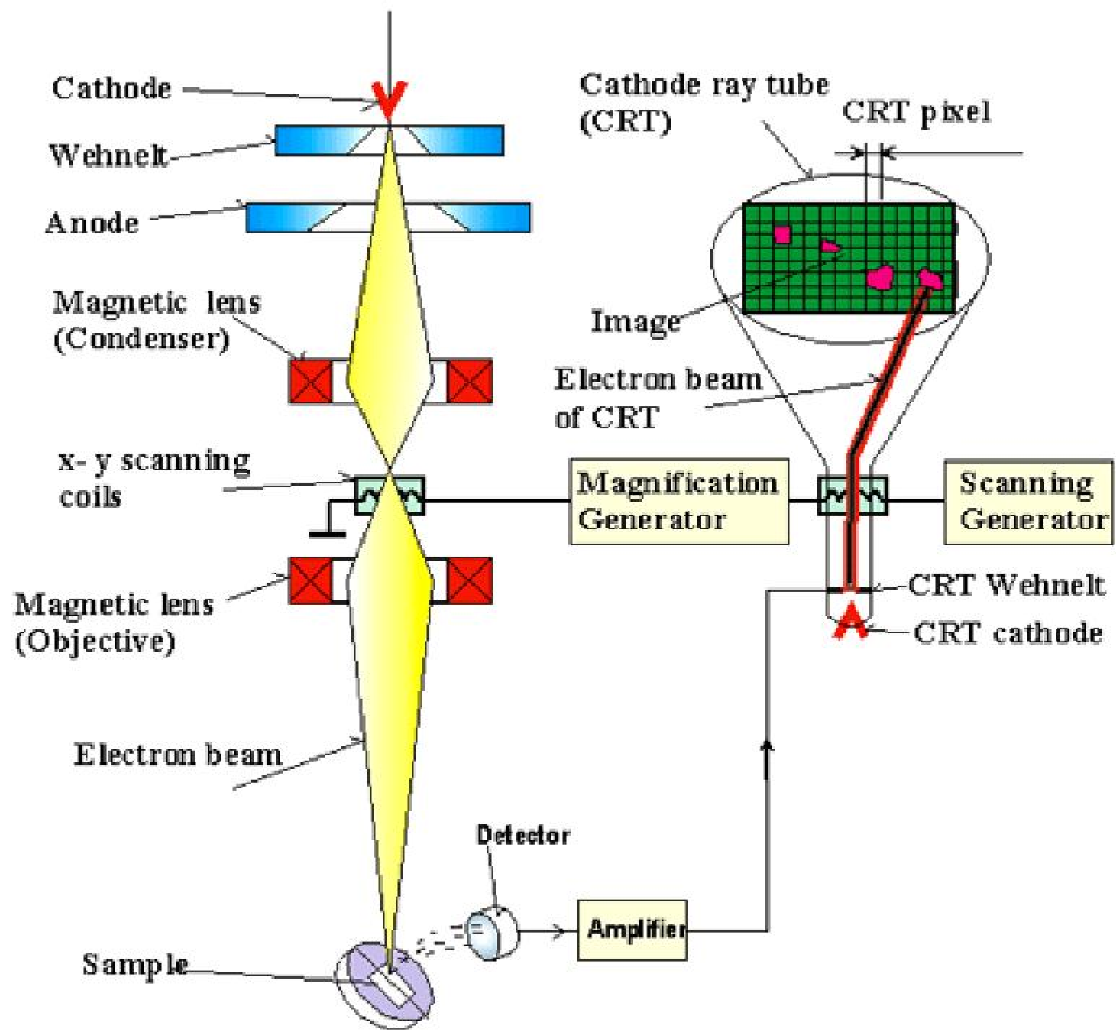
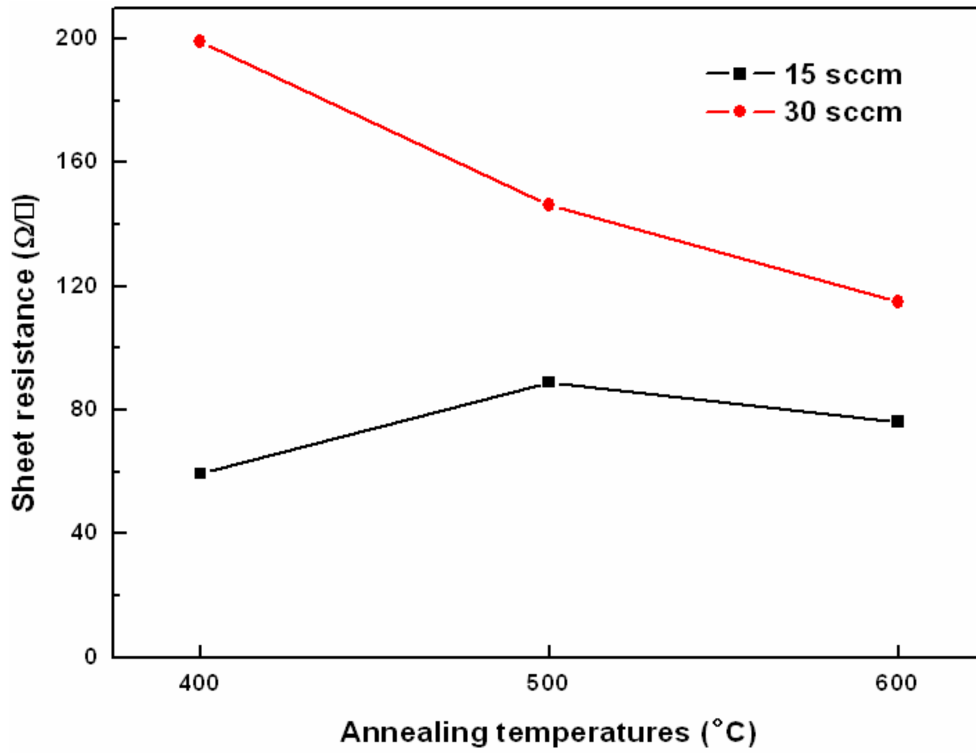
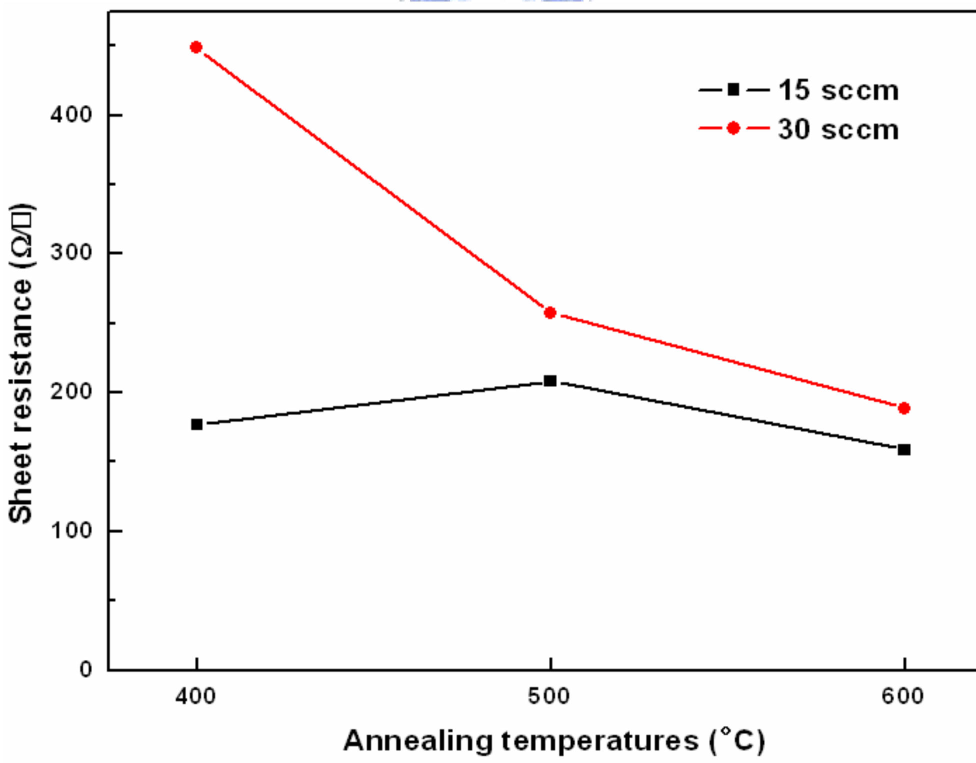


Fig. 2-5. A schematic diagram of a typical SEM.

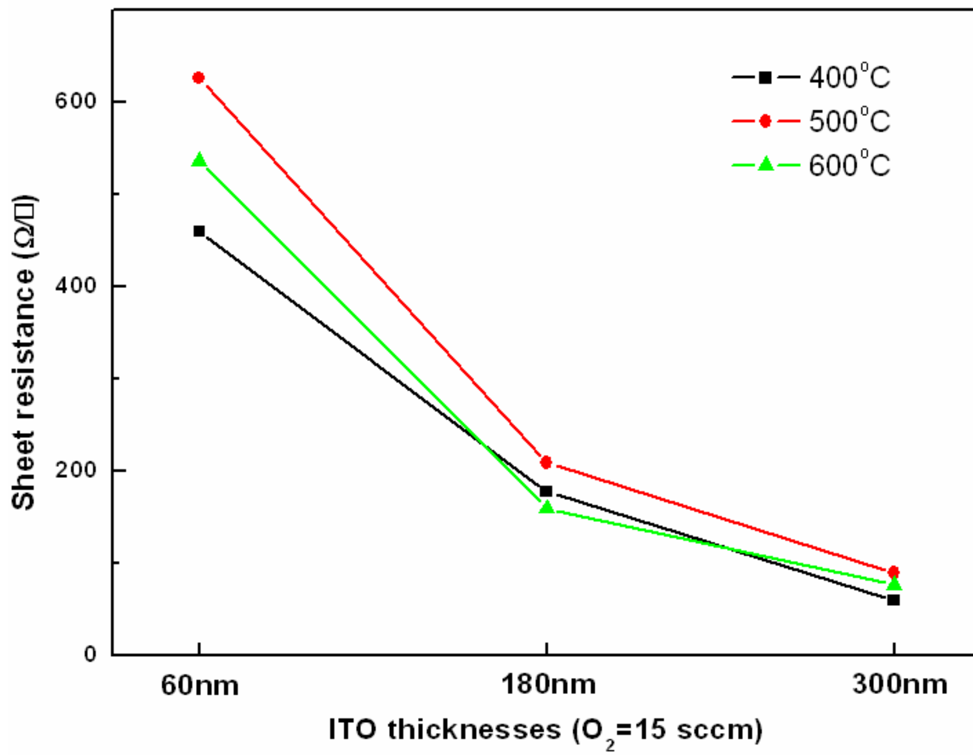


(a)

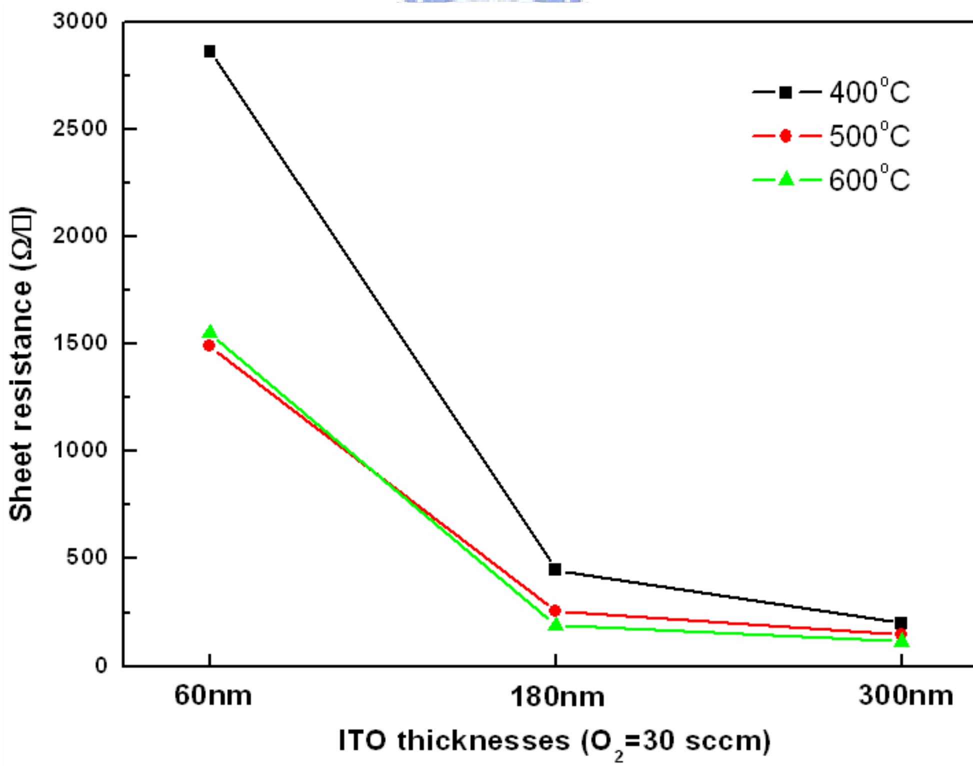


(b)

Fig. 3-1. R_S of (a) 300 nm, (b) 180 nm ITO films on quartz after different processes.

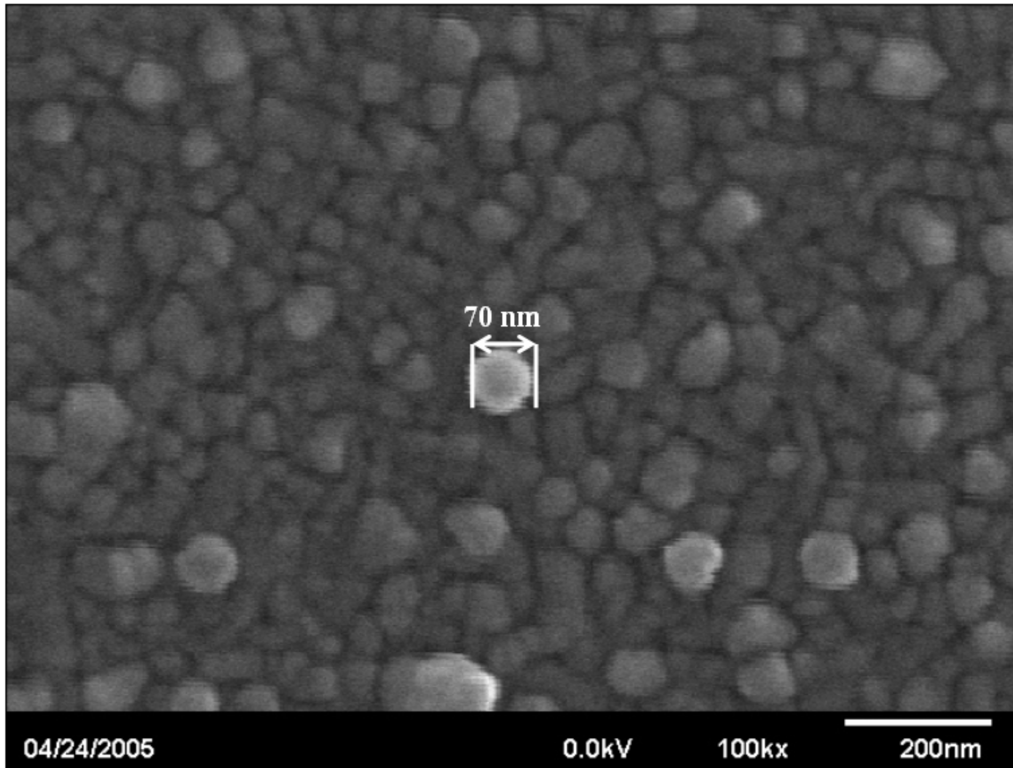


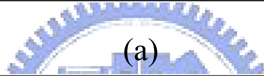
(a)



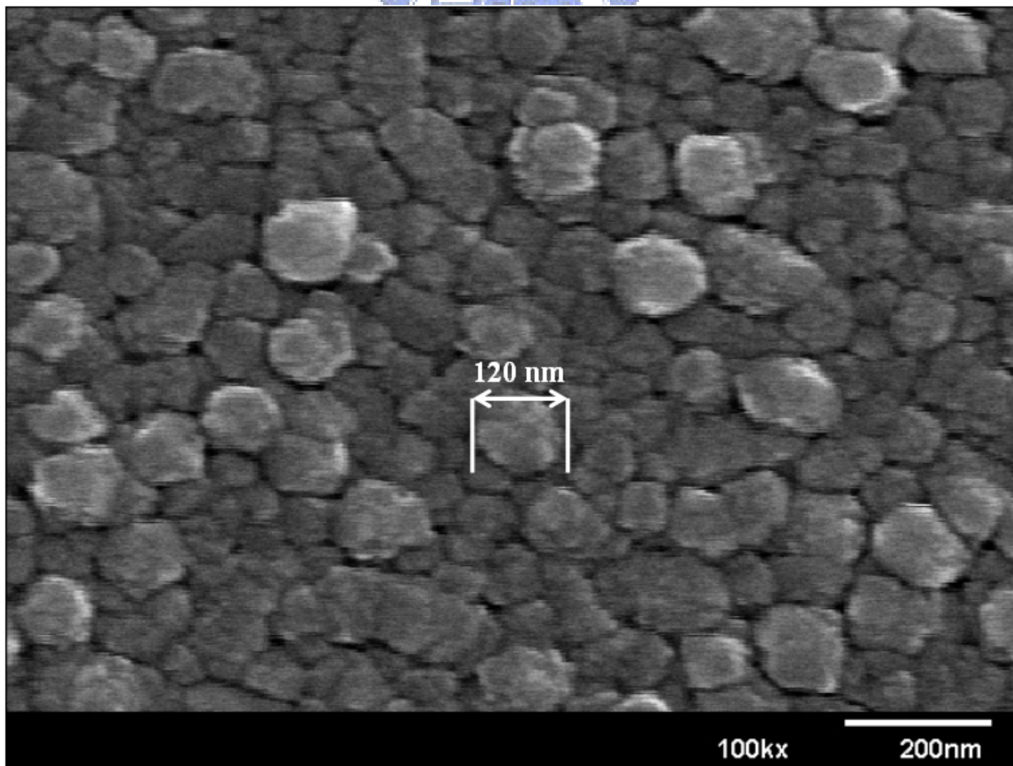
(b)

Fig. 3-2. R_S of different thicknesses of ITO films evaporated with (a) 15 sccm, (b) 30 sccm O_2 flow rate.

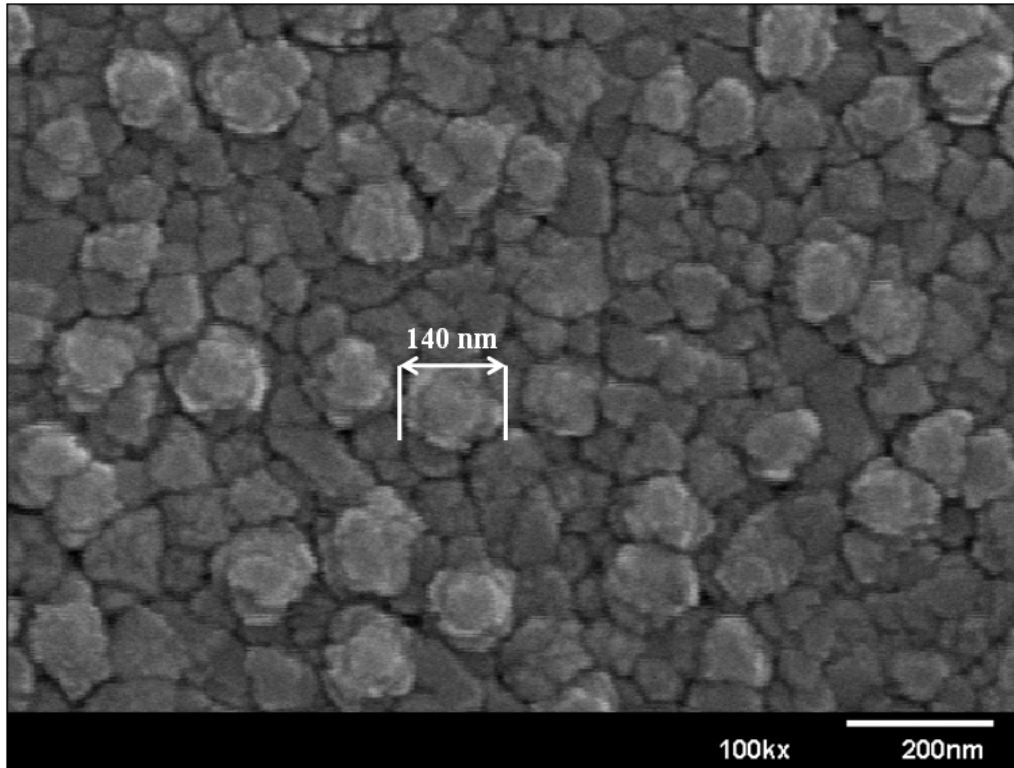




(a)



(b)



(c)

Fig. 3-3. SEM pictures of ITO films with thickness of (a) 60 nm, (b) 180 nm, (c) 300 nm.

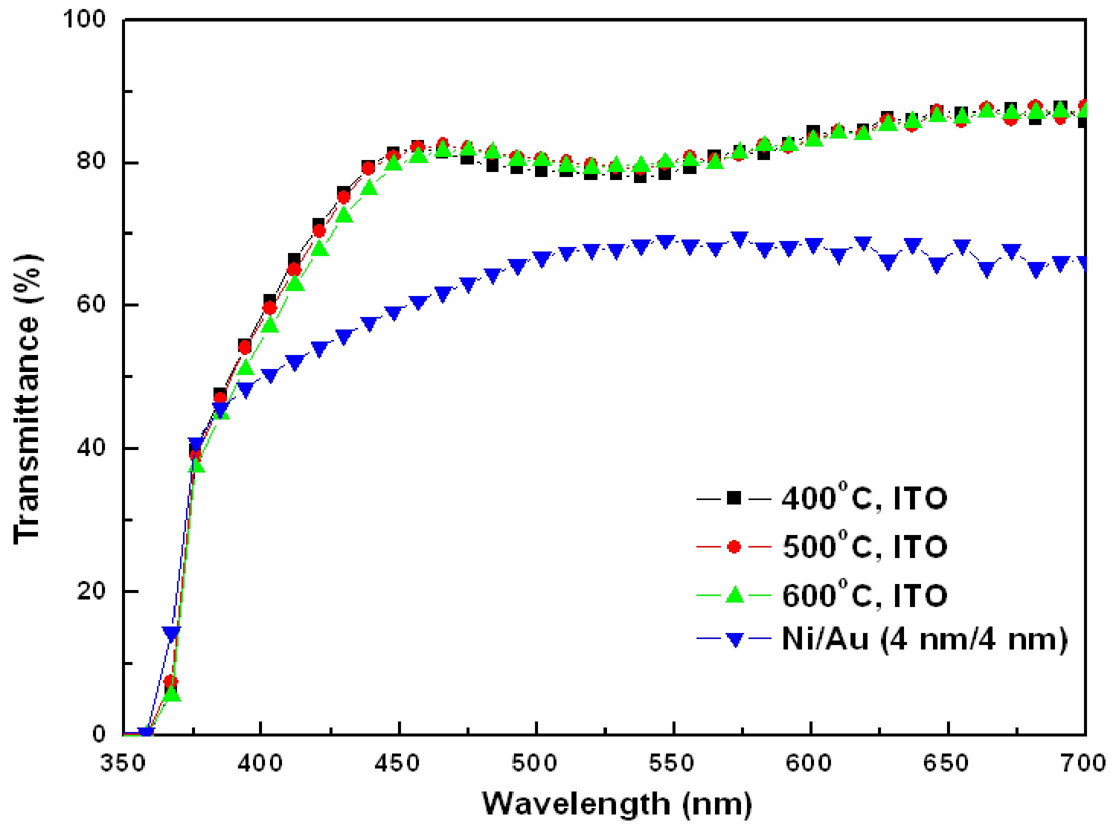


Fig. 3-4. Transmittance of 300 nm ITO on GaN substrate after annealed at different temperatures (O_2 = 15sccm).

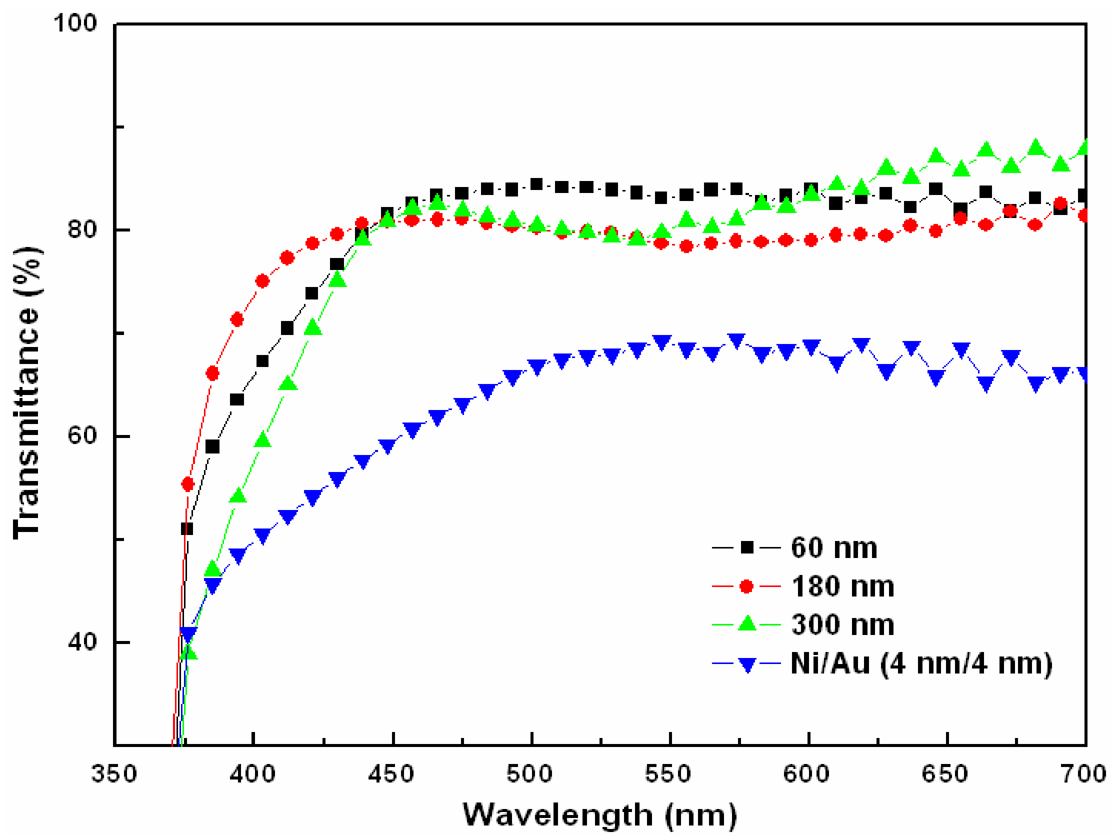


Fig. 3-5. Transmittance of different thicknesses of ITO films (O_2 = 15 sccm).

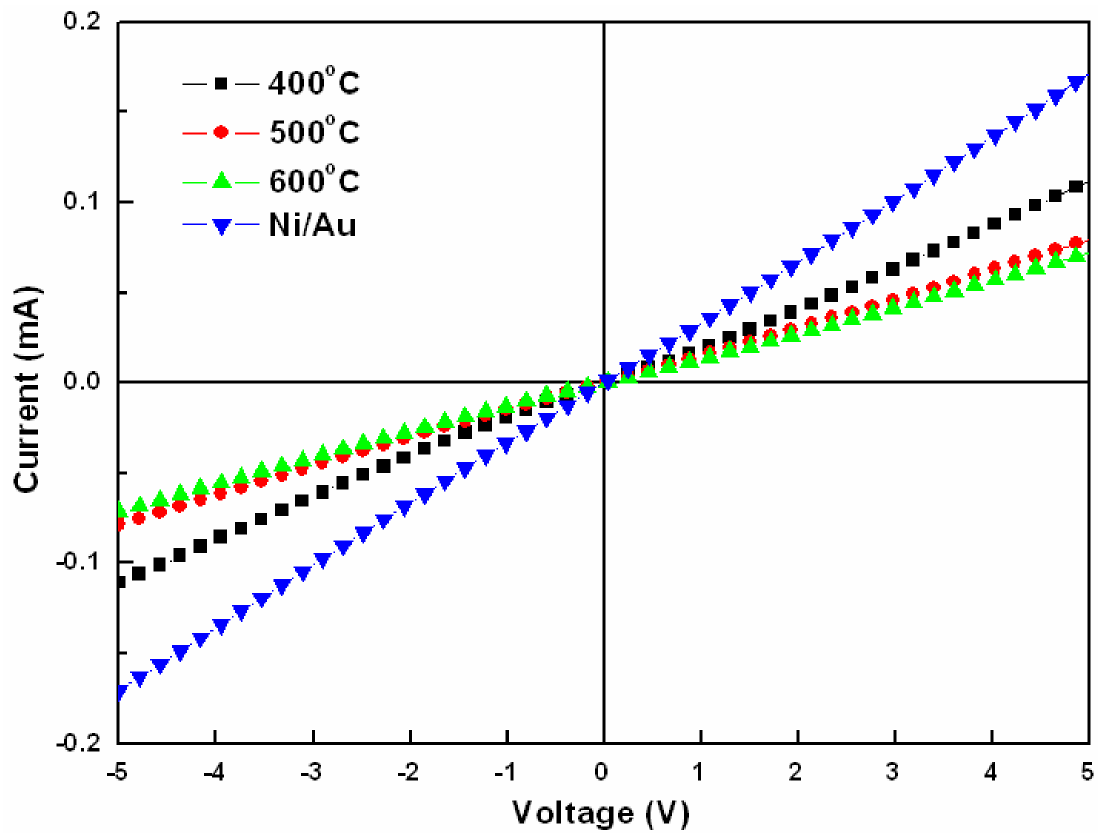


Fig. 3-6. I-V characteristics for Ni/Au (4 nm/4 nm) and ITO (300 nm) contacts on p-GaN after annealing at temperatures of 400-600℃ (O_2 = 15 sccm), measured between the TLM pads with a spacing of 20 μm.

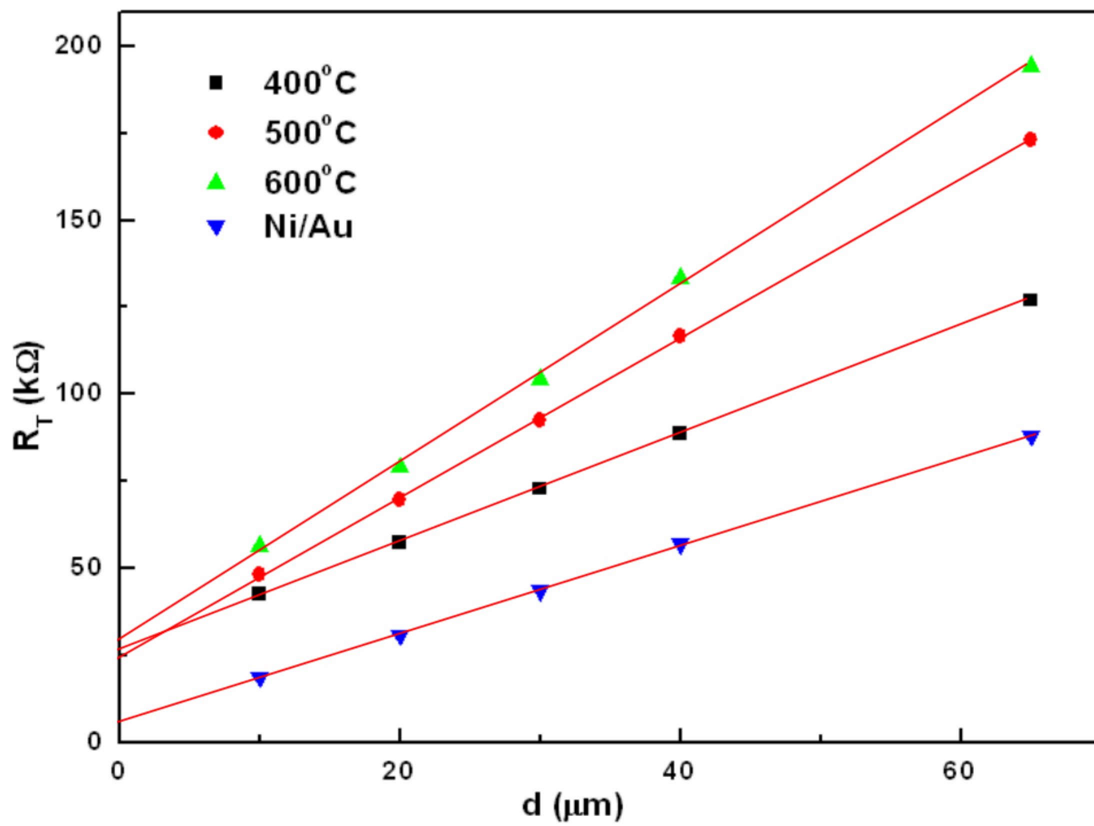


Fig. 3-7. The fitting diagram of measured data. The ITO film is 300 nm thick and was evaporated with 15 sccm O_2 flow rate.

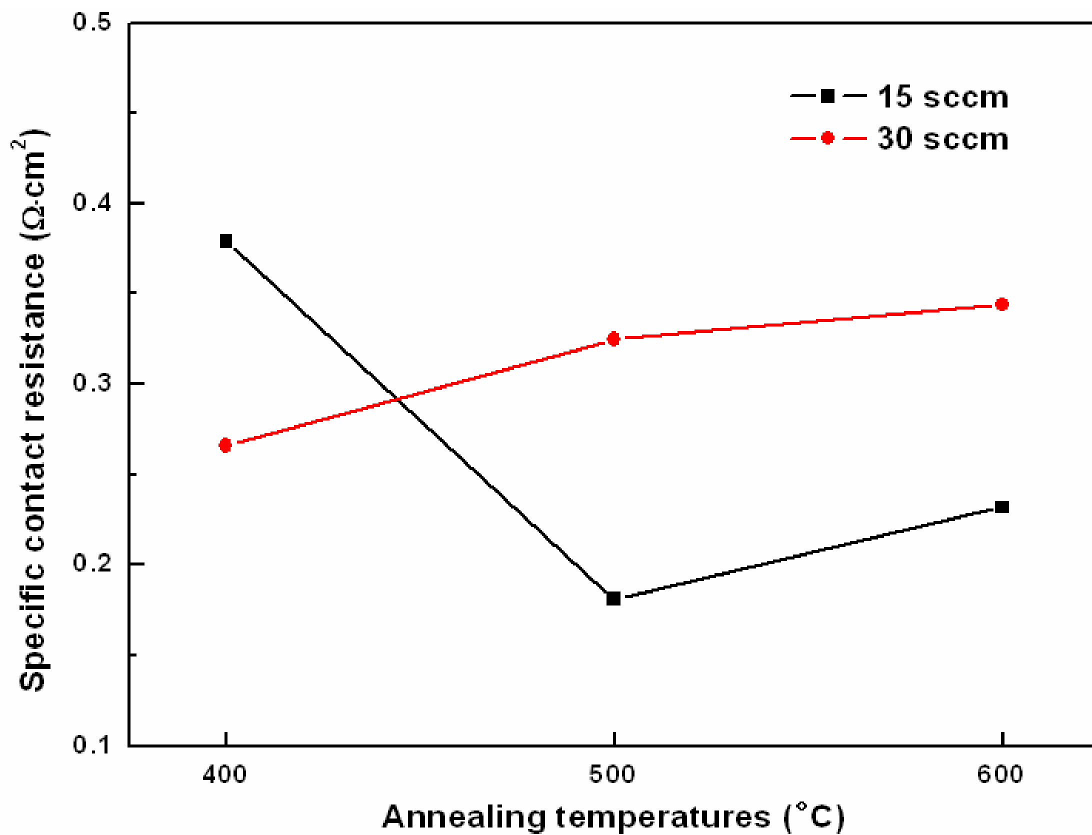
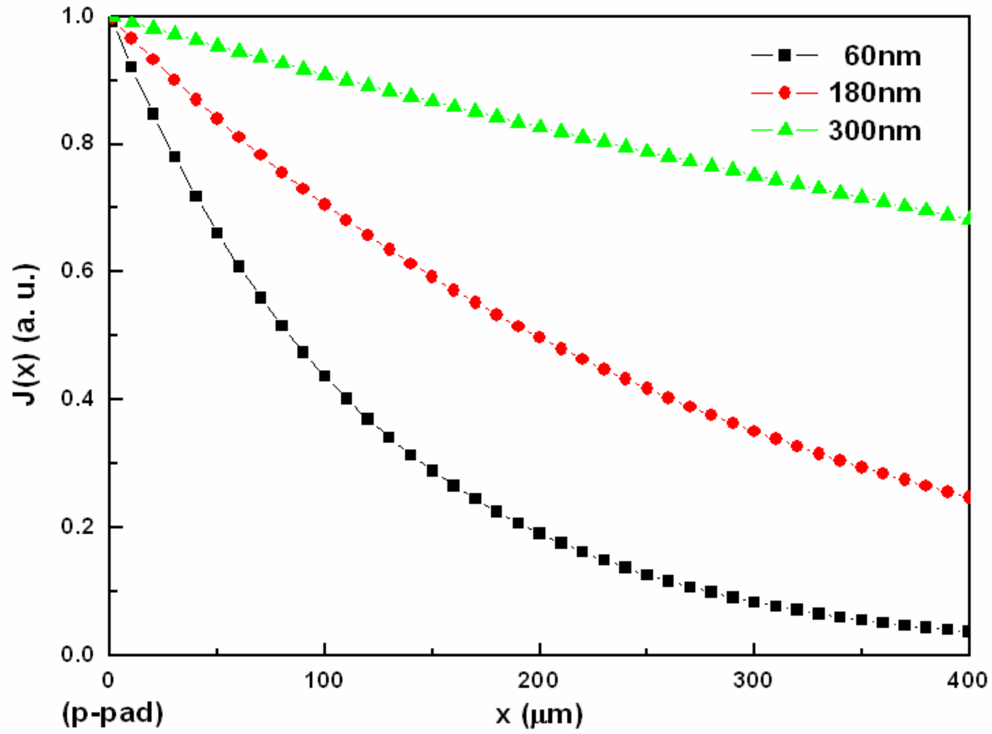
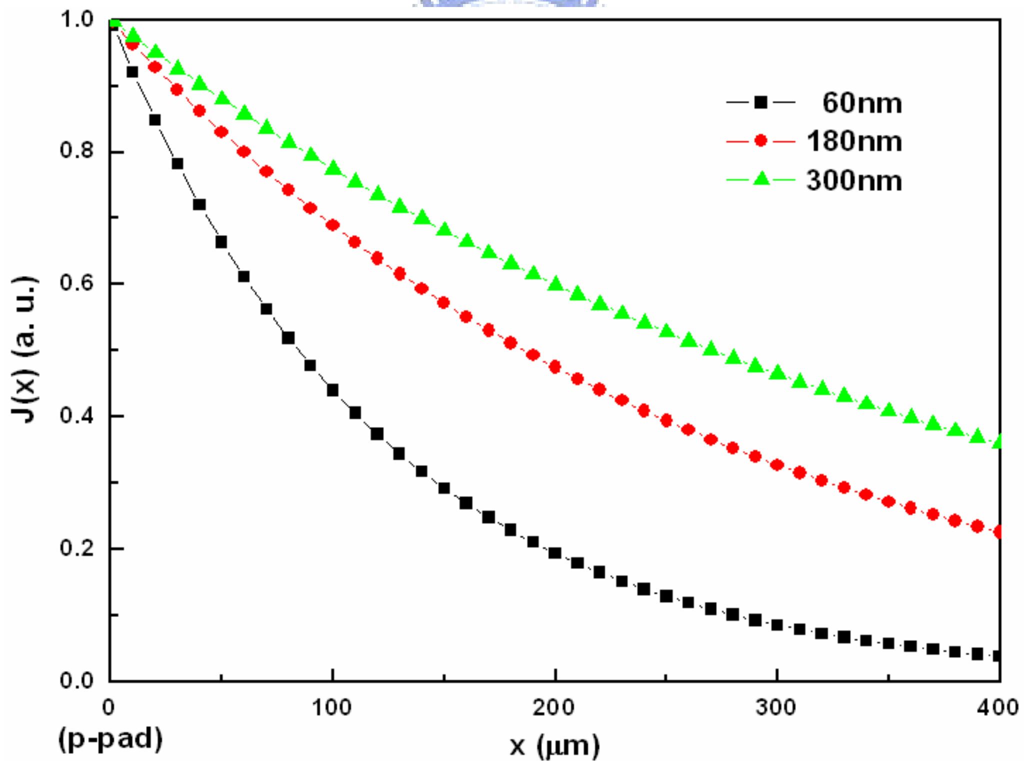


Fig. 3-8. Specific contact resistances of ITO films on p-GaN which were evaporated with different oxygen flow rates and annealed at 400-600℃. The thickness of ITO is 300 nm.



(a)



(b)

Fig. 3-9. Calculated current distribution vs. the lateral length x in a LED. The O_2 flow rate is (a) 15 sccm and (b) 30 sccm, and annealing temperatures are both 400℃.

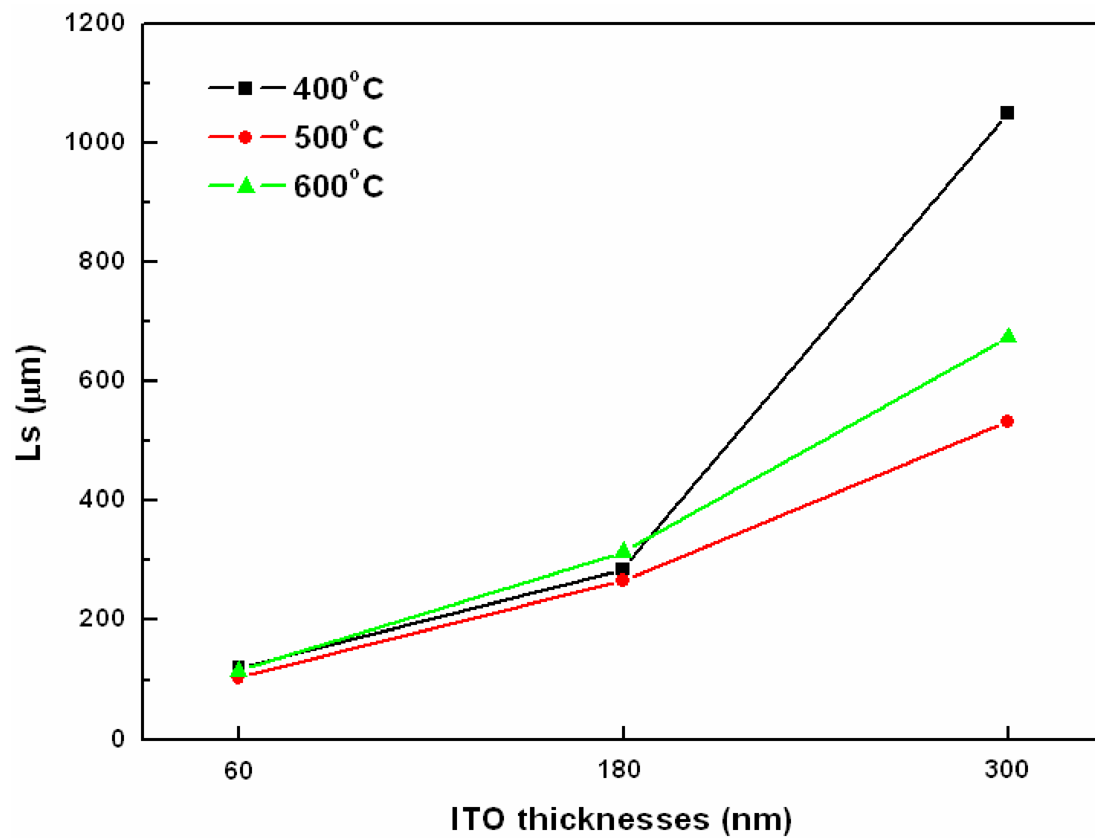


Fig. 3-10. Calculated current spreading length of different thicknesses of ITO films. The oxygen flow rate during evaporation is 15 sccm.

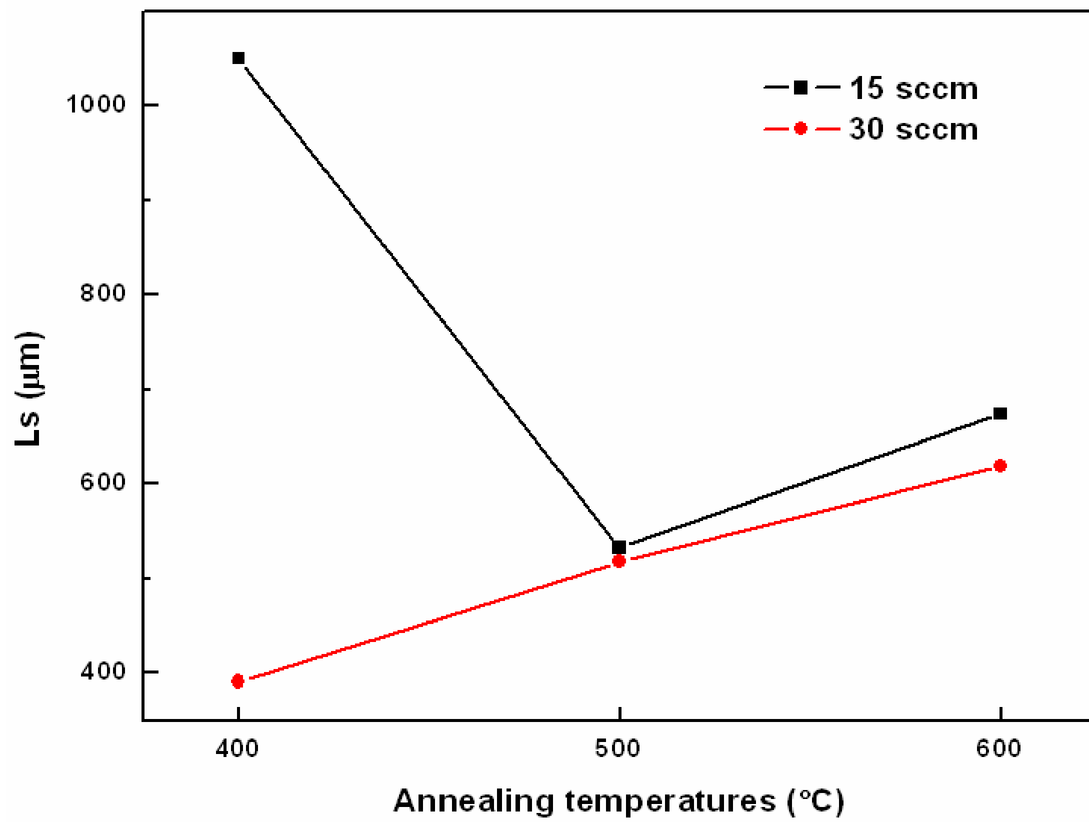


Fig. 3-11. Calculated current spreading length of 300 nm ITO films which were evaporated with different oxygen flow rates.

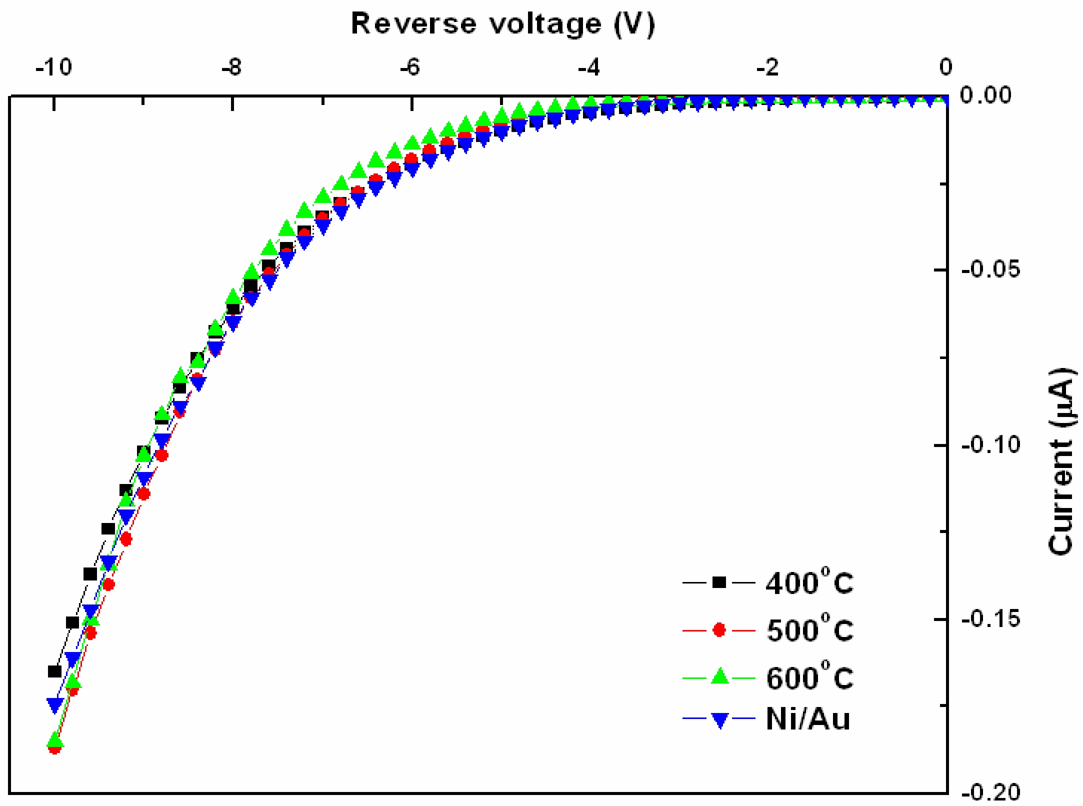
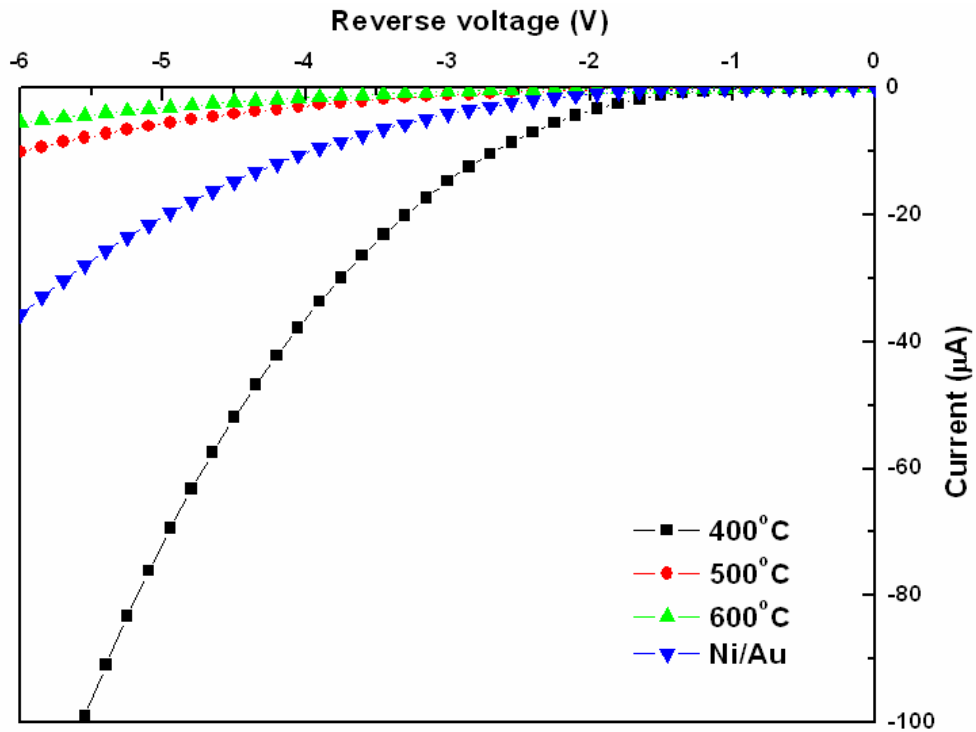
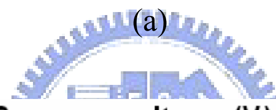
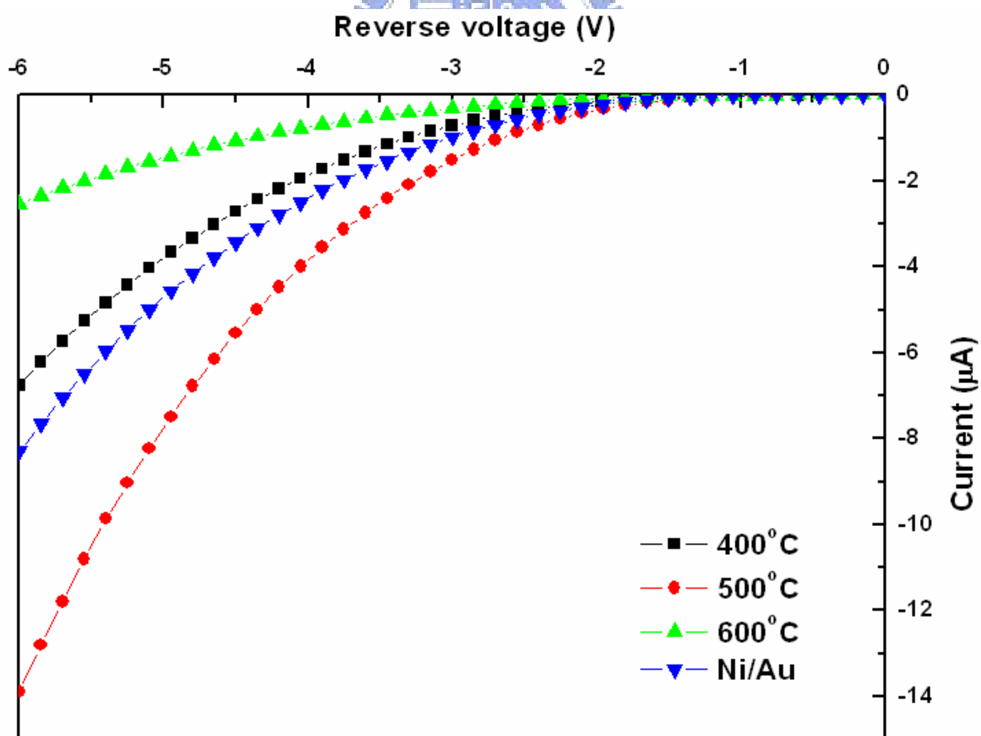


Fig. 3-12. Reverse voltage-current characteristics of the fabricated nitride-based LEDs with different annealing temperatures. The thickness of ITO film is 300 nm and evaporated with 15 sccm O_2 flow rate.



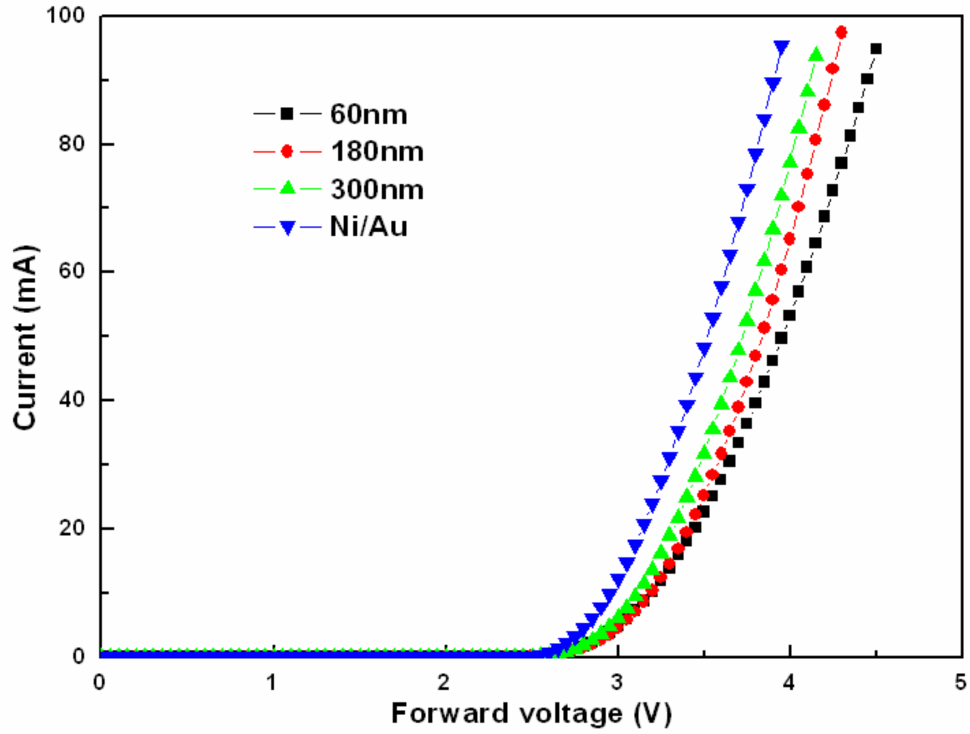
(a)



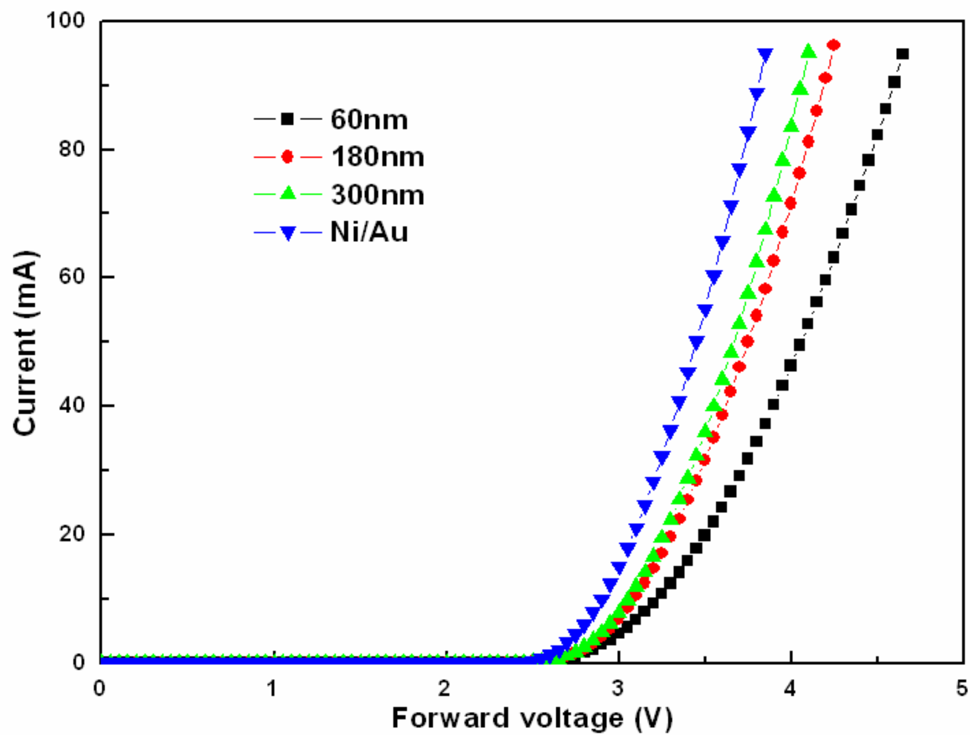


(b)

Fig. 3-13. The reverse I-V characteristics of devices on (a) condition B and (b) condition F.

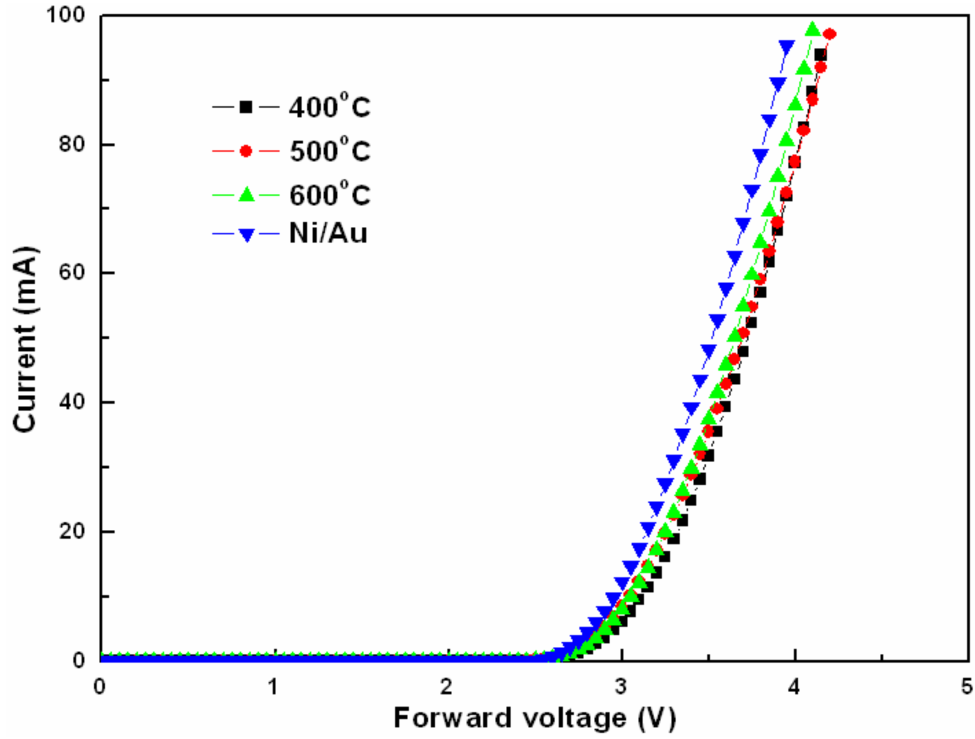


(a)

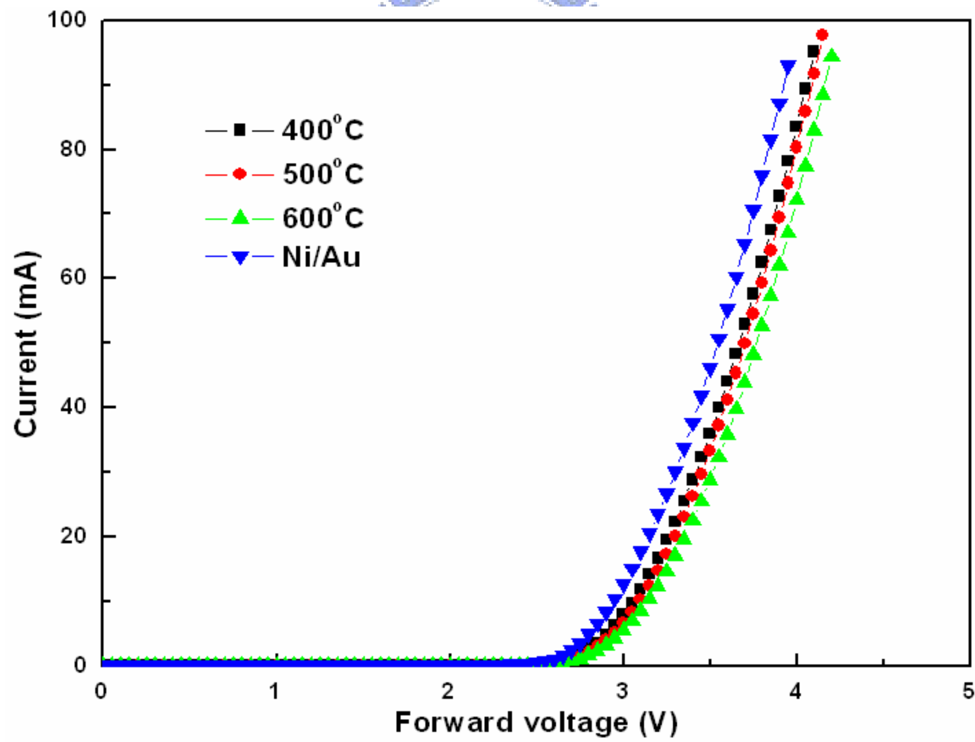


(b)

Fig. 3-14. Forward current-voltage characteristics of GaN LEDs with different thicknesses of ITO films which were annealed at 400℃ and evaporated with (a) O_2 = 15 sccm (b) O_2 = 30 sccm.

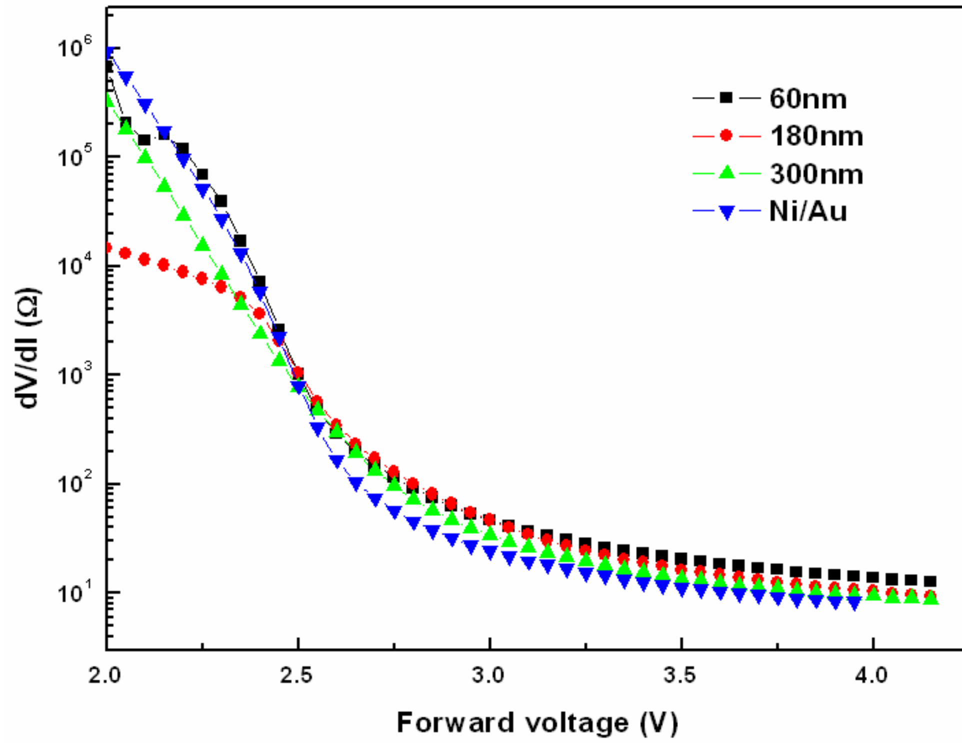


(a)

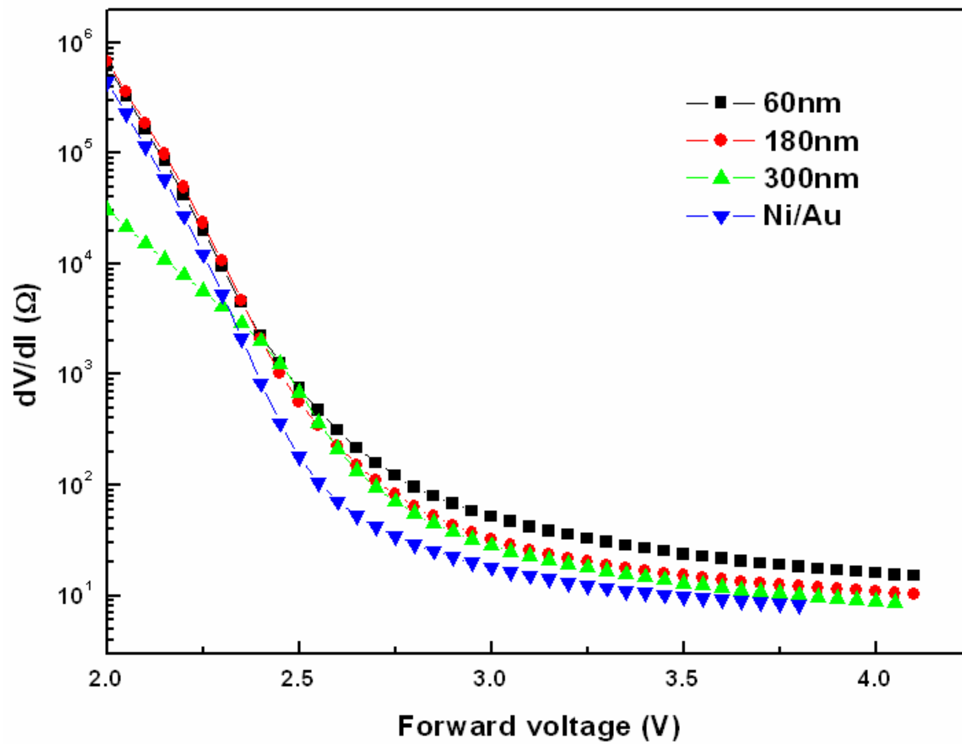


(b)

Fig. 3-15. Forward current-voltage characteristics of GaN LEDs with 300 nm ITO films annealed at different temperatures and evaporated with (a) O_2 = 15 sccm (b) O_2 = 30 sccm.

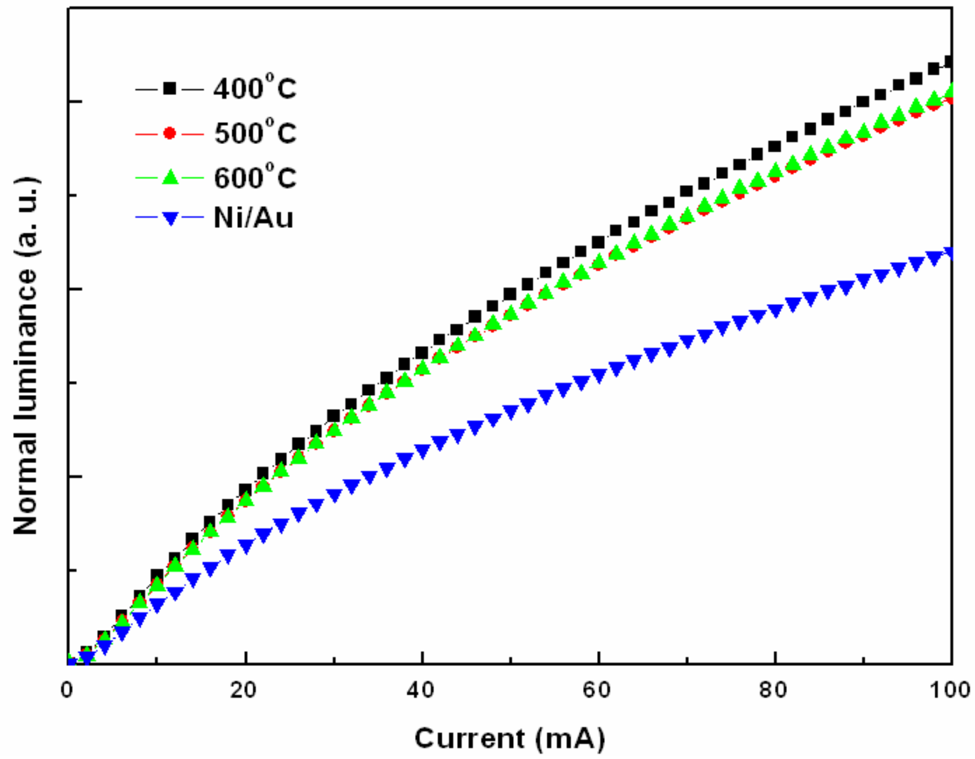


(a)

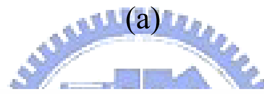


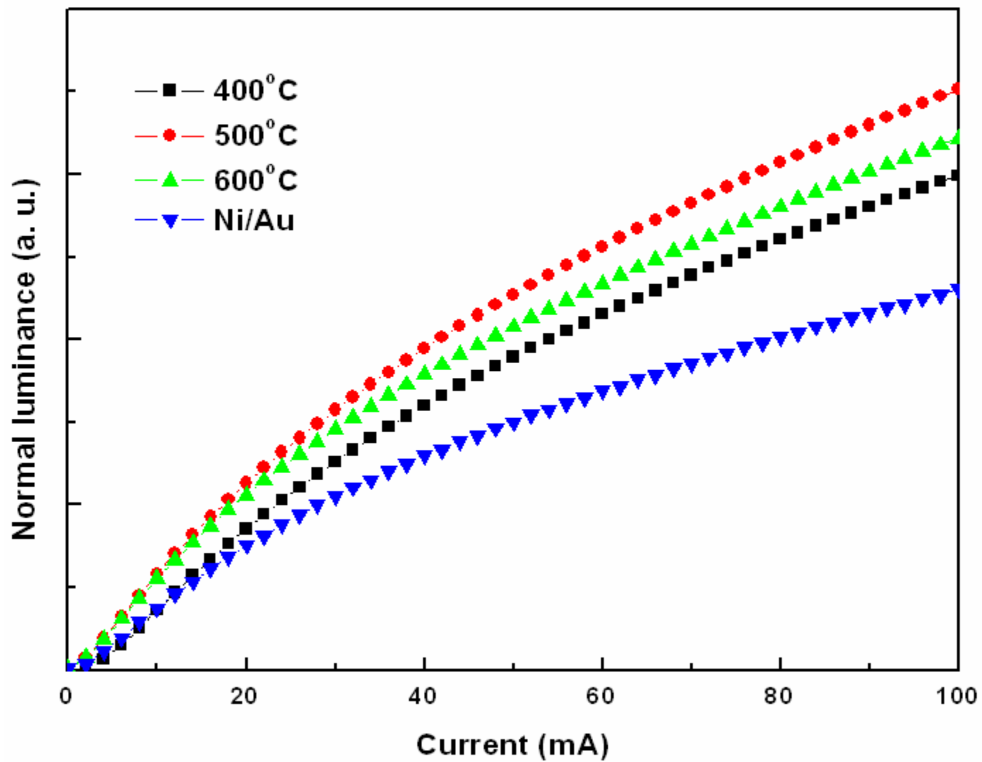
(b)

Fig. 3-16. Forward voltage vs. dynamic resistance in LEDs with ITO film annealed at 400℃ and evaporated with (a) 15 sccm and (b) 30 sccm O_2 flow rate.



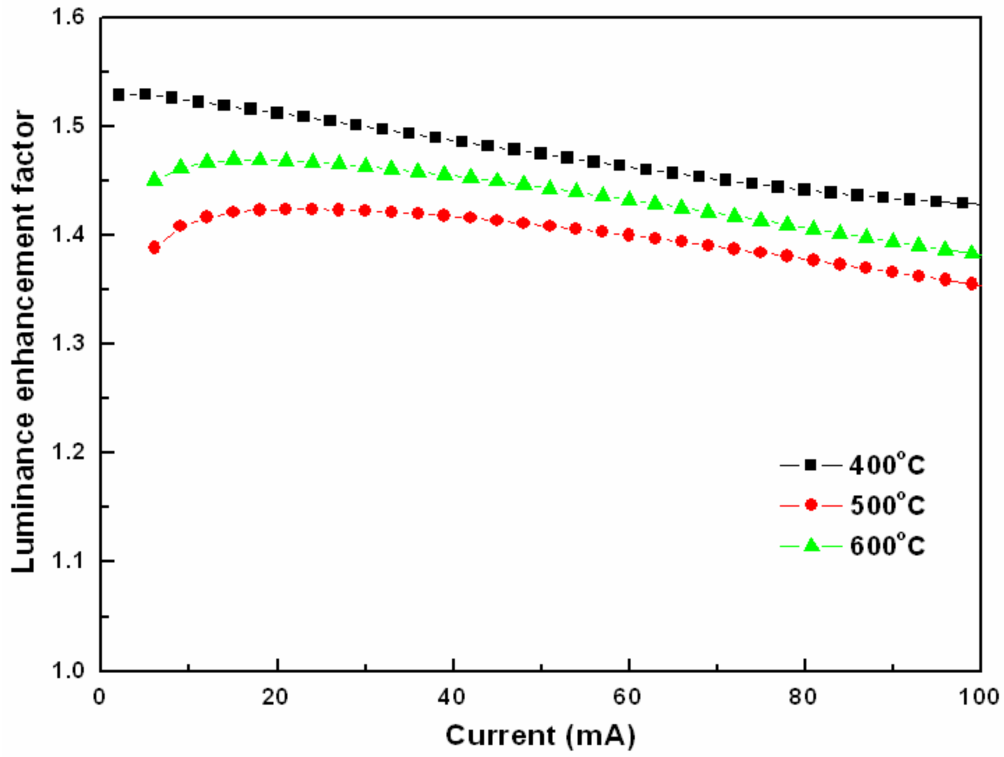
(a)



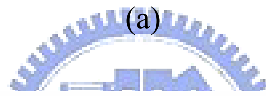


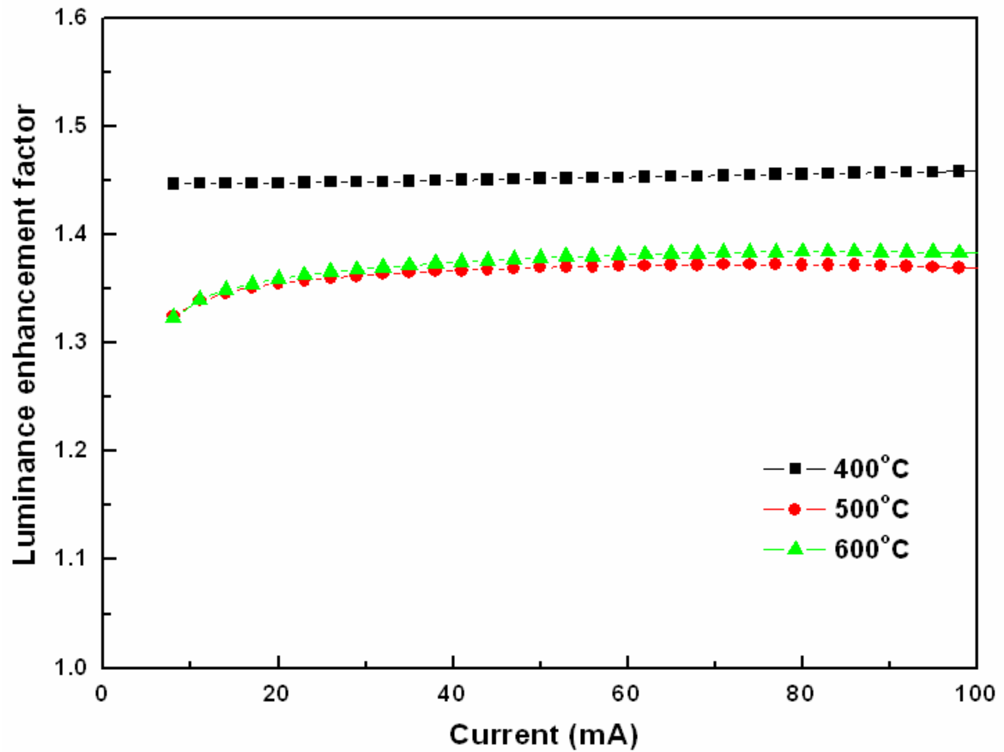
(b)

Fig. 3-17. The normal luminance vs. injection current of fabricated LEDs with 300 nm ITO film evaporated with (a) 15 sccm and (b) 30 sccm O_2 flow rate.



(a)





(b)

Fig. 3-18. The luminance enhancement vs. injection current of fabricated LEDs with (a) 60 nm and (b) 300 nm ITO film evaporated with 15 sccm O_2 flow rate.

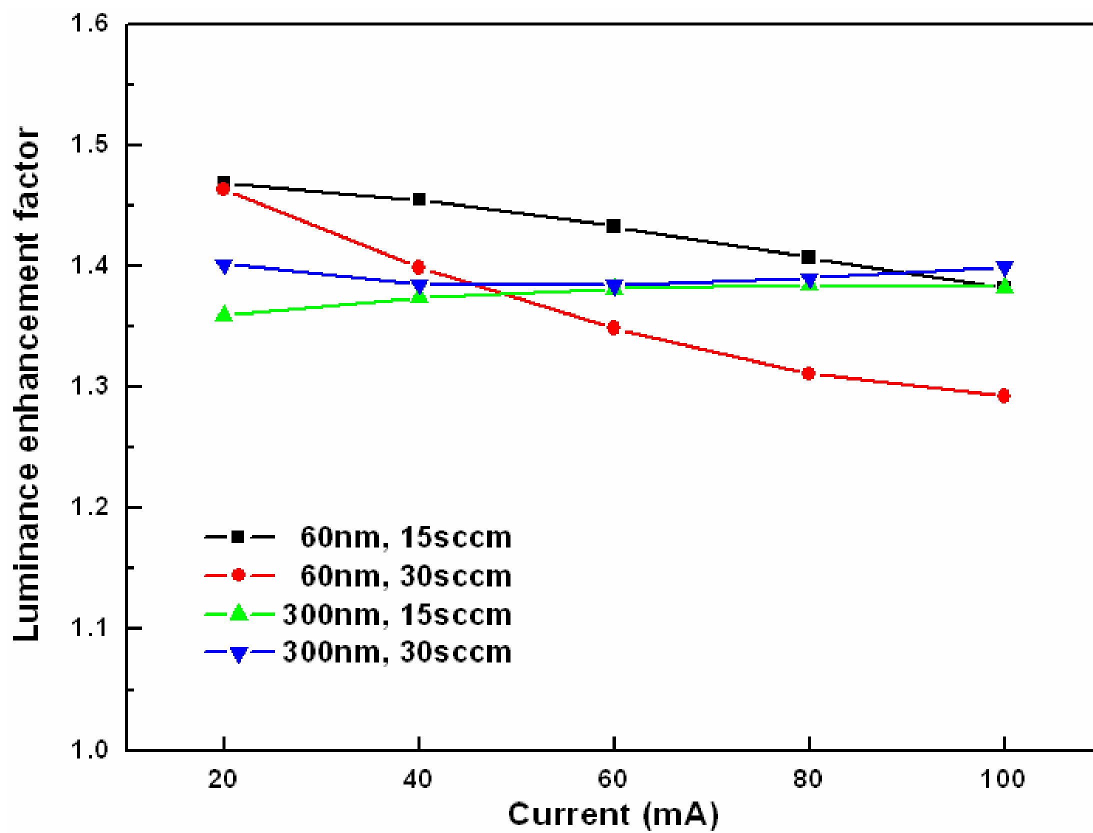
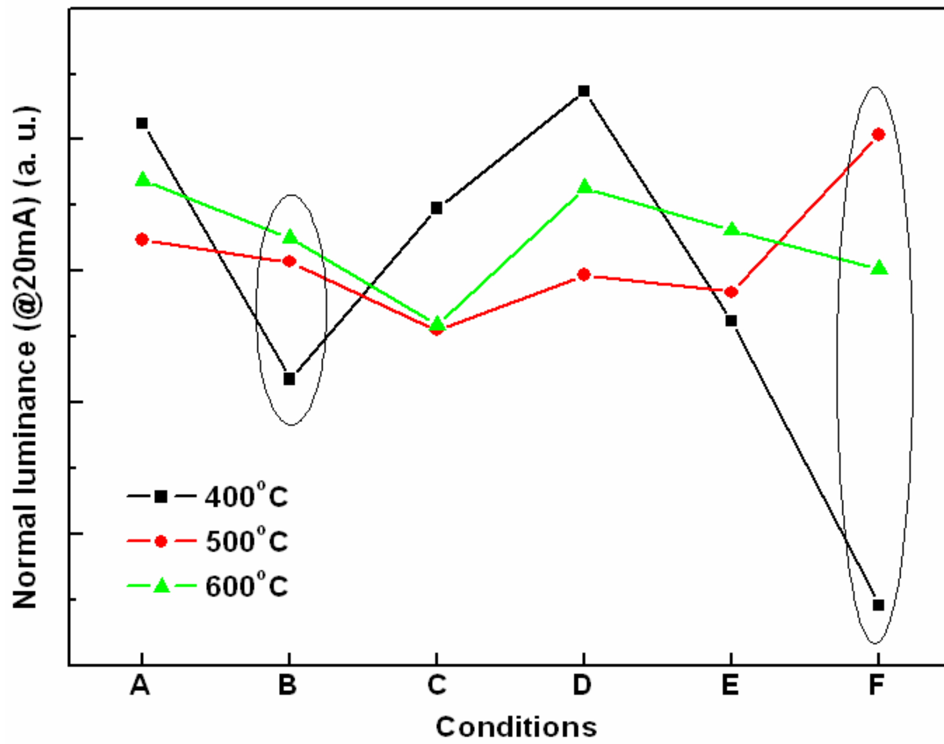
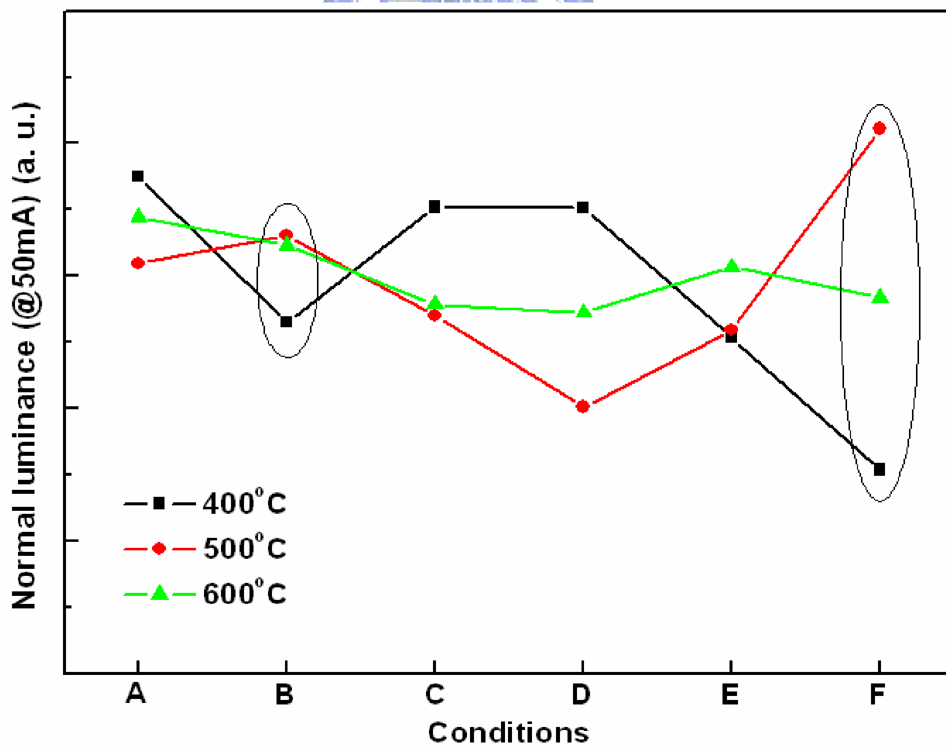


Fig. 3-19. Luminance enhancement with different ITO films at an injection current of 20-100 mA

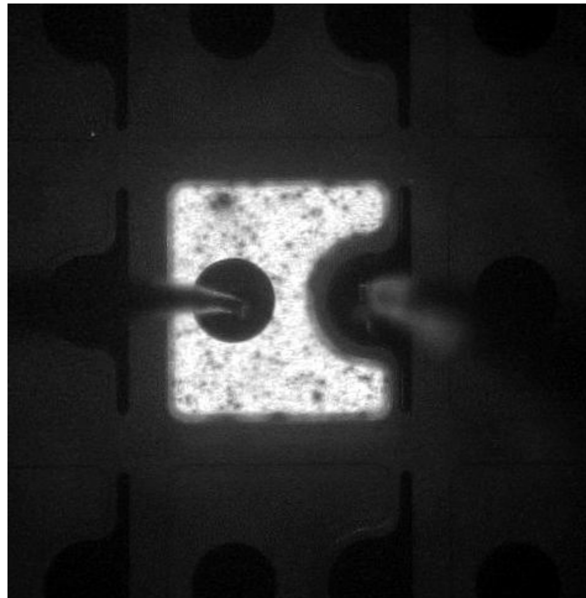


(a)



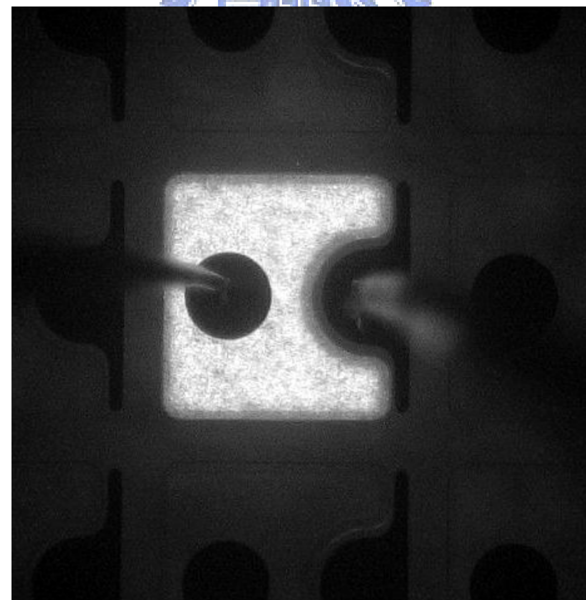
(b)

Fig. 3-20. Normal luminance of all ITO samples at an injection current of (a) 20 mA, and (b) 50 mA. Conditions are referred to table 2-1.



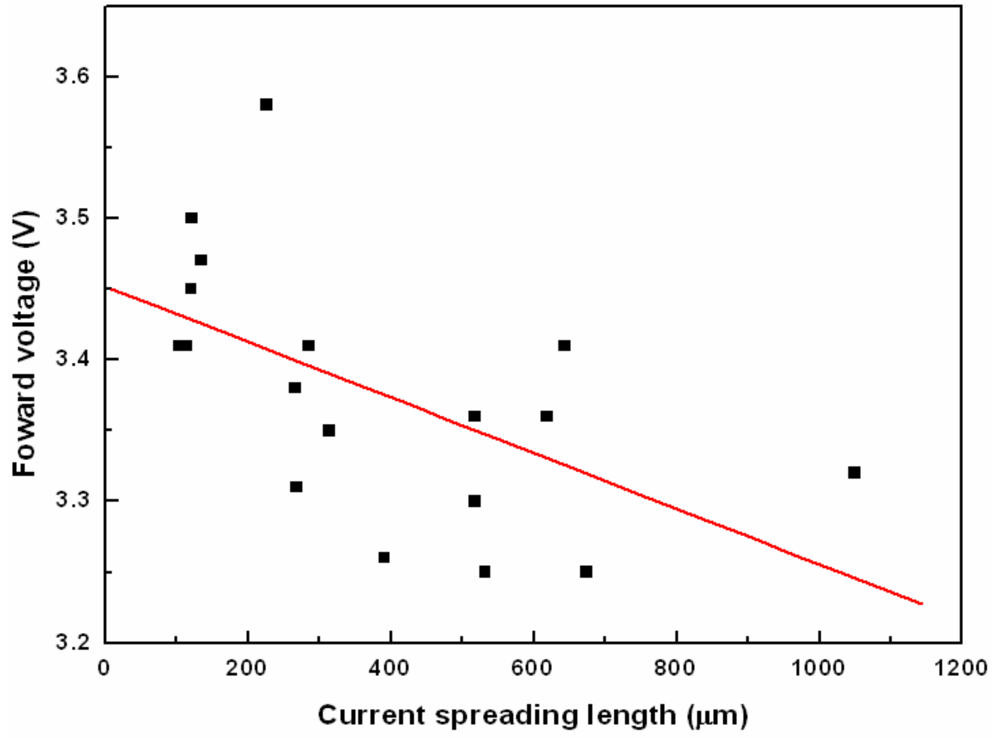
(a)

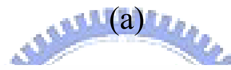
[university watermark]



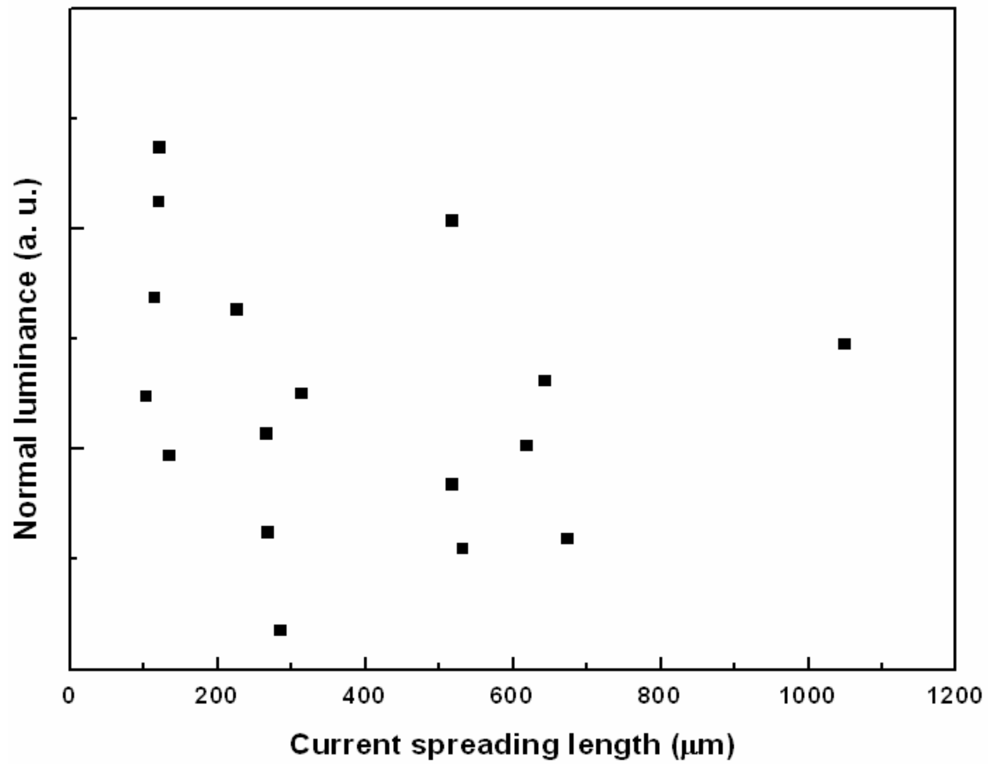
(b)

Fig. 3-21. Emission microscopy graphs of (a) condition B annealed at 400℃ and (b) normal LED at a dc current of 0.2 mA.





(a)



(b)

Fig. 3-22. The relationship between current spreading length and (a) forward voltage at 20 mA and (b) normal luminance.

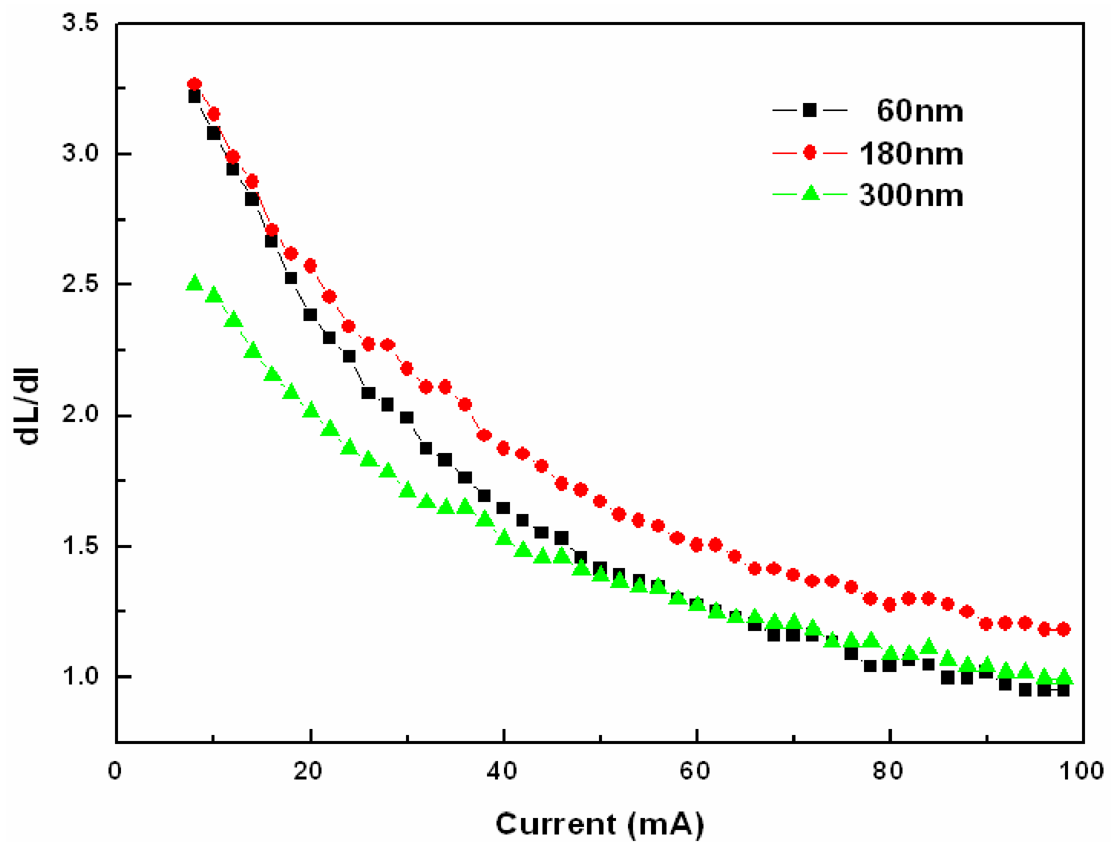


Fig. 3-23. The current converting efficiency (dL/dI) of LEDs vs. injection current. The ITO films were evaporated with 15 sccm O_2 flow rate and annealed at 500℃.

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碩士論文：氧化銦錫擴散電流層厚度對氮化鎵發光二極體光電特性的影響

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