應用於高速數位用戶迴路之適應性時域等化器 設計

Adaptive Time Domain Equalizer (TEQ) for VDSL System

研究生: 鍾沛如 Student: Pei-Ju Chung

指導教授: 林源倍 博士 Advisor: Dr. Yuan-Pei Lin



A Thesis

Submitted to Institute of Electrical and Control Engineering
College of Electrical Engineering and Computer Science
National Chiao Tung University
In Partial Fulfillment of the Requirements
For the Degree of Master

In

Electrical and Control Engineering
August 2005
Hsinchu, Taiwan, Republic of China
中華民國九十四年八月