

List of Tables

Table 3.1	USB 2.0 module pipe mapping table	49
Table 4.1	Collection of data sizes in transmitter DSP	59
Table 4.2	Mapping table for data transfer	59
Table 4.3	Derivation of the ratio for each mode	75
Table 4.4	Mode selection table	77
Table 5.1	System level evaluation of data sizes among three modes	82
Table 5.2	Comparison of logic gate count between adder and multiplier	85
Table 5.3	Mapping table of control bits for output truncation of IFFT	90
Table 5.4	Mapping table of control bits for output truncation of FFT.....	90
Table 5.5	Comparison between normal (NM) and bit-reversed (BR) order.....	92