考慮大型類比電路對稱性限制區塊放置的一個演算法

An Algorithm for Large-Scale Analog Block Placement with Symmetry Constraints

研究生:方信文 Student: Hsin-Wen Fang

指導教授:陳宏明 教授 Advisor: Professor Hung-Ming Chen

國立交通大學

電子工程學系電子研究所碩士班碩士論文 A Thesis

Submitted to Department of Electrical Engineering & Institute of Electronics

College of Electrical Engineering and Computer Engineering

National Chiao Tung University
in Partial Fulfillment of the Requirements
for the Degree of
Master
in
Electronics Engineering

January 2007 Hsinchu, Taiwan, Republic of China

中華民國九十六年一月