高速渦輪碼晶片之設計與實作

Design and Implementation for

high-throughput Turbo Decoder Chip

研 究 生：唐正浩　　　　Student：Cheng-Hao Tang

指導教授：張錫嘉教授　　Advisor：Hsie-Chia Chang

國 立 交 通 大 學

電子工程學系 電子研究所 碩士班

碩 士 論 文

A Thesis
Submitted to Department of Electronics Engineering & Institute Electronics
College of Electrical and Computer Engineering
National Chiao Tung University
In Partial Fulfillment of the Requirements
for the Degree of
Master of Science
in

Electronics Engineering
July 2006
Hsinchu, Taiwan, Republic of China

中華民國九十五年九月