

參考文獻

- [1] <http://www.cpcwedu.com/Document/wireless/181724703.htm>
- [2] K. W. Kobayashi, et al., "Ultra-Low DC Power GsAs HBTs and C-Band Low noise Amplifiers for portable Wireless Applications," *IEEE Trans.MTT*, no. 12, pp.3055-3061, Dec. 1995
- [3] TSMC 0.18 um mixed signal 1P6M + MIM salicide 1.8V/3.3V design guideline.
- [4] B. M. Ballweber, R. Gupta, and D. J. Allstot, "A fully integrated 0.5-5.5-GHz CMOS distributed amplifier," *IEEE Trans. Solid-State Circuits* , vol.35, no.2, pp.231-239, Feb. 2000
- [5] Sangyoub Lee, "Design and Analysis of Ultra-Wide Bandwidth Impulse Radio Receiver", *A dissertation presented to the faculty of southern California in partial fulfillment of the requirements for the degree of philosophy*, August 2002.
- [6] A. Bevilacqua and A.M. Niknejad, "An ultra-wideband CMOS LNA for 3.1 to 10.6 GHz wireless receivers", *2004 IEEE International Solid-State Circuits Conference , Digest of Technical Papers*, Vol.1, 15-19 Feb. 2004 Page(s): 382-533.
- [7] A. Ismail and A. Abidi, "A 3 to 10 GHz LNA using a wideband LC-ladder matching Network," *in IEEE ISSCC Dig.Tech.Papers*. 2004, pp.384-385.
- [8] P. Andreani and H. Sjoland , "Noise optimization of an inductively degenerated CMOS low noise amplifier", *Circuits and SystemsII: Analog and Digital Signal Processing, IEEE Transactions on* [see also *Circuits and SystemsII: Express Briefs, IEEE Transactions on* Volume 48, Issue 9, Sept. 2001 Page(s): 835-841.
- [9] S. Vishwakarma, Sung Yong Jung , and Young joong Joo, "Ultra wideband CMOS low noise amplifier with active input matching", *Ultra Wideband Systems*, 2004. *Joint UWBST & IWUWBS. 2004 International Workshop on* 18-21 May 2004 Page(s):415-419

- [10] David M. Pozar , *Microwave Engineering*, Second Edition.
- [11] T.H. Lee , *The Design of CMOS Radio-Frequency Integrated Circuits*, , Cambridge.
- [12] D. K. Shaeffer and T. H. Lee, “A 1.5V , 1.5GHz CMOS Low Noise Amplifier,”
IEEE Journal Solid-State Circuit , vol. 32, no.5, p.745, May 1997.
- [13] T. -K. Neuyen et al.,” CMOS low noise amplifier design optimization techniques,”
IEEE Trans. Microwave Theory Tech., vol.52, no.5, pp.1433-1442, May 2004.
- [14] S. S. Mohan et al.,” Bandwidth extension in CMOS with optimized on-chip
inductors,” *IEEE J. Solid-State Circuits*,vol.35 , no.3,pp.346-355,Mar.2000.
- [15] <http://www-users.cs.york.ac.uk/~fisher/lcfilter/>
- [16] http://hftc.ndl.org.tw/service/tplan/Layout_Rule_RFIC.pdf
- [17] P. W. Lee et.al.,”A SiGe Low Noise Amplifier for 2.4/5.2/5.7 GHz WLAN Applica
tions,” *ISSCC Dig. of Tech. Papers*, pp.224-225, Feb.2003.

