## 新型互補式金氧半類比至數位轉換器架構 之設計與分析

## The Design and Analysis of New Architectures for CMOS Analog-to-Digital Converters

研究生寒以義Student: Yu-Yee Liow指導教授: 吳重雨Advisor: Chung-Yu Wu

國立交通大學電子工程系電子研究所博士論文

A Dissertation
Submitted to

Institute of Electronics
College of Electrical Engineering and
Computer Science
National Chiao Tung University
In Partial Fulfillment of Requirements
for the Degree of
Doctor of Philosophy
in
Electronic Engineering

May 2004 Hsinchu, Taiwan, Republic of China

中華民國九十三年五月