# **Chapter 2**

## **Experimental Apparatus and Procedures for Pool Boiling**

## **2.1 Description of Experimental Apparatus**

In order to investigate the boiling phenomena and heat transfer performance of boiling enhanced surfaces including rectangular fin array surfaces and cavity array surfaces in horizontal and vertical orientations, an experimental facility with FC-72 as working fluid is designed and constructed. The schematic of the pool boiling experimental apparatus is depicted in Figure 2.1. The test rig consists five major parts: (1) test chamber, (2) heating module, (3) measurement and data acquisition system, (4) vacuum and degas system, and (5) environment control system which are described in the following.

## **2.1.1 Test Chamber**



As shown in Figure 2.2, the test chamber is used to simulate the pool boiling in saturated dielectric liquid FC-72 at atmospheric pressure inside a hermetic stainless steel pressure vessel, 304mm high and 154mm in diameter, with two quartz windows, each with a diameter of 100mm, to enable the flow patterns of the bubbles to be visualized. The internal water condenser is coiled in the upper section inside the vessel and connected to a thermostat to keep the vessel at atmospheric pressure. A pressure transducer with an operating range of 0 to  $4\times10^5$ Pa is also located at the gate of the vessel to measure the pool pressure. The liquid temperature is measured using two RTDs with a calibrated accuracy of 0.1K. An auxiliary cartridge heater is wrapped around the vessel to preheat the working liquid during the operation, and to protect against heat loss from the vessel to the ambient. All the flanges of vessel and quartz windows are sealed by Teflon O-ring to prevent leakage. Moreover, four flange type thermocouple/power-lead feedthroughs for vacuum system are installed on upside flange of vessel to prevent leakage from the thermocouples and power-lead wire. Meanwhile, before charge FC-72 into the test chamber, the leakage test is conducted by vacuumed the vessel and then charging high pressure nitrogen  $(3\times10^5$ Pa). After 48 hours waiting period, if the pressure variation is less than 10pa, the vessel is considered non-leakage and experiment can be carried out.

#### **2.1.2 Heating Module-Finned Surfaces**

The schematic details of the heating module are shown in Figure 2.3, which include a test surface, a dummy heater, a bakelite frame insulator and a Teflon substrate ( $k \approx 0.35 \text{Wm}^{-1} \text{K}^{-1}$ ). All finned surfaces used herein are manufactured from high-purity oxygen-free copper. By micro electrostatic discharge machining (EDM) process, the  $4\times4$ ,  $5\times5$  and  $7\times7$  fin array surfaces with different fin spacing of 2mm, 1mm and 0.5mm, as shown in Figure 2.4 are machined from the 10mm×10mm base area, with fins of width 1mm, and 0.5, 1, 2 and 4mm fin length respectively. The other finned surfaces are designed to be  $10\times10$  fin arrays with one fin spacing (0.5mm) and one fins width (0.5mm) and three fin lengths (0.5mm, 1.0mm and 2.0mm) on a 10mm×10mm base area. The base temperatures are measured using two T-Type thermocouples which are inserted and soldered in the two holes in the copper block at a depth of 1mm under the base surface. The microelectromechanical systems (MEMS) refer to the characteristic size less than 1 mm but larger than 1µm. Therefore, the finned surfaces fabricated herein can be divided to two sets: mini finned surfaces (fin width  $= 1$ mm) and micro finned surfaces (fin width  $= 0.5$ mm). The detailed geometric specifications of finned surfaces are list in Table 2.1 and 2.2. The dummy heater is fabricated by standard photolithography, sputtering, electro-less plating and immersion gold deposition process on a 10mm×10mm×0.09mm Aluminum-Nitride heat spreader ( $k \approx 170 \text{Wm}^{-1} \text{K}^{-1}$ ). The Titanium/Copper heating circuit (electrical resistance  $\approx 10 \Omega$ ) is sputtered on downward-facing surface of the heat spreader. On the upward-facing surface, Titanium/Copper thin film is also sputtered to be the adhesive layer. Then, the copper electro-less plating and immersion gold process are conducted on the Ti/Cu

layer for soldering with test surfaces. The bakelite frame insulator and the Teflon substrate are mounted with test surface to prevent side heat loss. Moreover, low thermal conductivity silicone adhesive is used to seal the crevice between test surface and bakelite to prevent boiling nucleation.

#### **2.1.3 Heating Module-Artificial Micro-Cavity Surfaces**

Micro-fabrication is a process of etching, patterning, and layering materials into microstructures. Bulk micromachining, which is used to fabricate some mechanical structures and create within the confines of a silicon wafer by selectively removing parts of the wafer material device, is a widely technology for making MEMS devices. Through the bulk micromachining, the etching process can be either isotropic, anisotropic or a combination of both. In isotropic etching, the etch rate is identical in all directions, while in anisotropic etching the etch rate depends on the crystallographic orientation of the wafer.

The micro cavities are produced by some simple processes as shown in Figure 2.5. The 3-D micro artificial cavities can be formed through two masks process. In order to avoid the distortion and bending of silicon because of thermal expansion effect, it needs the stage to provide the fixed contact and small gap. The microfabrication process, as illustrated in Figure 2.5(a) to (c), start with a dummy (100) double-side polished silicon wafer and the positive resist AZ6420 is used for the stage patterning. Then the Oxford ICP dry etching is used to make a vertical sidewall. In addition, the silicon nitride  $(Si<sub>3</sub>N<sub>4</sub>)$  is grown by LPCVD technique and used for etching barrier to resist Oxford ICP dry anisotropic etching. Finally, after removing the silicon nitride by RIE dry etching, the microcavities structure can be formed and the depth of artificial micro cavities can be easily controlled by the etching time of Oxford ICP dry anisotropic etching. Besides, the SEM pictures of test artificial micro-cavities surface are also presented in Figure 2.6.

#### **2.1.4 Data Acquisition**

The power generated from the dummy heater is provided by a DC power supply (0-160V, 0-5A). The heating power, simulating the heat generated by CPU, is supplied from the DC power supply.

All the signals, including those from thermocouples, RTDs and the pressure transducer are collected and converted by the internal calibration equation in a data acquisition device, which then transmitted the converted signals through a GPIB (IEEE488) interface to a personal computer and displayed them on screen for further operation.

A microscope with a high speed camera (Fuji S602) is installed in front of the quartz window to observe the mechanism of boiling on these test surfaces. The focus plane of the camera is adjusted on the first row fins or cavities. The diameter of the departure bubble is determined by compare with the side length of bakelite frame insulator instead of fin with or fin width because of thermal expansion of copper may results measurement deviation. The boiling images captured by the camera are stored and displayed using a PC with image-capturing software. The images are recorded and the data collected simultaneously during the experiment, to enable visual images to be compared with corresponding measurements.

#### **2.1.5 Degas System**

The working fluid used in present study, FC-72, is highly wetting dielectric fluid (perfluorocarbon). Moreover, FC-72 is apt to trap gas and dissolving gas be able obviously affect the thermal properties of dielectric fluid and caused the deviation of experimental data. Hence, prior to charge the FC-72 into the experimental vessel, the degassing process the movement, reduce the influence of dissolving gas to the experiment. Degas system mainly consist a stainless steel vessel with a heater, a condenser, a discharge valve and venting valve. The dissolving FC-72 is heated and boiled by the heater in the stainless steel vessel. The boiled movement causes dielectric fluid in fierce ebullition state, lies between dissolving gas which dielectric fluid vapor will release to condenser, the vapor flow through in the condenser, then will lie between dielectric fluid becomes the condensation the liquid backflow to the vessel, but will survive the dissolving gas to follow by the venting valve to discharged to the ambient. The degassing process carries on approximately 3 hours, until the pressure under saturation temperature error value in  $10^4$  Pa. As the degas process is completed, opens degas system with dielectric fluid between the circulatory system to pass the valve, charge the dielectric fluid the flow into the vacuum state experimental vessel.

## **2.1.6 Environment Control System**

During the experimental process, the test vessel is placed in a thermal state, 1.5m height, 1m width and depth, which used to control the internal ambient temperature inside the thermal state the same as the working fluid inside test vessel for preventing the heat loss.

#### **2.1.7 Working Fluid**

The liquids used in the immersed cooling of electronic component must meet several requirements which including chemically stable and inert, non-toxic, nonflammable, low dielectric constant, low boiling point and high dielectric strength. The perfluorocabons FC-72 made by 3M Inc. are fit for the requirement above. Hence, FC-72 is used to be the working fluid in present study. The thermal properties of FC-72 are shown in Table 2.3.

## **2.2 Experimental Procedures**

The working liquid FC-72 is filled into the test vessel before the test run. The liquid level is maintained at approximately 80 mm above the test surface to prevent any disturbance caused by the rise of vapor/bubbles to the level of liquid, which will influence the boiling test. The test vessel with the operating fluid is heated to the saturation temperature, using auxiliary cartridge heaters. As the saturation temperature is reached, the operating fluid is boiled vigorously for two hours to remove dissolved gases and any non-condensed gas before the test run.

After the liquid had been cooled for three hours, the data acquisition system, the environment control system and the auxiliary cartridge heater are used to control and preheat the liquid until the saturated state is obtained. The difference between the measured liquid temperature and the saturated temperature determined by the measured pressure is less than 0.1K, the steady state is reached and test run initiated.

The power input is increased in small steps during the test run. The voltages across the heater and the current values converted from the DC power supply are recorded by the data acquisition system, and the power input was calculated subsequently. For each value of the power input, the steady state is assumed to be reached when the difference between the temperatures measured by the two thermocouples embedded in the test surface is less than 0.2K in a waiting period of 120 seconds. In the steady state, the averaged temperature gradient between the heating surface and liquid and the power input are determined and recorded by the data acquisition system. Meanwhile, the flow pattern images are captured using a camera. In the high heat flux region near the point of CHF (where the heat flux is about 80~90% of CHF), the instantaneous surface temperature displayed on the screen is monitored and compared with the previous surface temperature for 60 seconds after each increment in power input. As the temperature difference between instantaneous surface temperature and previous surface temperature is larger than 20K, the CHF is assumed to have been reached and the power supply is shut off immediately.

After the power input had been shut off and the test surface cooled for 30 minutes, the power input is restarted and the heat flux is raised to 70~80% of CHF for a boiling period of 30 minutes, to activate the nucleation sites. Then, the heat flux is increased to a value close to CHF, to initiate the boiling experiment by decreasing heat flux, according to the steps described above with decreasing the power input.

The experiment process of effects of the test run number and non-boiling time on the test surface is keep the test surface and working fluid inside the vessel for 24 hours non-boiling time between two test run to maintain the consistency of experiment.

### **2.3 Temperature Calibration**

The temperature calibration of all thermocouples and resistance temperature detectors are take a HP2804A quartz thermometer as the reference datum. The probe of the quartz thermometer is located inside a hole drilled in the central of a copper cylinder block which placed in the water thermostat. The thermocouples and the resistance temperature detectors are also located in the holes dilled on the same copper block encircled the quartz thermometer probe. Vary the temperature of the thermostat from  $40~-90$  and measure 60 temperature value. Finally, the measurements of thermocouples, resistance temperature detectors and quartz thermometer are plotted to take correlations to modify the deviation of each thermocouple and resistance temperature detector.

## **2.4 Heat loss estimation and experimental uncertainty**

The insulators of the heating module, including the bakelite frame and the Teflon substrate, cannot completely prevent heat loss. Therefore, commercial computational fluid dynamic software (Flotherm) is used to perform the thermal simulation. Three detailed thermal models of the heating module are developed to examine the heat losses. All simulation models are setting as the heating module immersed in the FC-72, the ambient pressure and temperature is 1 atmospheric and 55.6 . The heat losses from bakelite frame and Teflon substrate in horizontal and vertical copper plain surfaces estimated by simulation are 15.5% to 9.3% and 13.6% to 9.0% respectively in the heat flux range  $0.2 \text{ Wcm}^{-2}$  to 1 Wcm<sup>-2</sup> in single phase natural convection condition. The simulation results indicate that heat losses of the bakelite frame and Teflon substrate cannot be neglected. Therefore, the heat loss will be subtracted in natural

convection region. However, as the boiling is initiated, the boiling heat transfer can provide two order magnitude of heat transfer coefficient than that of single phase and the heat loss in boiling region might be reduced in a neglecting range.

The uncertainties in the experimental results, including heat flux and measured temperature, are analyzed as proposed by Kline and McClintock [61]. Table 2.4 and 2.5 summarize the results of this uncertainty estimation.

#### **2.5 Data Reduction**

## **Plain Surfaces**

In the analysis of the boiling heat transfer performance, the overall heat transfer coefficient of the plain surface is defined as:

. A Addition

$$
h_p = q_p'' / \Delta T \tag{2-1}
$$

where  $q_p^{\prime\prime}$  is the heat flux through the heating surface and  $\Delta T$  represents the wall superheat, which can be determined by experimental measurement. The heat transfer rate is determined from output current (*I*) of the DC power supply and measured voltage (*V*) across the dummy heater.

$$
\dot{Q}_p = I \cdot V \tag{2-2}
$$

$$
q_p'' = \dot{Q}_p / A_p \tag{2-3}
$$

where  $A_p$  is the base area of the plain surface.

The heat transfer inside the base of plain surface is determined as one-dimensional heat conduction:

$$
T_b = T_m - \dot{Q}_p \cdot \Delta x / k \cdot A_p \tag{2-4}
$$

where  $T_m$  is the average measured temperature of two thermocouples;  $\Delta x$  is the distance between the base surface and the thermocouple, and  $k$  is the thermal conductivity of the test surface.

The wall superheat  $(\Delta T)$  is defined as the difference between the temperature of the base surface  $(T_b)$  and the saturated temperature of FC-72  $(T_{sat})$ .

$$
\Delta T = T_b - T_{sat} \tag{2-5}
$$

## **Finned Surfaces**

To analyze the boiling heat transfer effectiveness of the fined surface, the overall heat transfer coefficient,  $h_t$ , is defined as equal to:

$$
h_t = q_t'' / \Delta T \tag{2-6}
$$

where  $q''_t$  is the heat flux through the total area of the finned surface and  $\Delta T$  represents the wall superheat. The heat transfer rate  $\dot{Q}_f$  can be calculated by the output current (*I*) of the DC power supply and measured voltage  $(V)$  across the dummy heater. The heat flux can be based on either the base surface area  $A<sub>b</sub>$ , or the overall surface area ( $A<sub>t</sub>$ , the summation of the base area and the fin area).

$$
\dot{Q}_f = I \cdot V \tag{2-7}
$$

$$
q_t'' = \dot{Q}_f / A_t \tag{2-8}
$$

$$
q_b'' = \dot{Q}_f / A_b \tag{2-9}
$$

$$
A_t = A_b + n \cdot (4W \cdot L) \tag{2-10}
$$

where W is the width and L is the length of the fin and  $n$  represents the numbers of fins.

The heat transfer inside the base of finned surface is also determined as one-dimensional heat conduction:

$$
T_b = T_m - \dot{Q}_f \cdot \Delta x / k \cdot A_b \tag{2-11}
$$

where  $T_m$  is the average measured temperature of two thermocouples;  $\Delta x$  is the distance between the base surface and the thermocouple, and  $k$  is the thermal conductivity of the finned surface.

The wall superheat  $(\Delta T)$  is defined as the difference between the temperature of the base surface  $(T_b)$  and the saturated temperature of FC-72  $(T_{sat})$ .  $\Delta T = T_b - T_{sat}$  (2-12)

#### **Micro-Cavity Surfaces**

The overall heat transfer coefficient of the micro-cavity surface is defined as:

$$
h_t = q_t'' / \Delta T \tag{2-13}
$$

where  $q''_t$  is the heat flux through the total area of the micro-cavity surface and  $\Delta T$ represents the wall superheat. The heat flux can be based on either the base surface area  $A_b$ , or the total surface area  $(A_t)$ , the summation of the base area and the cavity area).

$$
\dot{Q}_c = I \cdot V \tag{2-13}
$$

$$
q_t'' = \dot{Q}_c / A_t
$$
 (2-14)

$$
q''_b = Q_c / A_b
$$
 (2-15)

$$
A_t = A_b + n \cdot (\pi d_c H_c + \frac{\pi d_c^2}{4})
$$
 (2-16)

where  $d_c$  and  $H_c$  is the diameter and depth of the cavity and  $n$  represents the numbers of cavities..

The heat transfer inside the base of micro-cavity surface is determined as one-dimensional heat conduction:

$$
T_b = T_m - \dot{Q}_c \cdot \Delta x / k \cdot A_b \tag{2-17}
$$

where  $T_m$  is the average measured temperature of two thermocouples;  $\Delta x$  is the distance between the base surface and the thermocouple, and  $k$  is the thermal conductivity of the test surface.

The wall superheat  $(\Delta T)$  is also defined as the difference between the temperature of the base surface  $(T_b)$  and the saturated temperature of FC-72  $(T_{sat})$ .

$$
\Delta T = T_b - T_{sat} \tag{2-18}
$$

# **Table 2.1 Specifications of mini-finned surfaces (W=1.0mm)**

# **(See Figure 2.4)**



**Table 2.2 Specifications of micro-finned surfaces (W=0.5mm)**

**(See Figure 2.4)** 



Properties	FC-72
Molecular weight	338 kg kgmole $^{-1}$
Critical temperature, $T_{crit}$	178
Saturation temperature, $T_{\text{sat}}$	56.6
Density of liquid, $\rho_1$	1600 kg m <sup>-3</sup>
Density of vapor, $\rho$ <sub>v</sub>	13.39 kg m <sup>-3</sup>
Heat of vaporization, $i_{i}$	94790 J $kg^{-1}$
Thermal conductivity of liquid, $k_i$	$0.0538$ W m <sup>-1</sup> K <sup>-1</sup>
Specific heat of liquid, $C_{nl}$	1102 J $kg^{-1}K^{-1}$
Kinematic viscosity of liquid, $v_i$	$2.729\times10^{-7}$ m <sup>2</sup> s <sup>-1</sup>
Prandtl number of liquid, $Pr_l$	8.900
Coefficient of thermal expansion of liquid, $\beta$ ,	$-0.001639 K^{-1}$
Surface tension of liquid, $\sigma_i$	$0.008348$ Nm <sup>-1</sup>
Dielectric Strength, KV(2.54mm gap)	42

**Table 2.3 Thermal properties of FC-72 at 1 atmosphere [62].** 

<b>Parameter</b>	Uncertainty
<b>Test Surface Geometry</b>	
Width of plain surface $(\%)$	$\pm 0.05$
Area of plain surface (%)	$\pm 0.07$
Fin length, width, and spacing (%)	$\pm 1.0$
Area of finned surface (%)	±1.4
<b>Parameter Measurement</b>	
Temperature, $T(K)$	$\pm 0.2$
Temperature difference, $\Delta T$ (K)	± 0.4
System pressure, $P$ (kPa)	± 0.5
Boiling heat transfer on plain surface	
Power input, $\dot{Q}$ (%)	± 5.0
Heat flux, $q''(\%)$ 1896	± 5.0
Heat transfer coefficient, $h(\%)$ <b>THEFT</b>	±11.1
Boiling heat transfer on finned surface	
Power input, $\dot{Q}$ (%)	± 5.0
Heat flux, $q''(\%)$	± 5.19
Heat transfer coefficient, $h$ (%)	±11.26

**Table 2.4 Uncertainty analysis of finned surfaces.** 

<b>Parameter</b>	Uncertainty
<b>Test Surface Geometry</b>	
Width of plain surface $(\%)$	$\pm 0.5$
Area of plain surface (%)	$\pm 1.0$
Cavity diameter and depth (%)	± 5.0
Area of cavity surface (%)	±7.0
<b>Parameter Measurement</b>	
Temperature, $T(K)$	$\pm 0.2$
Temperature difference, $\Delta T$ (K)	$\pm 0.4$
System pressure, $P$ (kPa)	$\pm 0.5$
Boiling heat transfer on plain surface	
Power input, $Q(\%)$	± 5.0
Heat flux, $q''(\%)$ 1896	± 5.1
Heat transfer coefficient, $h$ (%)	$\pm 11.2$
Boiling heat transfer on cavity surface	
Power input, $\dot{Q}$ (%)	± 5.0
Heat flux, $q''(\%)$	±8.6
Heat transfer coefficient, $h$ (%)	±13.1

**Table 2.5 Uncertainty analysis of micro-cavity surfaces.**



Figure 2.1 Schematic of pool boiling apparatus.



Figure 2.2 Schematic of simulative environment module.



Figure 2.3 Schematic of saturated pool boiling heating module (unit:mm).



Figure 2.4 Schematic of finned surfaces (unit: mm).



Figure 2.5 Test artificial micro-cavity surface fabrication process flow.

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 $\exists$  Si<sub>3</sub>N<sub>4</sub>

 $\sqrt{1}$ 







Figure 2.6 SEM pictures of a test artificial micro-cavity surface (25×25 array, cavity diameter 200µm, depth110µm).