

# Tailoring of amorphous $\text{SiO}_x$ nanowires grown by rapid thermal annealing

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## Abstract

The growth of Pt-catalyzed  $\text{SiO}_x$  nanowires by rapid thermal annealing at 900 °C is demonstrated in the study. The growth of the nanowire is found to occur via a catalyst driven VLS mechanism. The seed particle composed of Pt–Si alloy is observed from the reaction between  $\text{SiO}_2$  and the catalytic Pt film. When the annealing time exceeds 60 s, the  $\text{SiO}_x$  nanowires first agglomerate, and then collapse to form dendritic islands on the surface. The dendritic islands may result from the reaction between Pt–Si seed particle and  $\text{SiO}_x$  nanowires, and are identified to be the Pt–Si compound.

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## 1. Introduction

One-dimensional nanostructures have received increasing attention due to their unique electronic, optical, and magnetic properties as a result of their large surface-to-volume ratio and quantum confinement effect. Numerous studies of the growth of metal-catalyzed nanowires have been investigated by different groups using various growth techniques. The vapor–liquid–solid (VLS) mechanism first proposed by Wagner has been suggested as the mechanism for nanowire growth [1]. Recently, the characteristics of the VLS mechanism has been revisited by Cheyssac et al. [2]. In addition, Persson et al. also proposed the vapor–solid–solid (VSS) mechanism, where the Au seed particle does not exceed the melting temperature during the nanowire growth [3]. Furthermore, nanowires can also be grown without a metal seed, for example in the presence of an oxide or by masking a substrate [4,5].

Novel fabrication technology of nanowires is versatile and includes a wide range of vapor, liquid and solid state

processing routes. Elechiguerra et al. deposited Pd/Au films as a catalyst to synthesize  $\text{SiO}_x$  nanowires by thermal heating at 1100 °C [6]. Zhang et al. demonstrated that  $\text{SiO}_x$  nanowires could be grown directly from a  $\text{SiO}_2$  substrate by annealing at 1100 °C in an Ar/methane flow [7]. Lee et al. reported the fabrication of silica nanowires by solid state diffusion of silicon from the silica films [8]. The growth of  $\text{SiO}_x$  nanowires via VLS mechanism or solid state diffusion is usually conducted at high temperature (1050–1100 °C). To our knowledge, rapid thermal annealing (RTA) has rarely been used for the nanowire growth. In this Letter, we demonstrate the growth of Pt-catalyzed  $\text{SiO}_x$  nanowires by a simple procedure, i.e., rapid thermal annealing. The effect of growth parameters on the evolution of  $\text{SiO}_x$  nanowires, as well as its characterization, will be addressed.

## 2. Experimental

$\text{SiO}_2$  films with a thickness of 11 nm were grown on p-type silicon (100) substrates by thermal oxidation. A 50-nm-thick Pt layer was then deposited on  $\text{SiO}_2$  by thermal evaporation. The Pt/ $\text{SiO}_2$ /Si structure was subjected to

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RTA in nitrogen ambient at 700, 800, and 900 °C for 60, 180, and 300 s, respectively. The surface morphology was examined by field emission scanning electron microscopy (FESEM) (S-4000, Hitachi). The crystallinity of the nanostructure was analyzed by glancing incident angle X-ray diffraction (GIAXRD) (D/MAX2500, Rigaku, using  $\text{Cu K}\alpha$ ,  $\lambda \sim 0.154$  nm) with a fixed incident angle of 2°. The transmission electron microscopy (TEM) experiments were carried out on a JEM-2000FX (JEOL Ltd.) operated at 200 kV. The TEM was equipped with a Gatan image filter (GIF, Model 2000, Gatan) which provides the fingerprint of chemical bonding states with high lateral/energy resolution (i.e. electron energy loss spectroscopy (EELS) spectra), recorded with an energy resolution of 1.2 eV (full width at half maximum, FWHM, of zero-loss peak) and an energy dispersion of 0.3 eV per channel. Electron energy-loss spectroscopy when performed in a nanoanalytical electron microscope provides one route to this information via analysis of the energy-loss near-edge structure (ELNES) present on the ionization edges. The ionization edge onsets and EELS near-edge fine structures (ELNES) are sensitive to the local coordination and electronic structure.

### 3. Results and discussion

Fig. 1a and b shows the morphology of the Pt/SiO<sub>2</sub>/Si structure after RTA at 800 °C and 900 °C, respectively. It is noted that the formation of Pt nanoparticles occurs at 800 °C, while that of nanowires appears at 900 °C. The formation of Pt nanoparticles is also observed for samples after RTA at 700 °C. Consequently, the critical temperature for the growth of nanowires is above 900 °C. The growth temperature of the nanowire is above the eutectic melting point (847 °C) of Pt–Si alloy [9]. It is reported that annealing of SiO<sub>2</sub>/Si film stacks will lead to the formation of gaseous SiO at temperatures above approximately 800 °C [10]. The 11-nm-thick SiO<sub>2</sub> thin film may act as the source for the SiO<sub>x</sub> nanowire growth. On the other hand, the nonequilibrium nature of heat transfer in the nanoparticles/film system should be taken into account [11]. The nanoparticles may have higher temperature rise than the film or substrate. As a result, it is evident that the growth of the nanowire is driven by the VLS mechanism. Moreover, the length of the nanowire is around 500 nm. The diameter of the nanowires ranges from 50 nm to 110 nm, which are probably connected with the size of the Pt catalyst [12,13].

Fig. 2a shows the TEM micrograph of the nanowire grown after RTA at 900 °C. The seed particles, for the most part, are attached to the top of the grown nanowires. However, a few of them are embedded in the wire during growth [10]. When the catalyst particle is on the wire tip, the VLS mechanism seems to be responsible. However, the VLS mechanism may not account for the catalyst, which remains in between the wire top and wire bottom. As a result, the growth behavior of the nanowire in this study still needs further investigation. Energy-dispersive

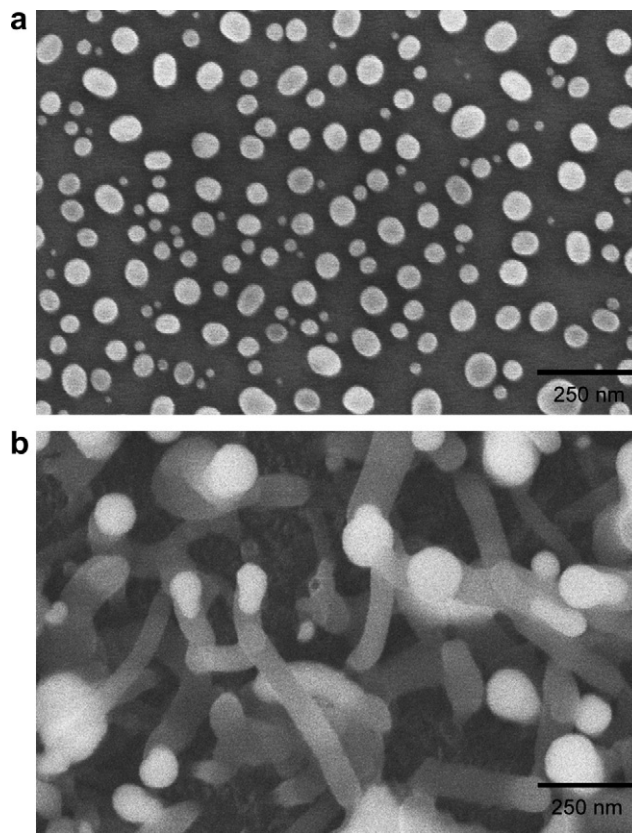


Fig. 1. Scanning electron microscopy images of (a) Pt nanoparticles on the surface after rapid thermal annealing at 800 °C for 60 s. (b) Pt assisted SiO<sub>x</sub> nanowires grown after rapid thermal annealing at 900 °C for 60 s.

X-ray (EDS) analysis shows that the wire is mainly composed of Si and O, whereas the seed particle is constituted by Pt and Si, as shown in Fig. 2b. The high-resolution TEM micrograph also indicates that the SiO<sub>x</sub> nanowire is amorphous (not shown). Analysis of the energy-loss near-edge structure (ELNES) present on the ionization edges can provide information on the local chemistry, structure and bonding. Fig. 2c shows the spatially resolved EEL spectra, indicating the Si L<sub>23</sub> ELNES of the nanowire and Si substrate, respectively. The Si L<sub>23</sub> ELNES of the nanowire shows a higher energy than that of the Si substrate. Schulmeister and Mader reported that the Si L<sub>23</sub> ELNES of amorphous silicon monoxide appears to be a superposition of the spectra of elemental silicon and of SiO<sub>2</sub> [14]. The onset of the near-edge structure of the nanowire is located at a lower energy than that of SiO<sub>2</sub>. The shoulder peak near 106 eV is corresponding to the SiO<sub>x</sub>, as compared to the reported literatures [14]. As a consequence, it can be concluded that the composition of the nanowire is silicon suboxide (SiO<sub>x</sub>).

Next, the effect of annealing time on the growth of SiO<sub>x</sub> nanowires is discussed. At the first stage of RTA, the Pt layer starts to agglomerate and forms nanoparticles on the SiO<sub>2</sub> surface. Afterwards, the Pt nanoparticles act as a catalyst and are responsible for initiating the growth

of  $\text{SiO}_x$  nanowires. The  $\text{SiO}_x$  nanowires are distributed individually after RTA at  $900^\circ\text{C}$  for 60 s, as shown in Fig. 1b. However, it is seen in Fig. 3a that the sample after annealing at  $900^\circ\text{C}$  for 180 s results in partial agglomeration of nanowires. Furthermore, annealing of the sample for 300 s leads to extinction of wires and forms dendritic islands on the surface, as shown in Fig. 3b. This

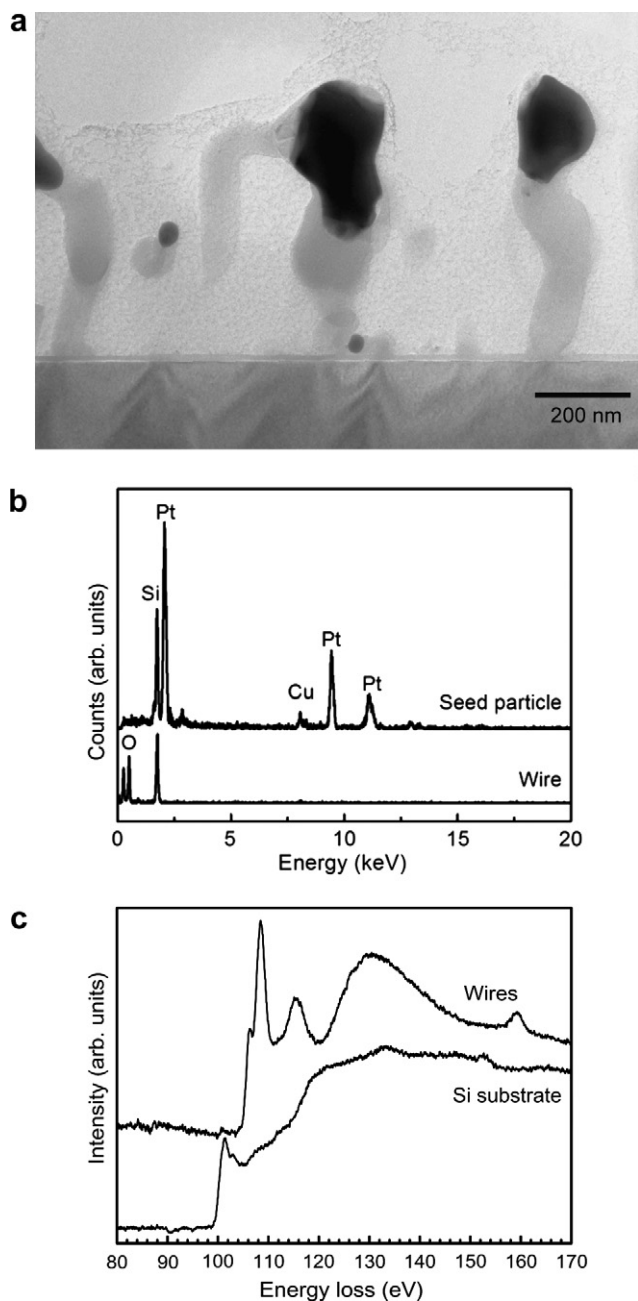


Fig. 2. (a) High resolution transmission electron microscopy images of the nanowire showing the seed particle (dark) and the wire (light gray). (b) The corresponding EDS spectrum of the nanowire, which reveals that the seed particles are mainly composed of Pt and Si, whereas the wires are mainly composed of Si and O. The Cu signal is contributed from the Cu grid. (c) Spatially resolved EEL spectra indicating the Si  $L_{23}$  ELNES of the nanowire and Si substrate.

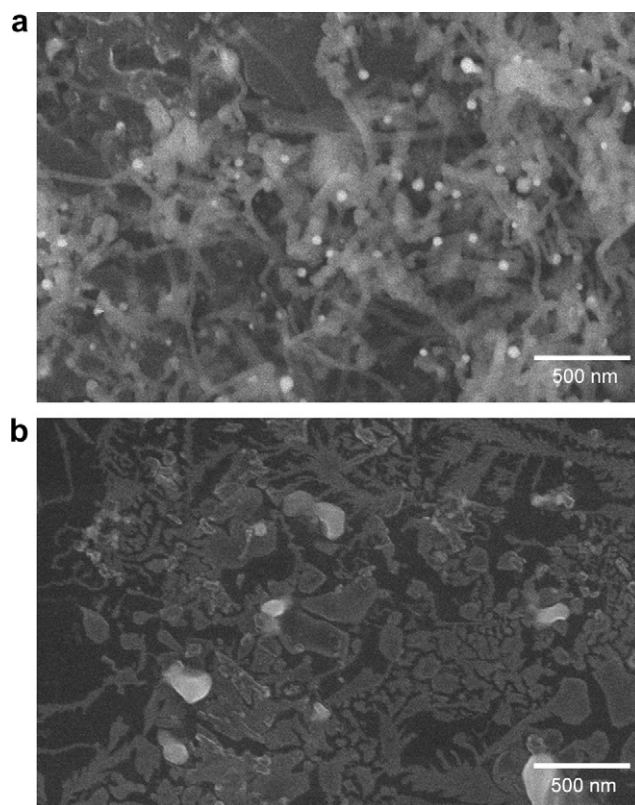


Fig. 3. Scanning electron microscopy images of Pt/SiO<sub>2</sub>/Si structures after rapid thermal annealing at  $900^\circ\text{C}$  for (a) 180 s, and (b) 300 s.

result indicates that the optimum heating time for the growth of  $\text{SiO}_x$  nanowires is not longer than 60 s.

Fig. 4a and b shows glancing incident angle X-ray diffraction (GIAXRD) patterns of Pt/SiO<sub>2</sub>/Si structures after  $900^\circ\text{C}$  annealing for 60 s and 180 s, respectively. The diffraction peaks in Fig. 4a can be identified as a face-centered-cubic Pt alloy (JCPDS file 04-0802) [15]. From EDS and GIAXRD results, it is revealed that the seed particle in Fig. 2a shall be a solid solution constituted by Pt and Si. As the annealing time increases, the Pt and Si form an orthogonal Pt–Si compound (JCPDS file 07-0251), as shown in Fig. 4b. The dendritic islands in Fig. 3b are identified to be the Pt–Si phase and they may originate from the reaction of Pt with the  $\text{SiO}_x$  nanowire.

#### 4. Conclusions

In conclusion,  $\text{SiO}_x$  nanowires have been successfully prepared by rapid thermal annealing with the use of Pt as catalysts, which initiate and guide the growth. The  $\text{SiO}_x$  nanowire can be grown at temperatures of  $900^\circ\text{C}$ . Well distributed nanowires have been found after RTA for 60 s. Further annealing on the sample will lead to the deterioration of the nanowire structure. Considering the experimental results including the presence of catalyst

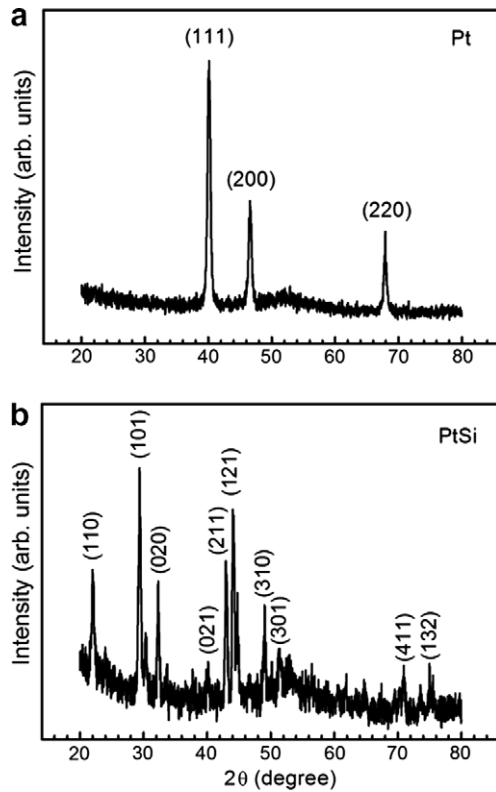


Fig. 4. Glancing incident angle X-ray diffraction (GIAXRD) patterns of Pt/SiO<sub>2</sub>/Si structures after rapid thermal annealing at 900 °C for (a) 60 s, and (b) 180 s.

on the nanowire tip, the VLS mechanism seems to be responsible for the SiO<sub>x</sub> nanowire growth via RTA.

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